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## **Thick Film Resistor Array**



CRA06E and CRA06S Thick Film resistor arrays are constructed on a high grade ceramic body with convex terminations. A small package enables the design of high density circuits. The single component reduces board space, component counts and assembly costs.

### **FEATURES**

- Convex terminal array available with either scalloped corners (E version) or square corners (S version)
- Wide ohmic range: 10R to 1M0
- · 4 or 8 terminal package with isolated resistors
- Lead (Pb)-free solder contacts on Ni barrier layer
- Pure tin plating provides compatibility with Lead (Pb)-free and lead containing soldering processes
- Compatible with "Restriction of the use of Hazardous Substances" (RoHS) directive 2002/95/EC (issue 2004)

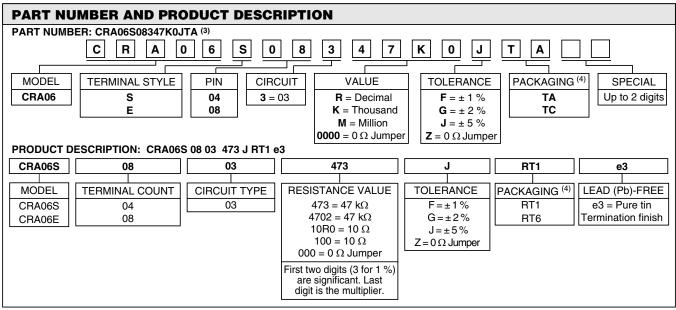
STANDARD ELECTRICAL SPECIFICATIONS								
MODEL	CIRCUIT	POWER RATING P <sub>70 °C</sub> W	LIMITING ELEMENT VOLTAGE MAX. V≅	TEMPERATURE COEFFICIENT ppm/K	TOLERANCE %	RESISTANCE RANGE Ω	E-SERIES	
CRA06E CRA06S	03	0.063	50	± 100 ± 200	± 1 ± 2; ± 5	10R - 1M0	24 + 96 24	
011/1000		Zero-Ohm-Resistor available; $R_{\text{max.}} = 50 \text{ m}\Omega$ , $I_{\text{max.}} = 1 \text{ A}$						

TECHNICAL SPECIFICATIONS					
PARAMETER	UNIT	CRA06E & S			
Rated Dissipation at 70 °C <sup>(2)</sup>	W per element	0.063			
Limiting Element Voltage (1)	V≅	50			
Insulation Voltage (1 min)	V <sub>dc/ac peak</sub>	100			
Category Temperature Range	°C	- 55 to + 155			
Insulation Resistance	Ω	> 10 <sup>9</sup>			

Notes

<sup>(1)</sup> Rated voltage:  $\sqrt{P \times R}$ 

<sup>(2)</sup> The power dissipation on the resistor generates a temperature rise against the local ambient, depending on the heat flow support of the printed-circuit board (thermal resistance). The rates dissipation applies only if the permitted film temperature of 155 °C is not exceed.



#### Notes

<sup>(3)</sup> Preferred way for ordering products is by use of the PART NUMBER

<sup>(4)</sup> Please refer to table PACKAGING, see next page

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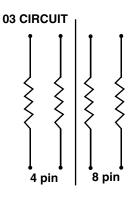
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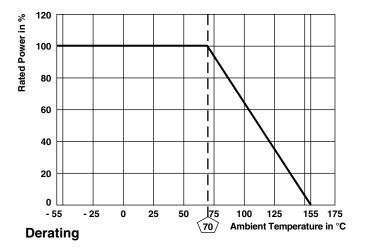
AVAILABLE TYPES AND RANGES					
MODEL	TERMINAL COUNT CIRCUIT		TEMPERATURE COEFFICIENT	TOLERANCE	
	04	03	± 100 ppm/K	±1%	
CRA06S	04		± 200 ppm/K	± 5 %; ± 2 %	
CHAU05	08		± 100 ppm/K	±1%	
	00	03	± 200 ppm/K	± 5 %; ± 2 %	
	08	03	± 100 ppm/K	±1%	
CRA06E	08	03	± 200 ppm/K	± 5 %; ± 2 %	

# PACKAGING

		DIAMETER	РІТСН	PIECES/REEL	PACKAGING CODE		
MODEL	TAPE WIDTH				PAPER TAPE		
					PART NUMBER	PRODUCT DESCRIPTION	
CRA06	8 mm	180 mm/7"	4 mm	5000	TA	RT1	
		330 mm/13"	4 mm	20 000	TC	RT6	

### CIRCUIT





E-Version

103

SOLDER PAD DIMENSIONS [in millimeters]

d

0.36

0.36

р

0.8

а

0.44

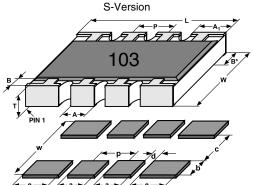
0.44

w

3.1

3.1

### DIMENSIONS



MODEL	PIN	DIMENSIONS [in millimeters]							
	NO#	L	Α	<b>A</b> <sub>1</sub>	В	B*	Р	Т	W
CRA06S	4	1.6	0.38	0.61	0.3	0.3	0.8	0.5	1.5
CRA06E	8	3.2	0.38	-	0.3	0.3	0.8	0.5	1.5
CRA06S	8	3.2	0.38	0.61	0.3	0.3	0.8	0.5	1.5
	TOL.	± 0.15	± 0.15	± 0.15	± 0.15	± 0.15	± 0.1	± 0.1	± 0.15

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MODEL

CRA06S

CRA06E

CRA06S

PINS

4

8

С

0.8

0.8

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b

1.15

1.15

0.63

е

# CRA06E, CRA06S

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## Thick Film Resistor Array



TEST PROCEDURES AND REQUIREMENTS EN 60115-1						
(clause)	CONDITIONS OF TEST	STABILITY CLASS 1 OR BETTER	STABILITY CLASS 2 OR BETTER			
	Stability for product types:		10 Ω to 1 MΩ			
	CRA06E/CRA06S	10 Ω to 1 MΩ				
Resistance (4.5)	-	± 1 %	± 2 %; ± 5 %			
Temperature coefficient (4.8.4.2)	20/- 55/20 °C and 20/125/20 °C	± 100 ppm/K	± 200 ppm/K			
Overload (4.13)	$U = 2.5 \times (P_{70} \times R)^{1/2}$ \$\le 2 \times U_{max.}; 0.5 \text{ s}	± (0.25 % <i>R</i> + 0.05 Ω)	$\pm$ (0.5 % R + 0.05 Ω)			
Solderability (4.17.5) <sup>(2)</sup>	Aging 4 h at 155 °C, dryheat solder bath method; 235 °C; 2 s visual examination	lder bath method; 235 °C; 2 s				
Resistance to soldering heat (4.18.2)	Solder bath method; (260 ± 5) °C; (10 ± 1) s	± (0.25 % <i>R</i> + 0.05 Ω)	± (0.5 % <i>R</i> + 0.05 Ω)			
Rapid change of temperature (4.19)	30 min at LCT = - 55 °C; 30 min at UCT = 125 °C; 5 cycles	± (0.25 % <i>R</i> + 0.05 Ω)	± (0.5 % <i>R</i> + 0.05 Ω)			
Damp heat, steady state (4.24)	(40 ± 2) °C; 56 days; (93 ± 3) % RH	± (1 % <i>R</i> + 0.05 Ω)	± (2 % <i>R</i> + 0.1 Ω)			
Climatic sequence (4.23)	16 h at UCT = 125 °C; 1 cycle at 55 °C; 2 h at LCT = -55 °C; 1 h/1 kPa at 15 °C to 35 °C; 5 cycles at 55 °C $U = (P_{70} \times R)^{1/2}$ $U = U_{max.}$ ; whichever is less severe	± (1 % <i>R</i> + 0.05 Ω)	± (2 % <i>R</i> + 0.1 Ω)			
Endurance at 70 °C (4.25.1)	$U = (P_{70} \ge R)^{1/2}$ $U = U_{max.}$ ; whichever is less severe 1.5 h ON; 0.5 h OFF; 70 °C; 1000 h	± (1 % <i>R</i> + 0.05 Ω)	± (2 % <i>R</i> + 0.1 Ω)			
Extended endurance (4.25.1.8)	Duration extended to 8000 h	± (2 % <i>R</i> + 0.1 Ω)	± (4 % <i>R</i> + 0.1 Ω)			
Endurance at upper category temperature (4.25.3)	UCT = 125 °C; 1000 h	± (1 % <i>R</i> + 0.05 Ω)	± (2 % <i>R</i> + 0.1 Ω)			

### Notes

<sup>(1)</sup> Figures are given for a single element

<sup>(2)</sup> Solderability is specified for 2 years after production or requalification. Permitted storage time is 20 years

### APPLICABLE SPECIFICATIONS

• EN 60115-1	Generic Specification
• EN 140400	Sectional Specification
• EN 140401-802	Detail Specification
• IEC 60068-2-X	Variety of environmental test procedures
• EIA 481	Packaging of SMD components

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