



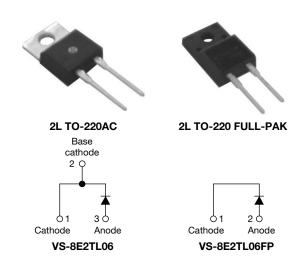
Vishay Semiconductors

RoHS

COMPLIANT

HALOGEN FREE

Ultralow V_F Ultrafast Rectifier, 8 A FRED Pt®



PRODUCT SUMMARY					
Package	2L TO-220AC, 2L TO-220 FP				
I _{F(AV)}	8 A				
V_{R}	600 V				
V _F at I _F	1.07 V				
t _{rr} (typ.)	60 ns				
T _J max.	175 °C				
Diode variation	Single die				

FEATURES

- \bullet Ultrafast recovery time, extremely low V_{F} and soft recovery
- 175 °C maximum operating junction temperature
- For PFC DCM operation
- True 2 pin package
- Low leakage current
- Fully isolated package (V_{INS} = 2500 V_{RMS})
- Compliant to RoHS directive 2002/95/EC
- Halogen-free according to IEC 61249-2-21 definition
- Designed and qualified for industrial level

DESCRIPTION

State of the art, ultralow V_F , soft-switching ultrafast rectifiers optimized for Discontinuous (Critical) Mode (DCM) Power Factor Correction (PFC).

The minimized conduction loss, optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

The device is also intended for use as a freewheeling diode in power supplies and other power switching applications.

APPLICATIONS

AC/DC SMPS 70 W to 400 W

e.g. laptop and printer AC adaptors, desktop PCs, TVs and monitors, games units and DVD ac-to-dc power supplies.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Peak repetitive reverse voltage	V_{RRM}		600	V	
Average rectified forward current FULL-PAK	I _{F(AV)}	T _C = 156 °C	- 8	٨	
		T _C = 131 °C			
Non-repetitive peak surge current	I _{FSM}	T _J = 25 °C	125	A	
Peak repetitive forward current	I _{FM}		16		
Operating junction and storage temperatures	T _J , T _{Stg}		- 65 to 175	°C	

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	600	-	-	
Forward voltage V _F	V	I _F = 8 A	-	1	1.07	V
	VF	I _F = 8 A, T _J = 150 °C	-	0.85	0.90	
Reverse leakage current I _R		$V_R = V_R$ rated	-	0.04	4	
		$T_J = 150 ^{\circ}\text{C}$, $V_R = V_R$ rated	-	10	70	μA
Junction capacitance	C _T	V _R = 600 V	-	6	-	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8	-	nH

Document Number: 93168 Revision: 19-Aug-10 For technical questions within your region, please contact one of the following: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com

Vishay Semiconductors

Ultralow V_F Ultrafast Rectifier, 8 A FRED Pt®



Document Number: 93168

Revision: 19-Aug-10

DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS	
		$I_F = 1.0 \text{ A}, dI_F/dt =$	$I_F = 1.0 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		60	100	
Reverse recovery time		$I_F = 8.0 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	150	250	
neverse recovery time	t _{rr}	T _J = 25 °C		-	200	-	ns - A - μC
		T _J = 125 °C	I _F = 8 A	-	255	-	
Dools was a summant	,	T _J = 25 °C		-	15	-	
Peak recovery current	I _{RRM}	T _J = 125 °C	dl _F /dt = 200 A/μs V _R = 390 V	-	20	-	
Reverse recovery charge	0	T _J = 25 °C		-	1.5	-	
	Q _{rr}	T _J = 125 °C		-	2.4	-	

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C	
Thermal resistance,	D		-	2	2.4		
junction to case FULL-PAK	R_{thJC}		-	5	5.5		
Thermal resistance, junction to ambient per leg	R _{thJA}	Typical socket mount	-	-	70	°C/W	
Typical thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-		
Maiabt			-	2	-	g	
Weight			-	0.07	-	OZ.	
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)	
Marking davise		Case style TO-220	8E2TL06				
Marking device		Case style TO-220 FULL-PAK		8E2TL06FP			





Ultralow V_F Ultrafast Rectifier, 8 A FRED Pt[®] Vishay Semiconductors

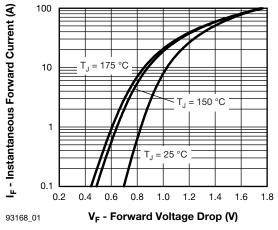


Fig. 1 - Typical Forward Voltage Drop Characteristics

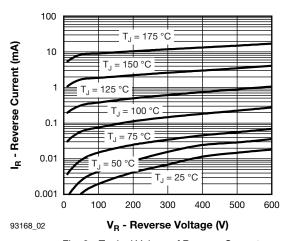


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

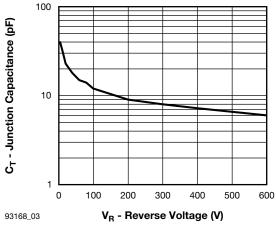


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

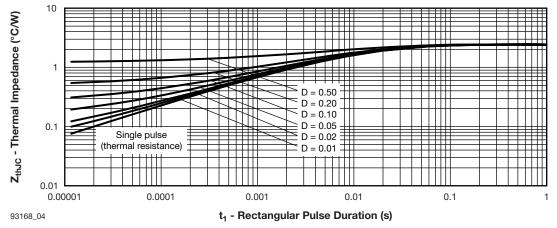


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (TO-220)

Vishay Semiconductors

Ultralow V_F Ultrafast Rectifier, 8 A FRED Pt[®]



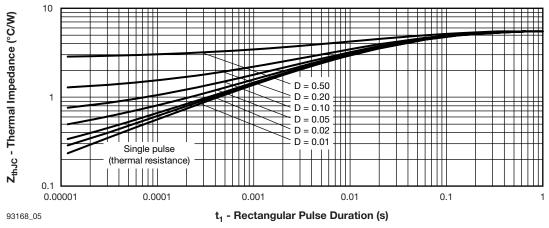


Fig. 5 - Maximum Thermal Impedance Z_{thJC} Characteristics (FULL-PAK)

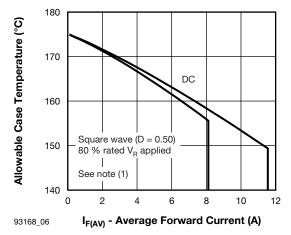


Fig. 6 - Maximum Allowable Case Temperature vs. Average Forward Current (TO-220)

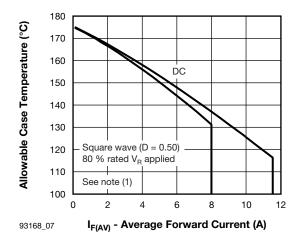


Fig. 7 - Maximum Allowable Case Temperature vs. Average Forward Current (FULL-PAK)

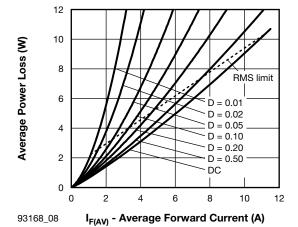


Fig. 8 - Forward Power Loss Characteristics

Note

 $^{(1)} \ \ Formula \ used: T_C = T_J - (Pd + Pd_{REV}) \ x \ R_{thJC}; Pd = Forward \ power \ loss = I_{F(AV)} \ x \ V_{FM} \ at \ (I_{F(AV)}/D) \ (see \ fig. \ 6); \\ Pd_{REV} = Inverse \ power \ loss = V_{R1} \ x \ I_R \ (1 - D); \ I_R \ at \ V_{R1} = Rated \ V_R$



Ultralow V_F Ultrafast Rectifier, 8 A FRED Pt[®] Vishay Semiconductors

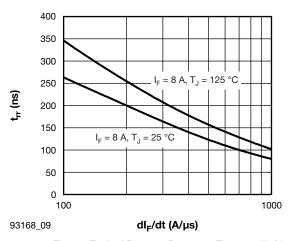


Fig. 9 - Typical Reverse Recovery Time vs. dI_F/dt

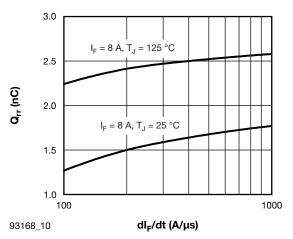


Fig. 10 - Typical Stored Charge vs. dl_F/dt

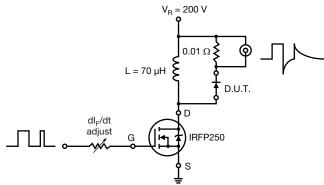
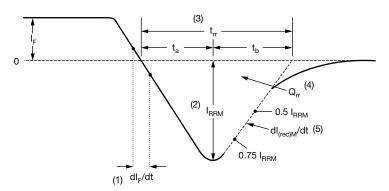


Fig. 11 - Reverse Recovery Parameter Test Circuit



- (1) dl_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_F$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) $\mathbf{Q}_{\rm rr}$ area under curve defined by $\mathbf{t}_{\rm rr}$ and $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 12 - Reverse Recovery Waveform and Definitions

Vishay Semiconductors

Ultralow V_F Ultrafast Rectifier, 8 A FRED Pt[®]



ORDERING INFORMATION TABLE

Device code

VS-	8	E	2	Т	L	06	FP	-E
1	2	3	4	5	6	7	8	9

1 - Vishay Semiconductors product suffix

2 - Current rating (8 = 8 A)

Circuit configuration:

E = Single diode

4 - 2 = True 2 pin package

5 - T = TO-220

6 - L = Ultrafast recovery time

Voltage code (06 = 600 V)

- • None = TO-220

• FP = FULL-PAK

9 - Environmental digit:

• -E = RoHS compliant and terminations lead (Pb)-free

• -M = Halogen-free, RoHS compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)						
PREFERRED P/N	QUANTITY PER TUBE	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION			
VS-8E2TL06-E	50	1000	Antistatic plastic tubes			
VS-8E2TL06-M	50	1000	Antistatic plastic tubes			
VS-8E2TL06FP-E	50	1000	Antistatic plastic tubes			

LINKS TO RELATED DOCUMENTS					
Dimensions	TO-220AC	www.vishay.com/doc?95259			
Dimensions	TO-220 FULL-PAK	www.vishay.com/doc?95260			
Part marking information	TO-220AC	www.vishay.com/doc?95391			
Part marking information	TO-220 FULL-PAK	www.vishay.com/doc?95392			
Packaging information		www.vishay.com/doc?95388			

Legal Disclaimer Notice



Vishay

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and/or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk and agree to fully indemnify and hold Vishay and its distributors harmless from and against any and all claims, liabilities, expenses and damages arising or resulting in connection with such use or sale, including attorneys fees, even if such claim alleges that Vishay or its distributor was negligent regarding the design or manufacture of the part. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

Document Number: 91000 www.vishay.com
Revision: 11-Mar-11 1