SiT8003 Low Power Programmable Oscillator



Features

- World's lowest power programmable oscillator, <3.5 mA typical current consumption
- + 1-80 MHz frequency range. Contact SiTime for frequencies between 80 MHz -110 MHz
- Extremely fast start-up time (<3 ms), enabling power-cycling for lower system power
- Available in four industry standard packages: 2.5 x 2.0, 3.2 x 2.5, 5.0 x 3.2, 7.0 x 5.0 mm
- Programmable standby or output enable modes
- + <10 $\mu \rm A$ current consumption in standby mode
- All-silicon device with outstanding reliability of 2 FIT, 10x improvement over quartz-based devices, enhancing system MTBF
- Outstanding mechanical robustness for portable applications
- · Ultra short lead time
- Ideal for portable applications :portable media players, digital cameras, digital camcorders, portable navigation device, handheld gaming, cell phone and other handheld applications.
- Ideal for high-speed serial protocols such as: USB 1.1, USB 2.0, SATA, SAS, Fiber Channel, Firewire, Ethernet, PCI Express, etc

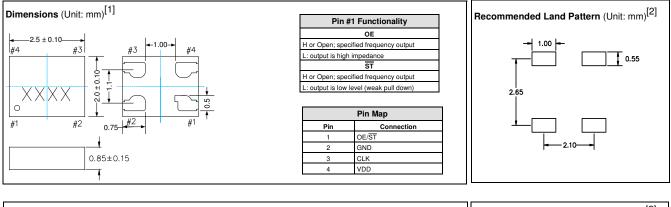
Specifications

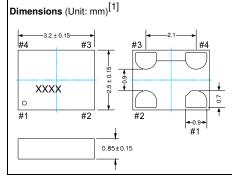
| Parameter | Symbol | Min. | Тур. | Max. | Unit | Condition |
|-----------------------------|------------|------|-------|------|-------|--|
| Output Frequency Range | f | 1 | - | 80 | MHz | Contact SiTime for frequencies between 80 MHz - 110 MHz |
| Frequency Tolerance | F_tol | -25 | - | +25 | PPM | Inclusive of: Initial tolerance, operating temperature, rated power, supply voltage change, load change, aging (1st yr@25 $^\circ$), shock and vibration. |
| | | -30 | _ | +30 | PPM | |
| | | -50 | _ | +50 | PPM | |
| | | -100 | _ | +100 | PPM | |
| Aging | Aq | - | - | 1.0 | PPM | 1st year at 25 ℃ |
| Storage Temperature Range | , .g | -55 | - | +125 | °C | |
| Operating Temperature Range | T use | -33 | _ | +70 | °C | Extended Commercial |
| Operating Temperature Range | I_use | -20 | _ | +70 | ℃ | Industrial |
| Supply Voltage | Vdd | 1.62 | - 1.8 | 1.98 | v | Industrial |
| | Vaa | - | 2.5 | 2.75 | V | - |
| | | 2.25 | - | - | V | - |
| | | 2.52 | 2.8 | 3.08 | | - |
| | | 2.97 | 3.3 | 3.63 | V | |
| Current Consumption | ldd | | 3.0 | 3.5 | mA | No load condition, f = 20 MHz, Vdd = 1.8 V |
| | | | 3.5 | 4.0 | mA | No load condition, f = 20 MHz, Vdd = 2.5 V, 2.8 V or 3.3 V |
| Standby Current | I_std | - | 3 | 10 | μΑ | Output is Weakly Pulled Down, ST = GND, Vdd = 1.8 V |
| | | - | 7 | 10 | μA | Output is Weakly Pulled Down, $\overline{ST} = GND$, Vdd = 2.5 V, 2.8V or 3.3 V |
| Duty Cycle | DC | 45 | - | 55 | % | All Vdds. f <= 70 MHz |
| | | 40 | - | 60 | % | All Vdds. f > 70 MHz |
| Rise/Fall Time | Tr, Tf | - | 1 | 2 | ns | 20% - 80% Vdd level, 15pf load |
| Output Voltage High | VOH | 90 | - | - | %Vdd | IOH = -4 mA (Vdd = 3.3 V) IOH = -3 mA (Vdd = 2.8 V and Vdd = 2.5 V) IOH = -2 mA (Vdd = 1.8 V) |
| Output Voltage Low | VOL | - | - | 10 | %Vdd | IOL = 4 mA (Vdd = 3.3 V) IOL = 3 mA (Vdd = 2.8 V and Vdd = 2.5 V) IOL = 2 mA (Vdd = 1.8 V) |
| Output Load | Ld | - | - | 15 | pF | Maximum frequency and supply voltage. Contact SiTime for higher load |
| Input Voltage High | VIH | 70 | - | - | %Vdd | Pin 1, OE or ST |
| Input Voltage Low | VIL | - | _ | 30 | %Vdd | Pin 1, OE or ST |
| Input Current | l_in | - | - | 10 | μA | |
| Start up Time | T_osc | - | - | 3 | ms | Measured from the time Vdd reaches its rated minimum value |
| RMS Period Jitter | T_jitt | _ | - | 6 | ps | f = 48 MHz, Vdd = 1.8 V |
| | | - | - | 4 | ps | f = 48 MHz, Vdd = 2.5 V, 2.8 V or 3.3 V |
| RMS Phase Jitter (random) | T phj | - | 1.60 | - | ps | f = 62.5 MHz, Integration bandwidth = 1.875 MHz to 20 MHz |
| | , | _ | 1.00 | - | ps | f = 75 MHz, Integration bandwidth = 900 kHz to 7.5 MHz |

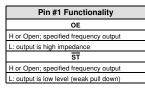


Rev. 1.2

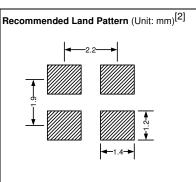
Dimensions, Pin Description and Land Pattern

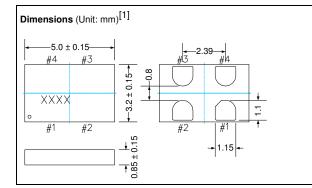


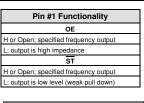


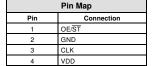


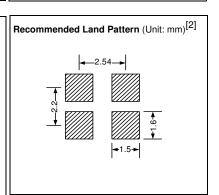
| Pin Map | | | | |
|---------|------------|--|--|--|
| Pin | Connection | | | |
| 1 | OE/ST | | | |
| 2 | GND | | | |
| 3 | CLK | | | |
| 4 | VDD | | | |

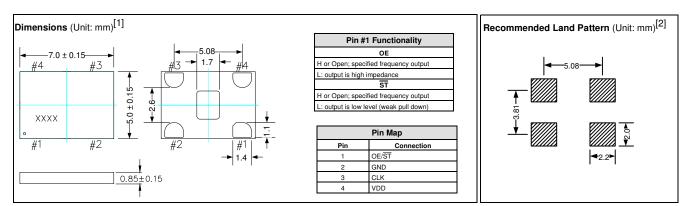










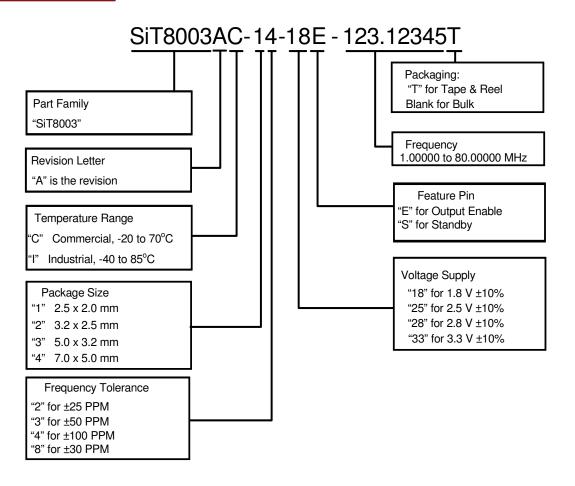


Notes:

1. XXXX top marking denotes manufacturing lot number.

2. A capacitor of value $0.1\mu F$ between Vdd and GND is recommended.

Part No. Guide- How to Order



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