

# 28F0121-0SR-10



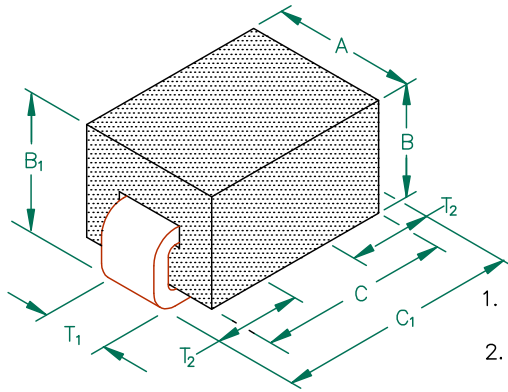
**UNCONTROLLED DOCUMENT**

**PHYSICAL DIMENSIONS:**

- A 3.05 [.120] ± 0.08 [.003]
- B 2.54 [.100] ± 0.08 [.003]
- B<sub>1</sub> 3.05 [.120] ± MAX
- C 4.06 [.160] ± 0.15 [.006]
- C<sub>1</sub> 5.08 [.200] ± MAX

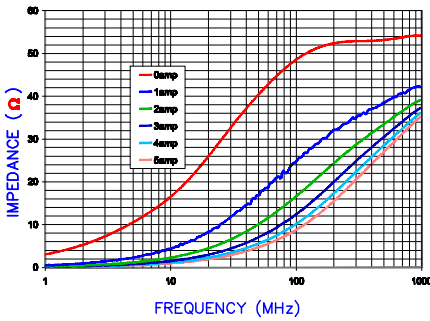
**WIRE DIMENSIONS:**

- T<sub>1</sub> 1.27 [.050] ± 0.05 [.002]
- T<sub>2</sub> 1.52 [.060] ± 0.51 [.020]



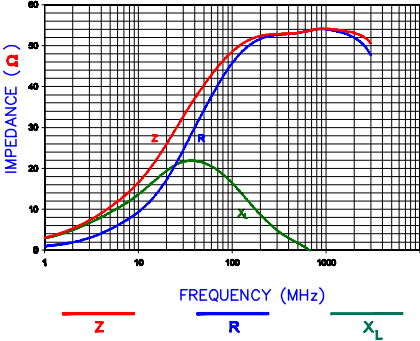
ELECTRICAL CHARACTERISTICS:			
Z @ 100MHz (Ω)	DCR (Ω)	Rated Current	
Nominal	48		
Minimum	36		
Maximum	60	.00075	10,000 mA

Z vs. FREQUENCY  
IMPEDANCE UNDER DC BIAS



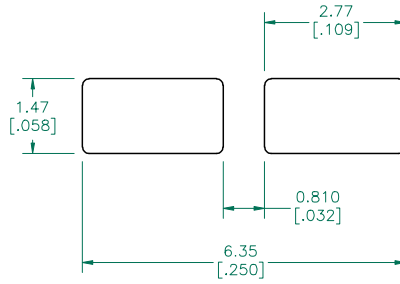
FREQUENCY (MHz)

|Z|, R, AND X vs. FREQUENCY



FREQUENCY (MHz)

**LAND PATTERNS FOR REFLOW SOLDERING**

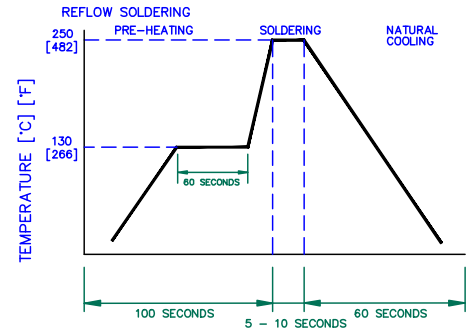


(For wave soldering, add 0.762 [.030] to this dimension.)

**NOTES: UNLESS OTHERWISE SPECIFIED**

- TAPED AND REELED per CURRENT EIA SPECIFICATIONS 13" REELS, 2500 PCS/REEL.
- COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- REF. CARRIER TAPE SPECIFICATION # CART121-03.

**RECOMMENDED SOLDERING CONDITIONS**



DIMENSIONS ARE IN mm (INCHES).				This print is the property of Laird Tech. and is loaned in confidence subject to return upon request and with the understanding that no copies shall be made without the written consent of Laird Tech. All rights to design or invention are reserved.	
H	UPDATE COMPANY LOGO	10/24/08	JRK	<b>Laird TECHNOLOGIES</b> PROJECT/PART NUMBER: <b>28F0121-0SR-10</b> REV: <b>H</b> PART TYPE: <b>ASSEMBLY</b> DRAWN BY: <b>JRK</b> DATE: 11/13/02 SCALE: <b>NTS</b> SHEET: <b>2 of 3</b> TOOL # -	
G	CORRECT TYPO ERROR IN ELECT TABLE	11/26/07	JRK		
F	UPDATE LANDPATTERN ADD ROHS SYMBOL	08/09/07	JRK		
E	UPDATE COMPANY LOGO	04/19/07	JRK		
D	CHANGE RATED CURRENT FROM 5000mA	02/17/04	JRK		
C	ADD DC BIAS CURVE UPDATE ZRX CURVE & DCR VALUE	08/29/03	JRK		
B	UPDATE REFLOW SOLDERING TEMPERATURE UPDATE NOTE # 5 ON SHT3	03/14/03	JRK		
REV	DESCRIPTION	DATE	INT		

AGILENT E4991A RF Impedance/Material Analyzer  
HP 16194A Test Fixture. TEST REF. 3609