

Vishay Siliconix

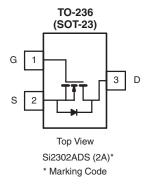
N-Channel 2.5-V (G-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)			
20	0.060 at V _{GS} = 4.5 V	2.4			
20	0.115 at V _{GS} = 2.5 V	2.0			

FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- Compliant to RoHS Directive 2002/95/EC





Ordering Information: Si2302ADS-T1-E3 (Lead (Pb)-free) Si2302ADS-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS	$T_A = 25 \ ^\circ C$, unle	ess otherwise	noted		
Parameter		Symbol	5 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	20		V
Gate-Source Voltage		V _{GS}	± 8		
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 25 °C	- I _D	2.4	2.1	٨
	T _A = 70 °C		1.9	1.7	
Pulsed Drain Current ^a		I _{DM}	10		A
Continuous Source Current (Diode Conduction) ^a		۱ _S	0.94	0.6	
Power Dissipation ^a	T _A = 25 °C	- P _D	0.9	0.7	w
	T _A = 70 °C		0.57	0.46	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum lungting to Ambiguit	t ≤ 5 s	R _{thJA}	115	140	°C/W	
Maximum Junction-to-Ambient ^a	Steady State		140	175	0/11	

Notes:

a. Surface mounted on FR4 board.

For SPICE model information via the Worldwide Web: www.vishay.com/www/product/spice.htm

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 V, I_D = 10 \mu A$				V	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 50 \ \mu A$	0.65	0.95	1.2	- v	
Gate Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			± 100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}$			0.1	μΑ	
	IDSS	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, \text{ T}_{J} = 55 ^{\circ}\text{C}$			2.0		
On-State Drain Current ^a		$V_{DS} \ge 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	6			А	
	I _{D(on)}	$V_{DS} \ge 5$ V, $V_{GS} = 2.5$ V	4			A	
Drain-Source On-Resistance ^a	Р	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 3.6 \text{ A}$		0.045	0.060 ^b	Ω	
	R _{DS(on)}	$V_{GS} = 2.5 \text{ V}, \text{ I}_{D} = 3.1 \text{ A}$		0.070	0.115		
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 5 \text{ V}, \text{ I}_{D} = 3.6 \text{ A}$		8		S	
Diode Forward Voltage	V _{SD}	I _S = 0.94 A, V _{GS} = 0 V		0.76	1.2	V	
Dynamic				•	•		
Total Gate Charge	Q _g			4.0	10	nC	
Gate-Source Charge	Q _{gs}	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 3.6 \text{ A}$		0.65			
Gate-Drain Charge	Q _{gd}			1.5			
Input Capacitance	C _{iss}			300			
Output Capacitance	C _{oss}	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$		120		pF	
Reverse Transfer Capacitance	C _{rss}			80			
Switching			• •				
Turn-On Delay Time	t _{d(on)}			7	15	- ns	
Rise Time	t _r	V_{DD} = 10 V, R_L = 2.8 Ω		55	80		
Turn-Off DelayTime	t _{d(off)}	$\text{I}_\text{D} \cong$ 3.6 A, V_GEN = 4.5 V, R_g = 6 Ω		16	60		
Fall Time	t _f	·		10	25		

Notes:

a. Pulse test; PW \leq 300 $\mu s,$ duty cycle \leq 2 %.

b. Effective for production 10/04.

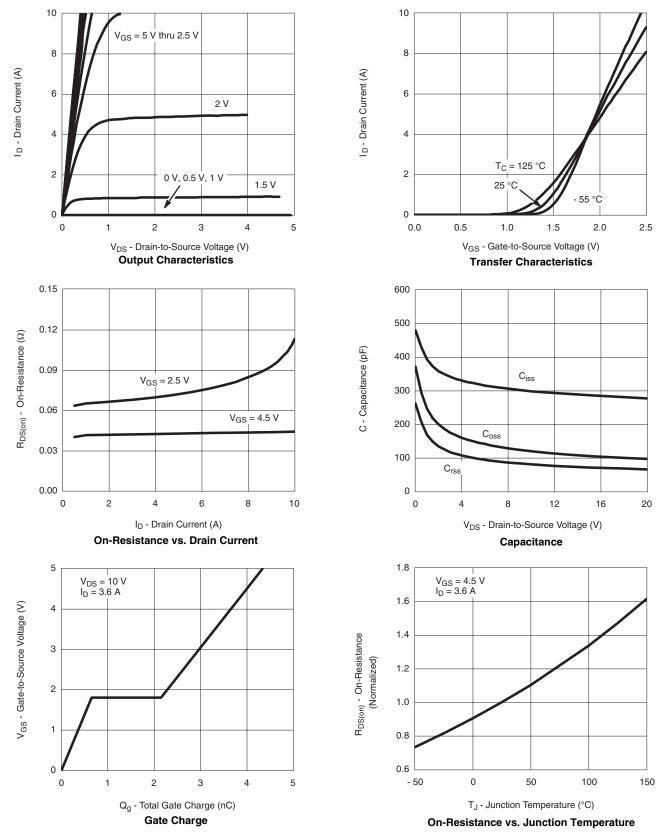
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



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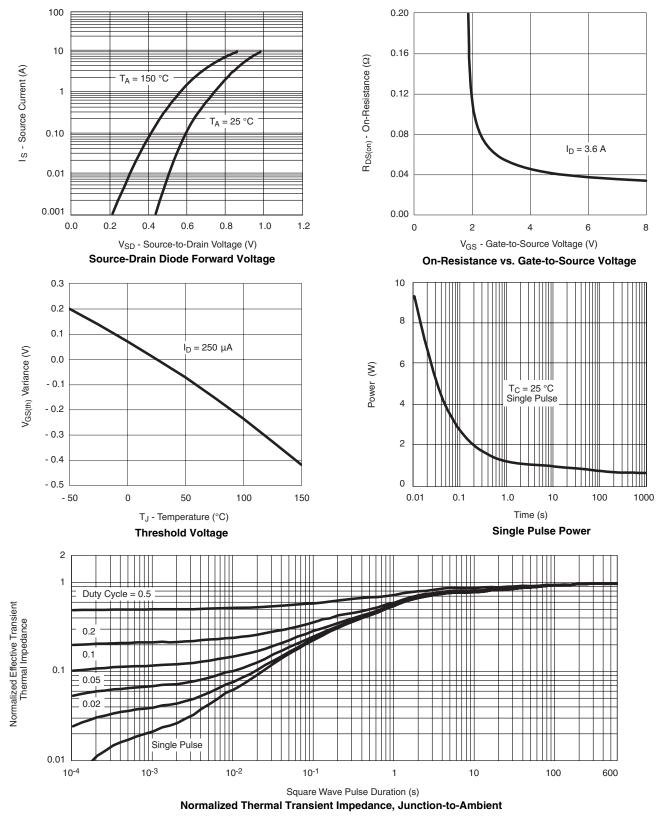


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Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?71831.

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