

Vishay Siliconix

P-Channel 2.5-V (G-S) MOSFET

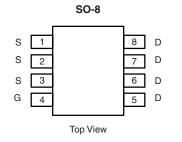
PRODUCT SUMMARY					
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)			
- 20	0.011 at V _{GS} = - 10 V	- 13.7			
	0.014 at V _{GS} = - 4.5 V	- 12.3			
	0.020 at V _{GS} = - 2.5 V	- 10.3			

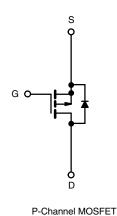
FEATURES

- Halogen-free According to IEC 61249-2-21
 Available
- TrenchFET[®] Power MOSFETs



Available





Ordering Information: Si4463BDY-T1-E3 (Lead (Pb)-free) Si4463BDY-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS	T _A = 25 °C, unle	ss otherwise r	noted		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	- 20		V
Gate-Source Voltage		V _{GS}	± 12		v
	T _A = 25 °C	– I _D	- 13.7	- 9.8	٩
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		- 11.1	- 7.9	
Pulsed Drain Current		I _{DM}	- 50		A
Continuous Source Current (Diode Conduction) ^a	uction) ^a Is - 2.		- 2.7	- 1.36	
Maximum Power Dissipation ^a	T _A = 25 °C	- P _D	3.0	1.5	W
	T _A = 70 °C		1.9	0.95	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
	t ≤ 10 s	- R _{thJA} R _{thJF}	35	42		
Maximum Junction-to-Ambient ^a	Steady State		70	84	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State		17	21		

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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Parameter	Symbol	ool Test Conditions		Тур.	Max.	Unit	
Static	•						
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \ \mu A$	- 0.6		- 1.4	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 12 V$		± 100	nA		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -20 V, V_{GS} = 0 V$			- 1	μA	
		V_{DS} = - 20 V, V_{GS} = 0 V, T_{J} = 70 °C			- 10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = -5 V, V_{GS} = -4.5 V$	- 30			Α	
Drain-Source On-State Resistance ^a		V _{GS} = - 10 V, I _D = - 13.7 A		0.0085	0.011		
	R _{DS(on)}	$V_{\rm GS} = -4.5 \text{ V}, \text{ I}_{\rm D} = -12.3 \text{ A}$ 0.010			0.014	Ω	
		V _{GS} = - 2.5 V, I _D = - 5 A		0.015	0.020		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 10 V, I _D = - 13.7 A		44		S	
Diode Forward Voltage ^a	V _{SD}	I _S = - 2.7 A, V _{GS} = 0 V		- 0.7	- 1.1	V	
Dynamic ^b		·					
Total Gate Charge	Qg			37	56		
Gate-Source Charge	Q _{gs}	V_{DS} = - 10 V, V_{GS} = - 4.5 V, I_D = - 13.7 A		8.7		nC	
Gate-Drain Charge	Q _{gd}			11]	
Gate Resistance	Rg	f = 1 MHz		2.7		Ω	
Turn-On Delay Time	t _{d(on)}			35	55		
Rise Time	t _r	V_{DD} = - 10 V, R_L = 10 Ω		60	90	ns	
Turn-Off Delay Time	t _{d(off)}	$I_{D}\cong$ - 1 A, V_{GEN} = - 4.5 V, R_{g} = 6 Ω		115	170		
Fall Time	t _f			75	115		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 2.3 A, dl/dt = 100 A/μs		50	75		

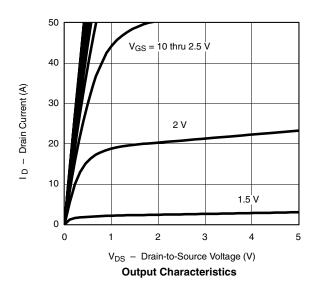
Notes:

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

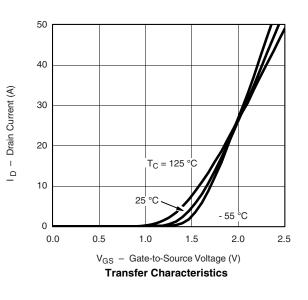
b. Guaranteed by design, not subject to production testing.

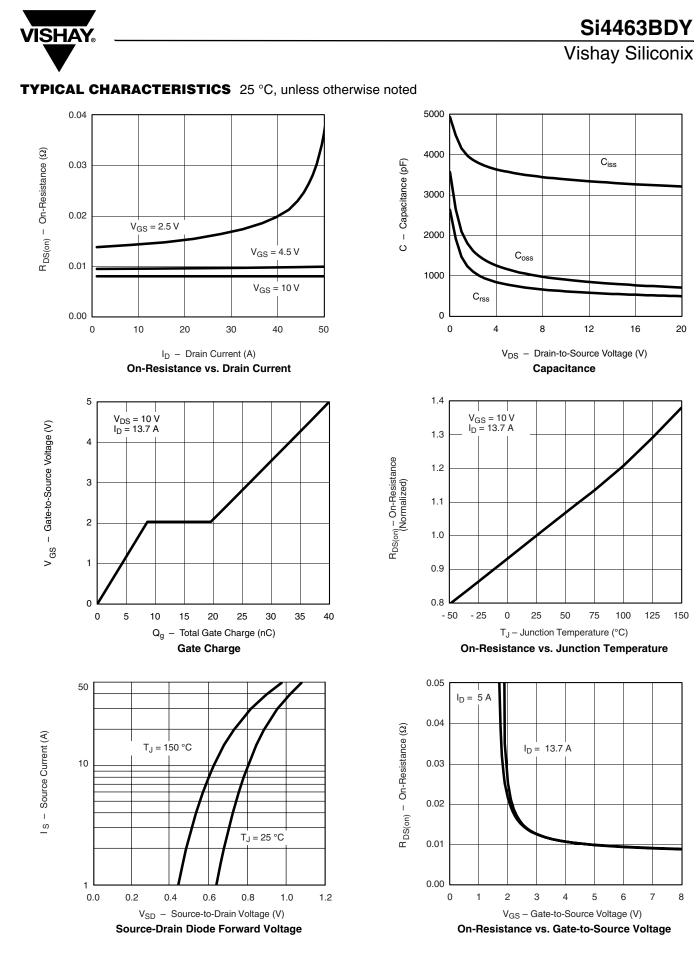
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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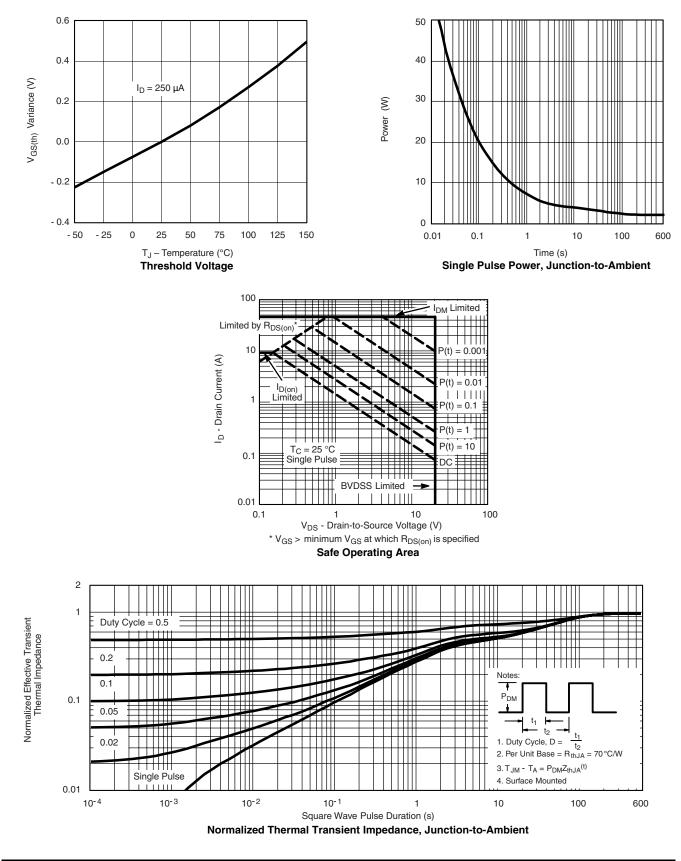
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Si4463BDY

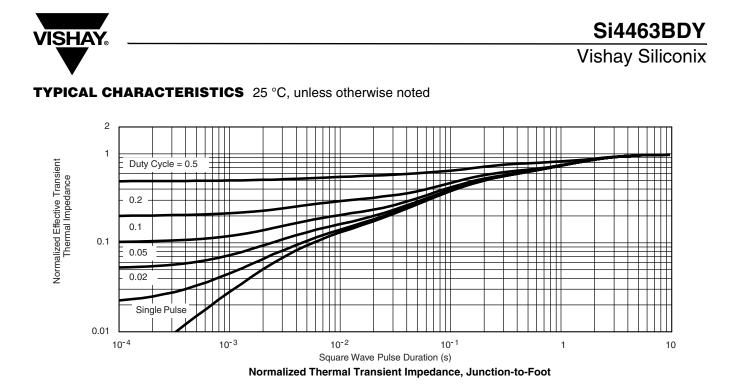
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Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?72789.



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