



P-Channel 1.8-V (G-S) MOSFET

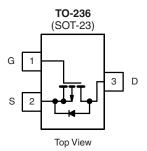
PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A)		
- 12	0.050 at V _{GS} = - 4.5 V	- 3.85		
	0.065 at V _{GS} = - 2.5 V	- 3.4		
	0.100 at V _{GS} = - 1.8V	- 2.7		

FEATURES

- Halogen-free Option Available
- TrenchFET® Power MOSFETs: 1.8 V Rated



RoHS'



Si2315BDS *(M5)

* Marking Code

Ordering Information: Si2315BDS-T1

Si2315BDS-T1-E3 (Lead (Pb)-free)

Si2315BDS-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS	A = 25 °C, unle	ss otherwise r	noted		
Parameter		Symbol	5 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	- 12		V
Gate-Source Voltage		V _{GS}	± 8		
Continuous Dunis Comment /T 150 °C)	T _A = 25 °C	- I _D	- 3.85	- 3.0	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		- 3.0	- 2.45	
Pulsed Drain Current ^a		I _{DM}	- 12		Α
Continuous Source Current (Diode Conduction) ^a		I _S	- 1.0	- 0.62	ı
Power Dissipation ^a	T _A = 25 °C	- P _D	1.19	0.75	W
	T _A = 70 °C		0.76	0.48	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Тур.	Max.	Unit
Manimum Innation to Ambient	t ≤ 5 s	- R _{thJA}	85	105	°C/W
Maximum Junction-to-Ambient ^a	Steady State		130	166	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	60	75	

Notes:

a. Surface Mounted on FR4 board.

b. t ≤ 5 s.

For SPICE model information via the Worldwide Web: http://www.vishay.com/www/product/spice.htm.

* Pb containing terminations are not RoHS compliant, exemptions may apply.

Si2315BDS

Vishay Siliconix



SPECIFICATIONS $T_J = 25$			Limits				
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static			_	•			
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = -10 \mu\text{A}$	- 12			V	
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = -250 \mu A$	- 0.45		- 0.90		
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 8 V			± 100	nA	
Zero Gate Voltage Drain Current		V _{DS} = - 12 V, V _{GS} = 0 V			- 1	—— uA	
	I _{DSS}	V _{DS} = - 12 V, V _{GS} = 0 V, T _J = 55 °C			- 10		
On-State Drain Current ^a		$V_{DS} \le$ - 5 V, $V_{GS} =$ - 4.5 V	- 6				
	I _{D(on)}	$V_{DS} \le -5 \text{ V}, V_{GS} = -2.5 \text{ V}$	- 3			A	
		$V_{GS} = -4.5 \text{ V}, I_D = -3.85 \text{ A}$		0.040	0.050	-	
Drain-Source On Resistance ^a	R _{DS(on)}	$V_{GS} = -2.5 \text{ V}, I_D = -3.4 \text{ A}$		0.050	0.065		
		$V_{GS} = -1.8 \text{ V}, I_D = -2.7 \text{ A}$		0.071	0.100	1	
Forward Transconductance ^a	g _{fs}	V _{DS} = - 5 V, I _D = - 3.85 A		7		S	
Diode Forward Voltage ^a	V_{SD}	I _S = - 1.6 A, V _{GS} = 0 V			- 1.2	V	
Dynamic ^b	<u> </u>						
Total Gate Charge	Q_g	V 0VV 45V		8	15		
Gate-Source Charge	Q_{gs}	$V_{DS} = -6 \text{ V}, V_{GS} = -4.5 \text{ V}$ $I_{D} \cong -3.85 \text{ A}$		1.1		nC	
Gate-Drain Charge	Q _{gd}	ID = - 3.00 A		2.3			
Input Capacitance	C _{iss}			715			
Output Capacitance	C _{oss}	$V_{DS} = -6 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		275		pF	
Reverse Transfer Capacitance	C _{rss}			200		1	
Switching ^b				•			
Turn-On Time	t _{d(on)}			15	20		
	t _r	$V_{DD} = -6 \text{ V}, R_L = 6 \Omega$ $I_D \cong -1.0 \text{ A}, V_{GEN} = -4.5 \text{ V}$		35	50	1	
T 0# Time	$t_{d(off)}$ $t_{D} \cong -1.0 \text{ A}, v_{GEN} = 0.0 \text{ A}$			50	70	ns	
Turn-Off Time	t _f	11G - 022		50	75	1	

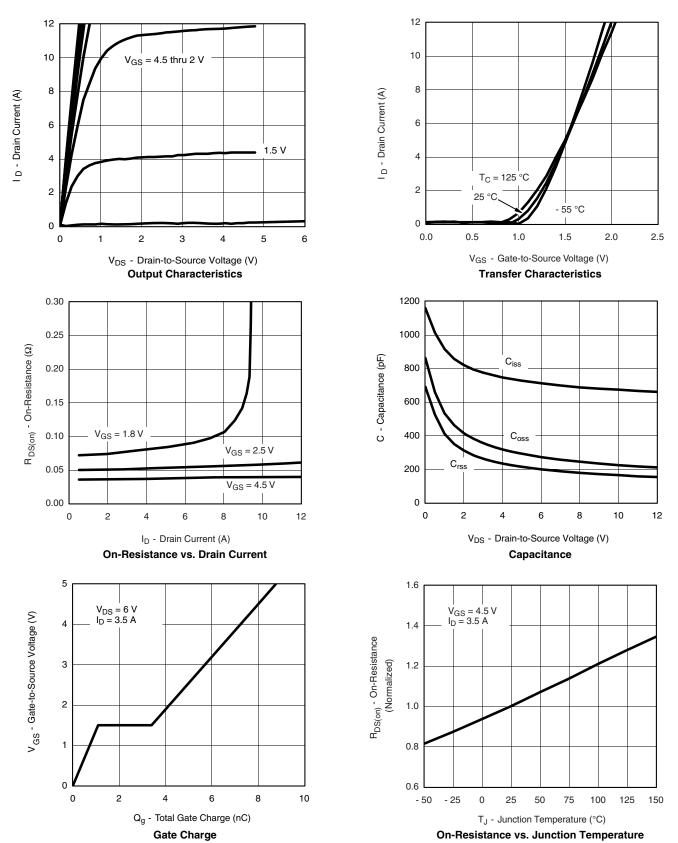
Notes:

- a. For DESIGN AID ONLY, not subject to production testing.
- b. Pulse test: PW \leq 300 μs duty cycle \leq 2 %.
- c. Switching time is essentially independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

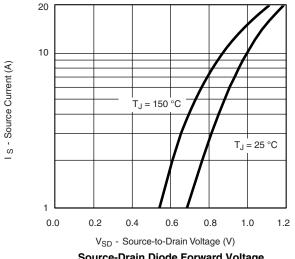


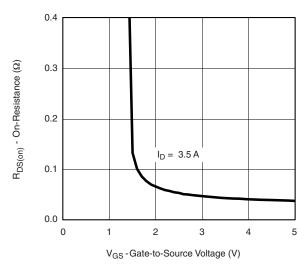
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



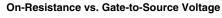
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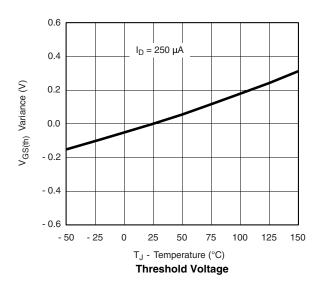
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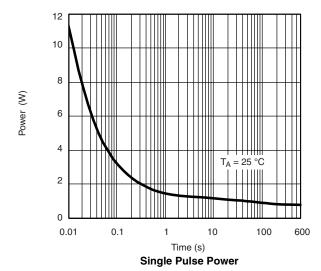


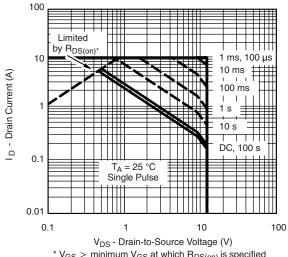


Source-Drain Diode Forward Voltage



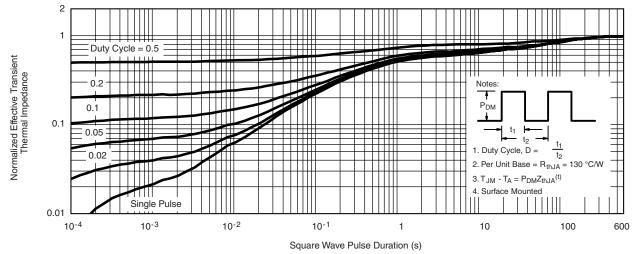








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Normalized Thermal Transient Impedance, Junction-to-Ambient

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?72014.

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