



# **Dual P-Channel 12-V (D-S) MOSFET**

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	$R_{DS(on)}\left(\Omega\right)$	I <sub>D</sub> (A)			
	0.018 at V <sub>GS</sub> = - 4.5 V	- 8.9			
- 12	0.022 at V <sub>GS</sub> = - 2.5 V	- 8.1			
	0.028 at V <sub>GS</sub> = - 1.8 V	- 3.6			

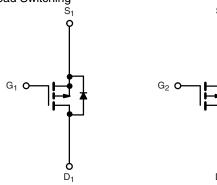
#### **FEATURES**

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- Advanced High Cell Density Process
- Compliant to RoHS Directive 2002/95/EC



#### **APPLICATIONS**

· Load Switching



P-Channel MOSFET

P-Channel MOSFET

		SO-8		
S <sub>1</sub> G <sub>1</sub> S <sub>2</sub> G <sub>2</sub>	1 2 3		8 7 6 5	D <sub>1</sub> D <sub>2</sub> D <sub>2</sub>
		Top View	l	

Ordering Information: Si4931DY-T1-E3 (Lead (Pb)-free)

Si4931DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

<b>ABSOLUTE MAXIMUM RATINGS</b>	T <sub>A</sub> = 25 °C, unle	ss otherwise r	noted		
Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V <sub>DS</sub>	- 12		V
Gate-Source Voltage		V <sub>GS</sub>	± 8		
Ocaliana Davis Ocasa (T., 450,00)3	T <sub>A</sub> = 25 °C	I <sub>D</sub>	- 8.9	- 6.7	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C		- 7.1	- 5.4	<b>A</b>
Pulsed Drain Current		I <sub>DM</sub>	- 30		Α
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	- 1.7	- 0.9	
	T <sub>A</sub> = 25 °C	- P <sub>D</sub>	2.0	1.1	W
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C		1.3	0.7	VV
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient <sup>a</sup>	t ≤ 10 s	- R <sub>thJA</sub>	46	62.5	°C/W
	Steady State		80	110	
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	24	32	]

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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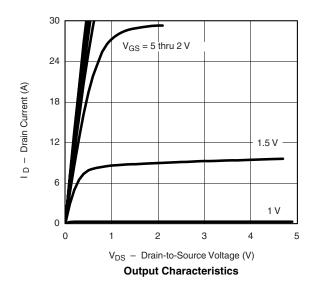
Parameter	Symbol	otherwise noted  Test Conditions Min		lin. Typ.	Max.	Unit
Static	<u>'</u>					
Gate Threshold Voltage V <sub>GS(th)</sub>		$V_{DS} = V_{GS}, I_{D} = -350 \mu A$	- 0.4		- 1.0	V
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$ $\pm 1$		± 100	nA	
Zero Gate Voltage Drain Current	,	V <sub>DS</sub> = - 12 V, V <sub>GS</sub> = 0 V			- 1	μΑ
	I <sub>DSS</sub>	V <sub>DS</sub> = - 12 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			- 5	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = - 5 V, V <sub>GS</sub> = - 4.5 V	- 30			Α
Drain-Source On-State Resistance <sup>a</sup>		V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 8.9 A	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 8.9 A		0.018	Ω
	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 2.5 V, I <sub>D</sub> = - 8.1 A		0.018	0.022	
		V <sub>GS</sub> = - 1.8 V, I <sub>D</sub> = - 3.6 A		0.023	0.028	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 6 V, I <sub>D</sub> = - 8.9 A		26		S
Diode Forward Voltage <sup>a</sup>	$V_{SD}$	I <sub>S</sub> = - 1.7 A, V <sub>GS</sub> = 0 V		- 0.7	- 1.2	٧
Dynamic <sup>b</sup>						
Total Gate Charge	$Q_g$			34.5	52	
Gate-Source Charge	Q <sub>gs</sub> Q <sub>gd</sub>	$V_{DS} = -6 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -8.9 \text{ A}$		5.1		nC
Gate-Drain Charge				9.6		
Gate Resistance	$R_g$			9		Ω
Turn-On Delay Time	t <sub>d(on)</sub>			25	40	
Rise Time	t <sub>r</sub>	$V_{DD} = -6 \text{ V}, R_L = 6 \Omega$		46	70	
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong$ - 1 A, $V_{GEN}$ = - 4.5 V, $R_g$ = 6 $\Omega$		230	345	ns
Fall Time	t <sub>f</sub>			155	235	
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = - 1.7 A, dl/dt = 100 A/μs		128	200	

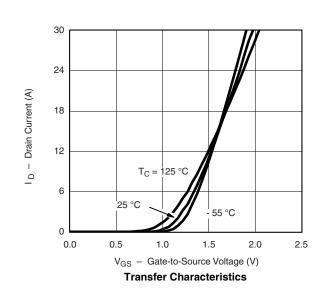
#### Notes:

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS 25 °C unless otherwise noted



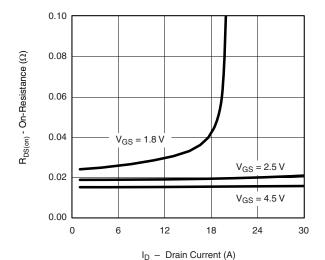




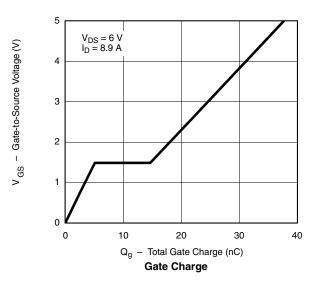


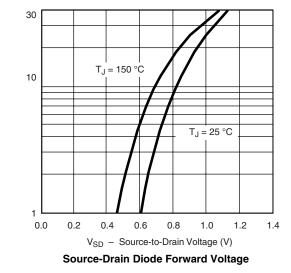


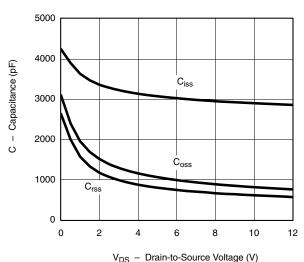
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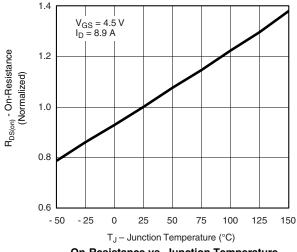
On-Resistance vs. Drain Current



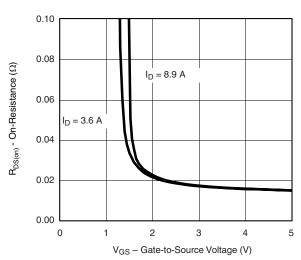




Capacitance



On-Resistance vs. Junction Temperature



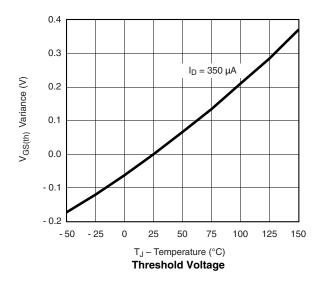
On-Resistance vs. Gate-to-Source Voltage

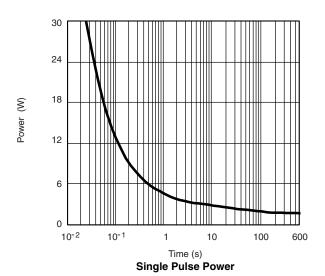
Source Current (A)

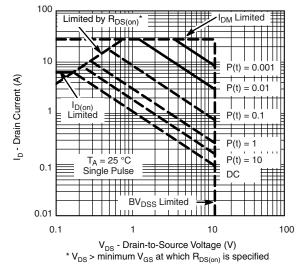
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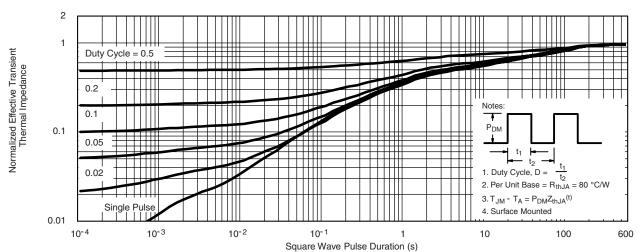
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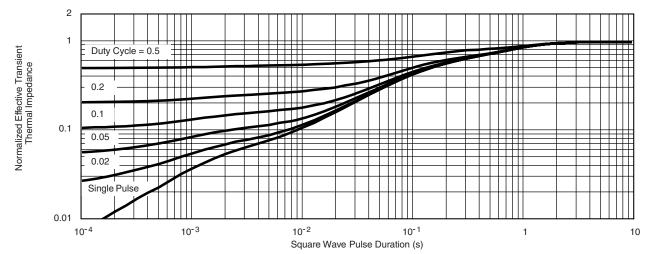
Safe Operating Area, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Ambient



#### TYPICAL CHARACTERISTICS 25 °C unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

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