

**Vishay Siliconix** 

# P-Channel 2.5-V (G-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω) I <sub>D</sub> (A			
	0.060 at V <sub>GS</sub> = - 4.5 V	- 4.7		
- 20	0.090 at V <sub>GS</sub> = - 2.7 V	- 3.8		
	0.100 at V <sub>GS</sub> = - 2.5 V	- 3.7		

#### **FEATURES**

- Halogen-free According to IEC 61249-2-21
  Definition
- TrenchFET<sup>®</sup> Power MOSFET
- 100 % R<sub>g</sub> Tested
- Compliant to RoHS Directive 2002/95/EC



Ordering Information: Si3443BDV-T1-E3 (Lead (Pb)-free) Si3443BDV-T1-GE3 (Lead (Pb)-free and Halogen-free) Part Marking Code: 3B

	(4) S O
(3) G <b>O</b> —	
	<b>O</b> (1, 2, 5, 6) D

P-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> $T_A = 25 \text{ °C}$ , unless otherwise noted						
Parameter		Symbol	5 s	Steady State	Unit	
Drain-Source Voltage		V <sub>DS</sub>	- 20		v	
Gate-Source Voltage		V <sub>GS</sub>	± 12			
	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	- 4.7	- 3.6		
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C		- 3.8	- 2.8		
Pulsed Drain Current		I <sub>DM</sub>	- 20		A	
Continuous Source Current (Diode Conduction) <sup>a</sup>		۱ <sub>S</sub>	- 1.7	- 0.9		
Marian David Disainaliana	T <sub>A</sub> = 25 °C	- P <sub>D</sub>	2.0	1.1	W	
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 70 °C		1.3	0.7	vv	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
	t ≤ 5 s	- R <sub>thJA</sub> R <sub>thJF</sub>	50	62.5	
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		90	110	°C/W
Maximum Junction-to-Foot (Drain)	Steady State		30	36	

Notes

a. Surface Mounted on FR4 board,  $t \leq 5 \mbox{ s.}$ 

For SPICE model information via the Worldwide Web: www.vishay.com/www/product/spice.htm

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TSOP-6 Top View

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 0.6		- 1.4	V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 12 V$			± 100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = -20 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1	μA	
		$V_{DS}$ = - 20 V, $V_{GS}$ = 0 V, $T_{J}$ = 70 °C			- 5		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = -5 V, V_{GS} = -4.5 V$	- 15			Α	
Drain-Source On-State Resistance <sup>a</sup>		$V_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -4.7 \text{ A}$		0.048	0.060		
	R <sub>DS(on)</sub>	$V_{GS} = -2.7 \text{ V}, \text{ I}_{D} = -3.8 \text{ A}$		0.070	0.090	Ω	
		$V_{GS} = -2.5 \text{ V}, \text{ I}_{D} = -1 \text{ A}$		0.080	0.100		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 10 V, I <sub>D</sub> = - 4.7 A		11		S	
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	$I_{S} = -1.7 \text{ A}, V_{GS} = 0 \text{ V}$		- 0.8	- 1.2	V	
Dynamic <sup>b</sup>	<u> </u>			1			
Total Gate Charge	Qg			6	9		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = - 10 V, $V_{GS}$ = - 4.5 V, $I_D$ = - 4.7 A		1.4		nC	
Gate-Drain Charge	Q <sub>gd</sub>			1.9		1	
Gate Resistance	R <sub>g</sub>	f = 1 MHz	5	9.5	16.2	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			22	35		
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 10 V, $R_L$ = 10 $\Omega$		35	55	ns	
Turn-Off Delay Time	t <sub>d(off)</sub>	$\text{I}_{\text{D}}\cong$ - 1.0 A, $\text{V}_{\text{GEN}}$ = - 4.5 V, $\text{R}_{\text{g}}$ = 6 $\Omega$		45	70		
Fall Time	t <sub>f</sub>			25	40		
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = - 1.7 A, dl/dt = 100 A/μs		25	50		

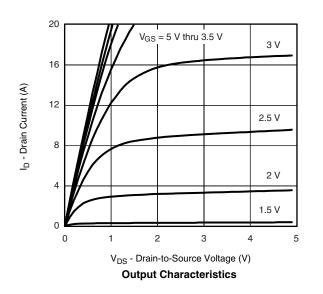
Notes

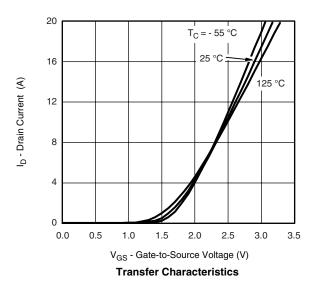
a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



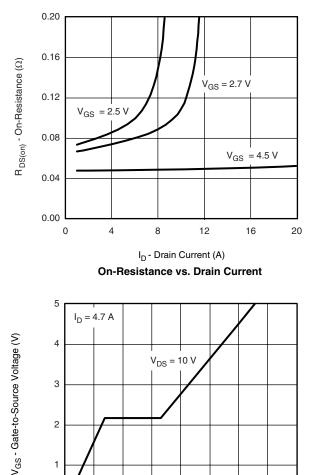


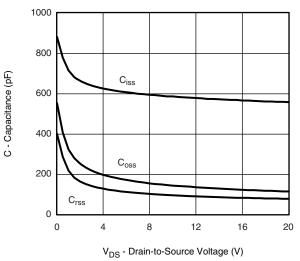
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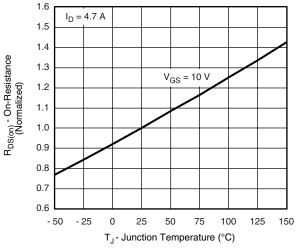
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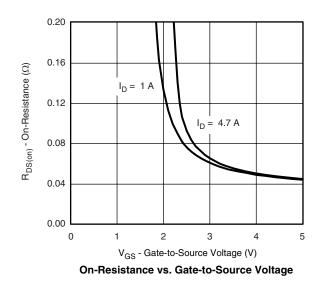












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1

0.0

0.2

0.4

0.6

V<sub>SD</sub> - Source-to-Drain Voltage (V)

Source-Drain Diode Forward Voltage

0.8

1.0

1.2

1.4

0

30

10

I<sub>S</sub> - Source Current (A)

0

1

2

3

5

4

Q<sub>g</sub> - Total Gate Charge (nC) Gate Charge

T<sub>J</sub> = 150 °C

6

7

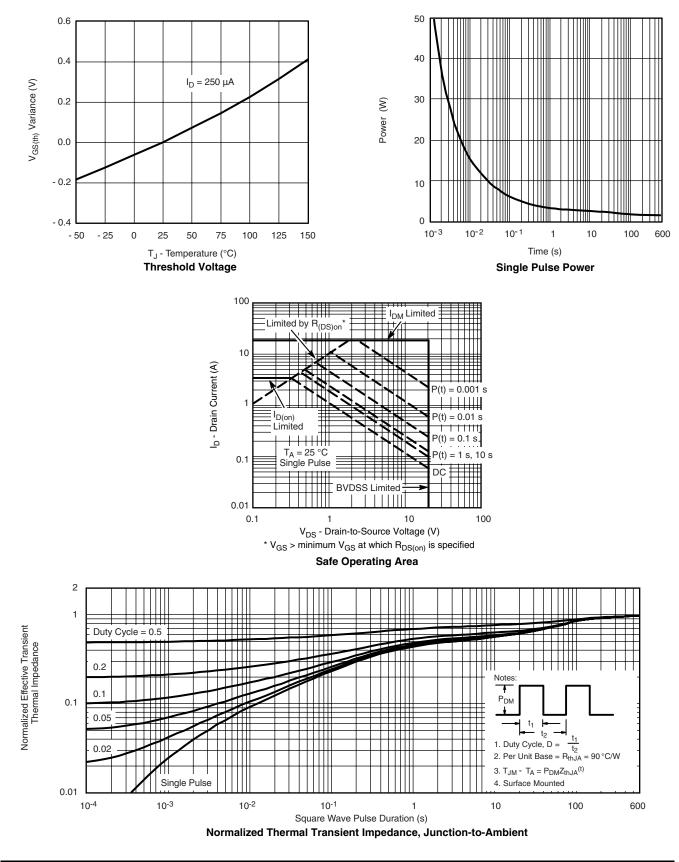
T<sub>J</sub> = 25 °C

8

### **Vishay Siliconix**



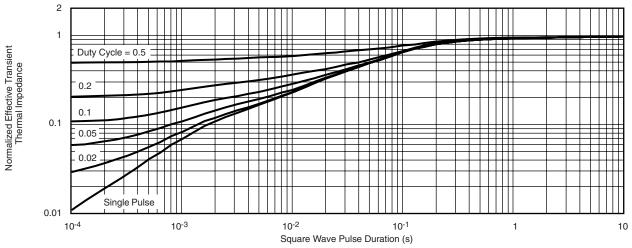
### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





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### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="http://www.vishay.com/ppg772749">www.vishay.com/ppg772749</a>.



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