

Vishay Siliconix

P-Channel 30-V (D-S) MOSFET

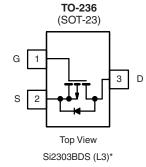
| PRODUCT SUMMARY | | | |
|---------------------|--|---------------------------------|--|
| V _{DS} (V) | R_{DS(on)} (Ω) | I _D (A) ^b | |
| - 30 | 0.200 at V _{GS} = - 10 V | - 1.64 | |
| | 0.380 at V _{GS} = - 4.5 V | - 1.0 | |

FEATURES

• Halogen-free Option Available



COMPLIANT



* Marking Code



| ABSOLUTE MAXIMUM RATINGS | T _A = 25 °C, unle | ss otherwise r | noted | | |
|---|------------------------------|-----------------------------------|-------------|--------------|------|
| Parameter | | Symbol | 5 s | Steady State | Unit |
| Drain-Source Voltage | | V _{DS} | - 30 | | V |
| Gate-Source Voltage | | V _{GS} | ± 20 | | |
| Continuous Durin Convert (T. 150 °C)b | T _A = 25 °C | – I _D | - 1.64 | - 1.49 | |
| Continuous Drain Current (T _J = 150 °C) ^b | T _A = 70 °C | | - 1.31 | - 1.2 | |
| Pulsed Drain Current ^a | | I _{DM} | - 10 | | A |
| Continuous Source Current (Diode Conduction) ^b | | ۱ _S | - 0.75 | - 0.6 | |
| | T _A = 25 °C | - P _D | 0.9 | 0.7 | W |
| Power Dissipation ^b | T _A = 70 °C | | 0.57 | 0.45 | vv |
| Operating Junction and Storage Temperature Range | | T _J , T _{stg} | - 55 to 150 | | °C |

| THERMAL RESISTANCE RATINGS | | | | | |
|--|---------------------|---------|---------|------|--|
| Parameter | Symbol | Typical | Maximum | Unit | |
| Maximum Junction-to-Ambient ^b | – R _{thJA} | 120 | 145 | °C/W | |
| Maximum Junction-to-Ambient ^c | | 140 | 175 | 0/11 | |

Notes:

a. Pulse width limited by maximum junction temperature.

b. Surface Mounted on FR4 board, t \leq 5 s.

c. Surface Mounted on FR4 board.

For SPICE model information via the Worldwide Web: http://www.vishay.com/www/product/spice.htm

* Pb containing terminations are not RoHS compliant, exemptions may apply.

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| | Symbol | | Limits | | | | |
|---|----------------------|---|-------------|--------|-------|------|--|
| Parameter | | Test Conditions | Min. | Тур. | Max. | Unit | |
| Static | | | | | | | |
| Drain-Source Breakdown Voltage | V _{(BR)DSS} | V_{GS} = 0 V, I _D = - 10 μ A | - 30 | | | V | |
| Gate-Threshold Voltage | V _{GS(th)} | $V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$ | - 1.0 - 3.0 | | - 3.0 | v | |
| Gate-Body Leakage | I _{GSS} | $V_{DS} = 0 V, V_{GS} = \pm 20 V$ | | | ± 100 | nA | |
| Zero Gate Voltage Drain Current | I _{DSS} | $V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$ | - 1 | | · . | | |
| | | $V_{DS} = -30$ V, $V_{GS} = 0$ V, $T_{J} = 55$ °C | | | - 10 | μΑ | |
| On-State Drain Current ^a | I _{D(on)} | $V_{DS} \leq$ - 5 V, V_{GS} = - 10 V | - 6 | | | А | |
| Drain-Source On-Resistance ^a | Б | V _{GS} = - 10 V, I _D = - 1.7 A | | 0.150 | 0.200 | Ω | |
| | R _{DS(on)} | V _{GS} = - 4.5 V, I _D = - 1.3 A | | 0.285 | 0.380 | | |
| Forward Transconductance ^a | 9 _{fs} | V _{DS} = - 5 V, I _D = - 1.7 A | | 2.0 | | S | |
| Diode Forward Voltage | V _{SD} | I _S = - 0.75 A, V _{GS} = 0 V | | - 0.85 | - 1.2 | V | |
| Dynamic ^b | | | • | • | | | |
| Total Gate Charge | Qg | | | 4.3 | 10 | nC | |
| Gate-Source Charge | Q _{gs} | V_{DS} = - 15 V, V_{GS} = - 10 V, $I_{D} \cong$ - 1.7 A | | 0.8 | | | |
| Gate-Drain Charge | Q _{gd} | | | 1.3 | | 1 | |
| Input Capacitance | C _{iss} | | | 180 | | | |
| Output Capacitance | C _{oss} | V_{DS} = - 15 V, V_{GS} = 0 V, f = 1 MHz | | 50 | | pF | |
| Reverse Transfer Capacitance | C _{rss} | | | 35 | | | |
| Switching ^c | • | | | | | | |
| Turn-On Time | t _{d(on)} | | | 55 | 80 | | |
| | t _r | $V_{DD} = -15 \text{ V}, \text{ R}_{L} = 15 \Omega$ $I_{D} \cong -1.0 \text{ A}, \text{ V}_{GEN} = -4.5 \text{ V}$ | | 40 | 60 |] | |
| Time Off Time | t _{d(off)} | $R_{\rm D} \cong$ - 1.0 A, $V_{\rm GEN} =$ - 4.5 V R _G = 6 Ω | | 10 | 20 | ns | |
| Turn-Off Time | t _f | 1.G - 0.32 | | 10 | 20 | | |

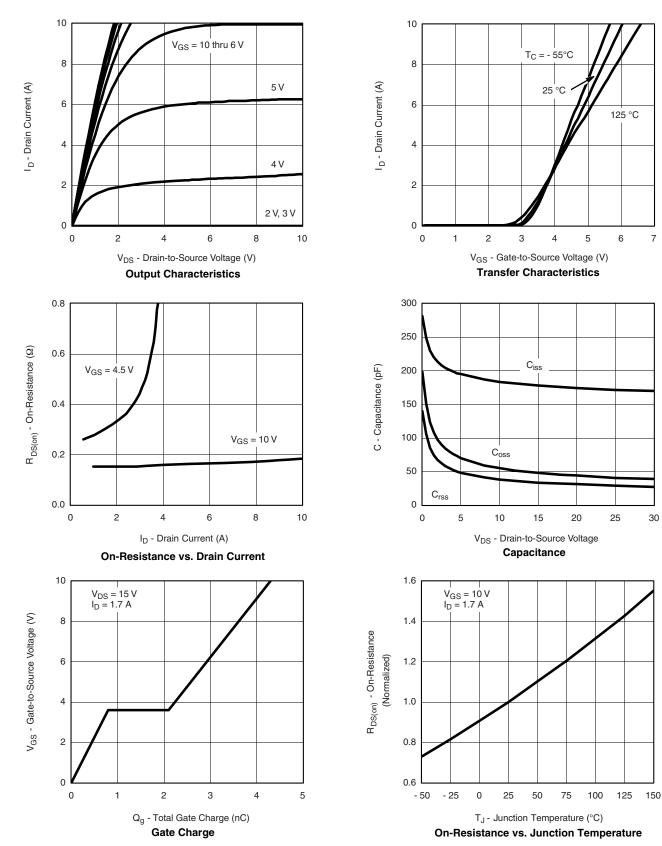
Notes:

a. Pulse test: PW \leq 300 $\mu s,$ duty cycle \leq 2 %.

b. For DESIGN AID ONLY, not subject to production testing.

c. Switching time is essentially independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

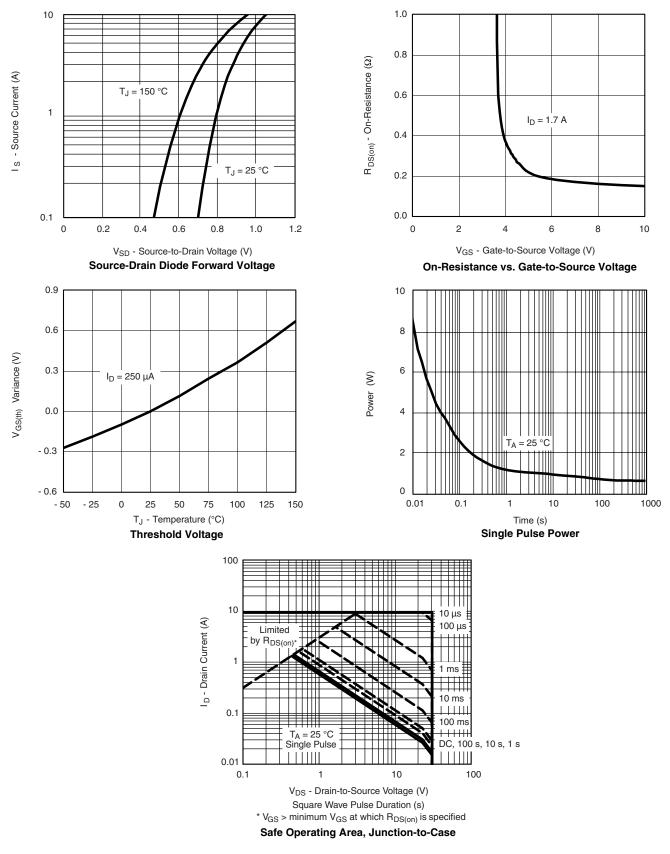
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Si2303BDS

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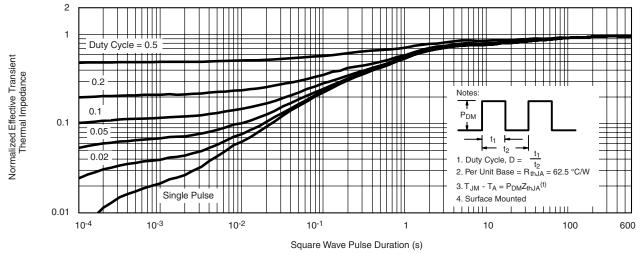


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Si2303BDS Vishay Siliconix

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?72065.



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