



## **Dual N-Channel 30-V (D-S) MOSFET**

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$	I <sub>D</sub> (A)		
30	$0.0165 \text{ at V}_{GS} = 10 \text{ V}$	8.7		
	0.022 at V <sub>GS</sub> = 4.5 V	7.5		

SO-8

Top View

Ordering Information: Si4330DY-T1-E3 (Lead (Pb)-free)

 $D_1$  $D_1$  $D_2$  $D_2$ 

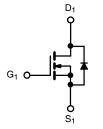
#### **FEATURES**

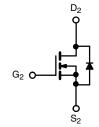
- Halogen-free According to IEC 61249-2-21 **Available**
- TrenchFET® Power MOSFET
- 100 % R<sub>g</sub> Tested

## COMPLIANT **HALOGEN FREE**

#### **APPLICATIONS**

- Notebook
  - Load Switch
  - DC/DC Conversion
  - Auxiliary Voltage





Si4330DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

N-Channel MOSFET

N-Channel MOSFET

<b>ABSOLUTE MAXIMUM RATINGS</b> T <sub>A</sub> = 25 °C, unless otherwise noted						
Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage		V <sub>DS</sub>	30		V	
Gate-Source Voltage		$V_{GS}$	± 20			
Continuous Drain Current /T 150 °C\a	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	8.7	6.6	Δ.	
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a</sup>	T <sub>A</sub> = 70 °C		7.0	5.3		
Pulsed Drain Current		I <sub>DM</sub>	± 30		А	
Continuous Source Current (Diode Conduction) <sup>a</sup>		I <sub>S</sub>	1.7	0.9		
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C	- P <sub>D</sub>	2.0	1.1	W	
	T <sub>A</sub> = 70 °C		1.3	0.7		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>a</sup>	t ≤ 10 s	- R <sub>thJA</sub>	45	62.5		
Maximum Junction-to-Ambient*	Steady State		85	110	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	26	35		

#### Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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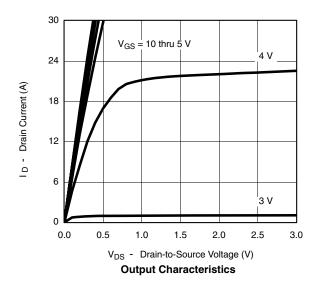
<b>SPECIFICATIONS</b> T <sub>J</sub> = 25 °C, unless otherwise noted								
Parameter	Symbol	Test Conditions	Min.	Min. Typ.		Unit		
Static								
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	1		3	V		
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA		
Zero Gate Voltage Drain Current		V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V	<sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V		1			
	I <sub>DSS</sub>	$V_{DS}$ = 30 V, $V_{GS}$ = 0 V, $T_J$ = 55 °C			5	μΑ		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	30			Α		
Drain-Source On-State Resistance <sup>a</sup>	D	$V_{GS} = 10 \text{ V}, I_D = 8.7 \text{ A}$		0.013	0.0165	0		
	R <sub>DS(on)</sub>	$V_{GS} = 4.5 \text{ V}, I_D = 7.5 \text{ A}$		0.018	0.022	Ω		
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	$V_{DS} = 15 \text{ V}, I_{D} = 8.7 \text{ A}$		28		S		
Diode Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>S</sub> = 1.7 A, V <sub>GS</sub> = 0 V		0.8	1.2	V		
Dynamic <sup>b</sup>								
Total Gate Charge	$Q_g$			13	20			
Gate-Source Charge	$Q_{gs}$	$V_{DS} = 15 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 8.7 \text{ A}$		7.1		nC		
Gate-Drain Charge	$Q_{gd}$			3.5				
Gate Resistance	$R_g$		0.5	1	1.7	Ω		
Turn-On Delay Time	t <sub>d(on)</sub>			10	15			
Rise Time	t <sub>r</sub>	$V_{DD}$ = 15 V, $R_L$ = 15 $\Omega$		10	15	ns		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D\cong$ 1 A, $V_{GEN}$ = 10 V, $R_g$ = 6 $\Omega$		40	60			
Fall Time	t <sub>f</sub>			12	20			
Source-Drain Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 1.7 A, dl/dt = 100 A/μs		45	70			

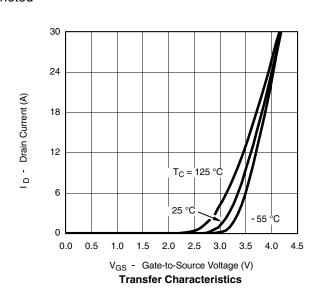
#### Notes:

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



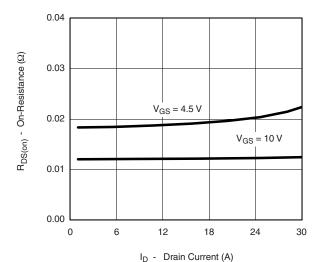




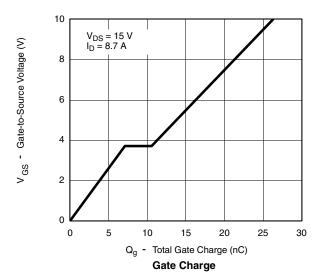


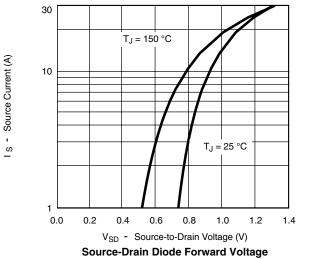


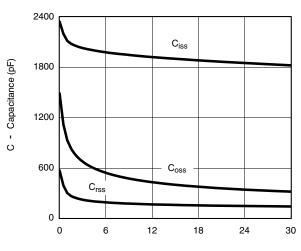
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



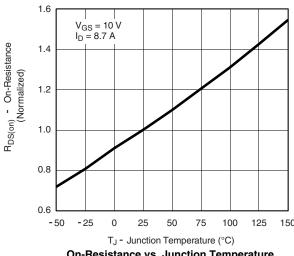
#### On-Resistance vs. Drain Current



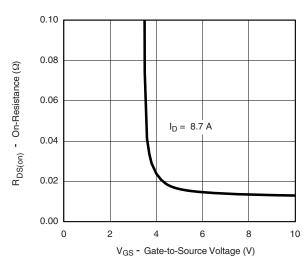




V<sub>DS</sub> - Drain-to-Source Voltage (V) Capacitance



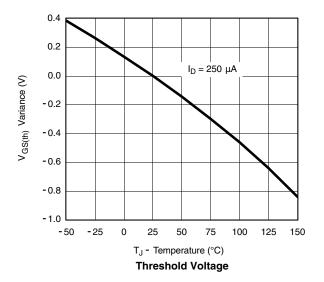
On-Resistance vs. Junction Temperature

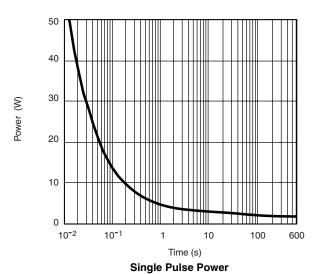


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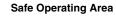
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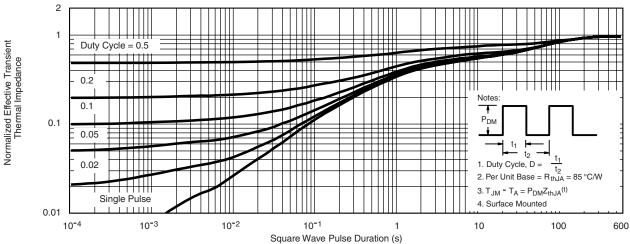
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





100 Limited by R<sub>DS(on)</sub> 10 I<sub>D</sub> - Drain Current (A) P(t) = 0.001P(t) = 0.01P(t) = 0.1 $T_A = 25 \, ^{\circ}C$ 0.1 Single Pulse P(t) = 10dc ## **BVDSS Limited** 0.01 0.1 100 V<sub>DS</sub> - Drain-to-Source Voltage (V) \*  $V_{GS}$  > minimum  $V_{GS}$  at which  $R_{DS(on)}$  is specified

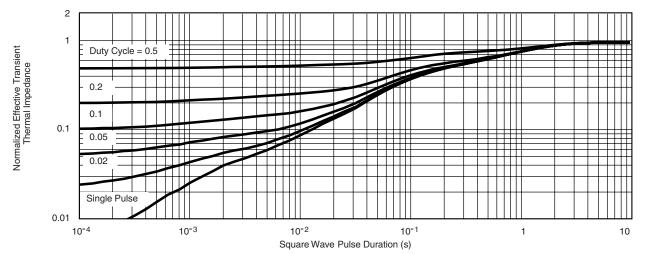




Normalized Thermal Transient Impedance, Junction-to-Ambient



#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

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