



Dual P-Channel 25-V (G-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A)			
- 30	0.023 at V _{GS} = - 10 V	- 7.6			
- 30	0.029 at V _{GS} = - 6 V	- 6.8			

FEATURES

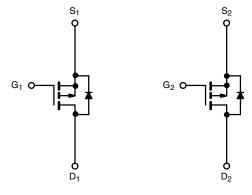
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET
- 25 V V_{GS} Provides Extra Head Room for Safe Operation
- Compliant to RoHS Directive 2002/95/EC





APPLICATIONS

- Notebook
 - Load Switch
 - Battery Charger Switch





P-Channel MOSFET

	SO-8		
S ₁ 1 G ₁ 2		8	D ₁
S ₂ 3		6	D ₂
G ₂ 4	Top View	5	D ₂

Ordering Information: Si4973DY-T1-E3 (Lead (Pb)-free)

Si4973DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

Parameter		Symbol	10 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	- 30		V
Gate-Source Voltage		V _{GS}	± 25		V
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 25 °C	- I _D	- 7.6	- 5.8	^
	T _A = 70 °C		- 6.1	- 4.6	
Pulsed Drain Current		I _{DM}	- 30		Α
Continuous Source Current (Diode Conduction) ^a		I _S	- 1.7 - 0.9		
	T _A = 25 °C	P _D	2.0	1.1	W
Maximum Power Dissipation ^a	T _A = 70 °C		1.3	0.7	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Mariana la Ambianta	t ≤ 10 s	R_{thJA}	45	62.5		
Maximum Junction-to-Ambient ^a	Steady State	' 'thJA	85	110	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	26	35		

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

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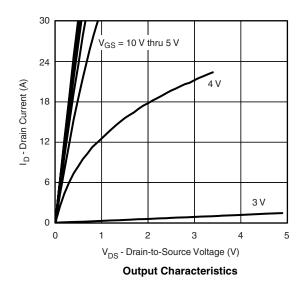
Parameter	Symbol	Test Conditions Min. Typ.			Max.	Unit	
Static					l .		
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1		- 3	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100		
		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 25 \text{ V}$			± 200	nA	
Zero Gate Voltage Drain Current		$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$	- 1		- 1		
	I _{DSS}	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 \text{ °C}$			- 25	μΑ	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = - 5 V, V _{GS} = - 10 V	- 30			Α	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 10 V, I _D = - 7.6 A		0.018	0.023	0	
		V _{GS} = - 6 V, I _D = - 6.8 A		0.023	0.029	Ω	
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 10 V, I _D = - 7.6 A		22		S	
Diode Forward Voltage ^a	V_{SD}	I _S = - 1.7 A, V _{GS} = 0 V		- 0.8	- 1.2	V	
Dynamic ^b							
Total Gate Charge	Q_g			37	56		
Gate-Source Charge	Q_{gs} $V_{DS} = -15 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -7.6 \text{ A}$		6		nC		
Gate-Drain Charge	Q_{gd}			11		1	
Turn-On Delay Time	t _{d(on)}			10	15		
Rise Time	t _r	V_{DD} = - 15 V, R_L = 15 Ω		15	25		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ - 1 A, V_{GEN} = - 10 V, R_g = 6 Ω		115	180	ns	
Fall Time	t _f			90	140		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 1.7 A, dl/dt = 100 A/μs		80	120		

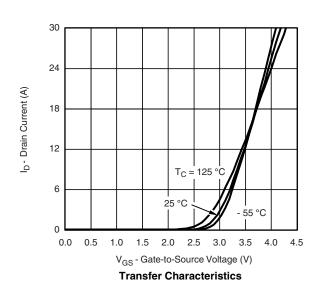
Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted







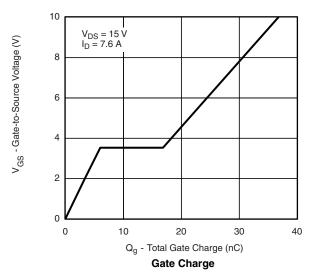


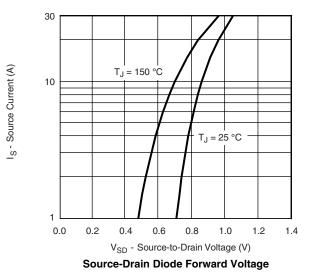


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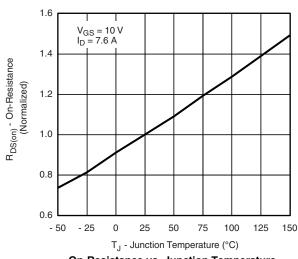
On-Resistance vs. Drain Current



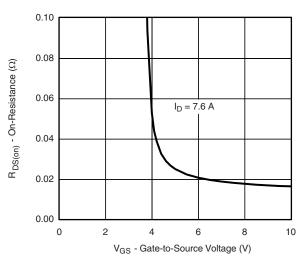


2500 2000 C - Capacitance (pF) 1500 1000 $\mathsf{C}_{\mathsf{oss}}$ 500 C_{rss} 0 5 0 10 15 20 25 30

V_{DS} - Drain-to-Source Voltage (V) **Capacitance**



On-Resistance vs. Junction Temperature

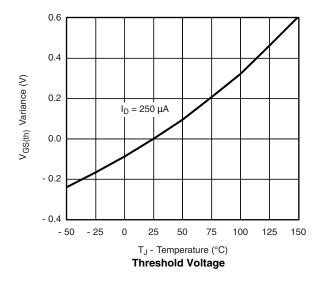


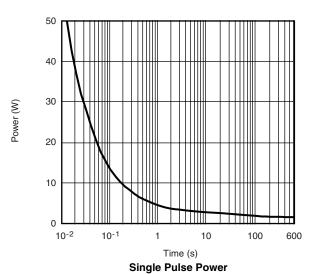
On-Resistance vs. Gate-to-Source Voltage

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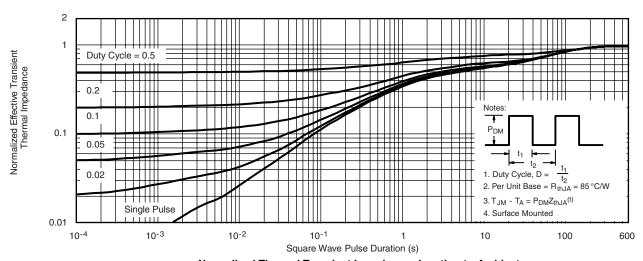
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





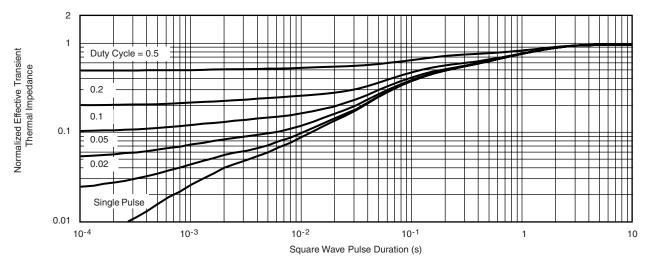
Safe Operating Area, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Ambient



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

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