

Vishay Siliconix

## P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	<b>R<sub>DS(on)</sub> (</b> Ω)	I <sub>D</sub> (A)			
- 30	0.053 at V <sub>GS</sub> = - 10 V	- 4.0			
	0.086 at V <sub>GS</sub> = - 4.5 V	- 3.1			

#### **FEATURES**

- Halogen-free According to IEC 61249-2-21
  Available
- TrenchFET<sup>®</sup> Power MOSFET

#### **APPLICATIONS**

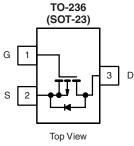
- Load Switch
- PA Switch



COMPLIANT

HALOGEN

vailable



Si2343DS (F3)\* \* Marking Code

Ordering Information: Si2343DS-T1

Si2343DS-T1-E3 (Lead (Pb)-free)

Si2343DS-T1-GE3 (Lead (Pb)-free and Halogen-free)

<b>ABSOLUTE MAXIMUM RATINGS</b> T	<sub>A</sub> = 25 °C, unle	ss otherwise r	noted			
Parameter		Symbol	5 s	Steady State	Unit	
Drain-Source Voltage		V <sub>DS</sub>	- 30		V	
Gate-Source Voltage		V <sub>GS</sub>	± 20			
	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	- 4.0	- 3.1		
Continuous Drain Current (T <sub>J</sub> = 150 °C) <sup>a, b</sup>	T <sub>A</sub> = 70 °C		- 3.2	- 2.5		
Pulsed Drain Current		I <sub>DM</sub>	- 15		A	
Continuous Source Current (Diode Conduction) <sup>a, b</sup>		۱ <sub>S</sub>	- 1.0	- 0.6		
	T <sub>A</sub> = 25 °C	– P <sub>D</sub>	1.25	0.75	W	
Maximum Power Dissipation <sup>a, b</sup>	T <sub>A</sub> = 70 °C		0.8	0.48		
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
	t ≤ 5 s	- R <sub>thJA</sub> R <sub>thJF</sub>	75	100	°C/W	
Maximum Junction-to-Ambient <sup>a</sup>	Steady State		120	166		
Maximum Junction-to-Foot (Drain)	Steady State		40	50		

Notes:

a. Surface Mounted on 1" x 1" FR4 board.

b. Pulse width limited by maximum junction temperature.

\* Pb containing terminations are not RoHS compliant, exemptions may apply

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			Limits				
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0 V, I_D = -250 \mu A$				v	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_D = -250 \ \mu A$	- 1		- 3	v	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zero Gate Voltage Drain Current		$V_{DS} = -24 V, V_{GS} = 0 V$			- 1	μA	
	IDSS	$V_{DS}$ = - 24 V, $V_{GS}$ = 0 V, $T_{J}$ = 55 °C			- 10		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS}$ $\leq$ - 5 V, $V_{GS}$ = - 10 V	- 15			А	
Drain-Source On-Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 4.0 A	0.043 0.0		0.053	0	
		$V_{GS} = -4.5 \text{ V}, I_D = -3.1 \text{ A}$		0.068	0.086	Ω	
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	$V_{DS} = -5 V, I_{D} = -4.0 A$		10		S	
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 1.0 A, V <sub>GS</sub> = 0 V		- 0.7	- 1.2	V	
Dynamic <sup>b</sup>							
Total Gate Charge	Qg			14	21	nC	
Gate-Source Charge	Q <sub>gs</sub>	V <sub>DS</sub> = - 15 V, V <sub>GS</sub> = - 10 V I <sub>D</sub> ≅ - 4.0 A		1.9			
Gate-Drain Charge	Q <sub>gd</sub>	D = -4.0 A		3.7			
Input Capacitance	C <sub>iss</sub>			540		1	
Output Capacitance	C <sub>oss</sub>	$V_{DS} = -15 \text{ V}, V_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$		131		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			105			
Switching <sup>c</sup>							
Turn-On Time	t <sub>d(on)</sub>			10	15		
	t <sub>r</sub>	$V_{DD} = -15$ V, R <sub>L</sub> = 15 Ω I <sub>D</sub> $\cong$ - 1.0 A, V <sub>GEN</sub> = -10 V		15	25	ns	
Turne Off Time	t <sub>d(off)</sub>	$I_D \cong -1.0 \text{ A}, \text{ V}_{\text{GEN}} = -10 \text{ V}$ $R_G = 6 \Omega$		31	50		
Turn-Off Time	t <sub>f</sub>	1.6 - 0.22		20	30		

Notes:

a. Pulse test: PW  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %.

b. For DESIGN AID ONLY, not subject to production testing.

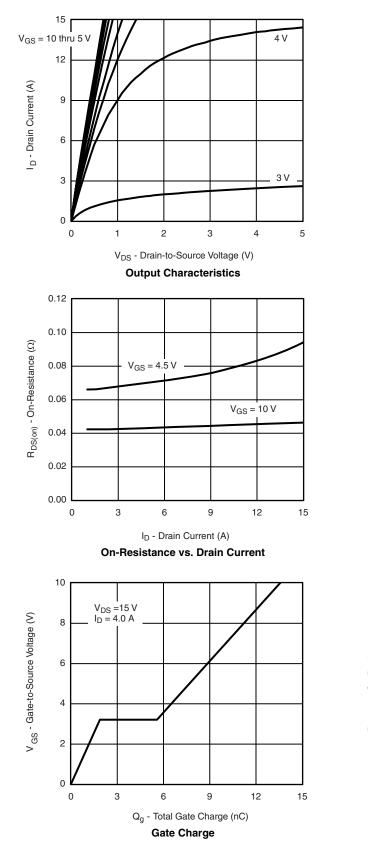
c. Switching time is essentially independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

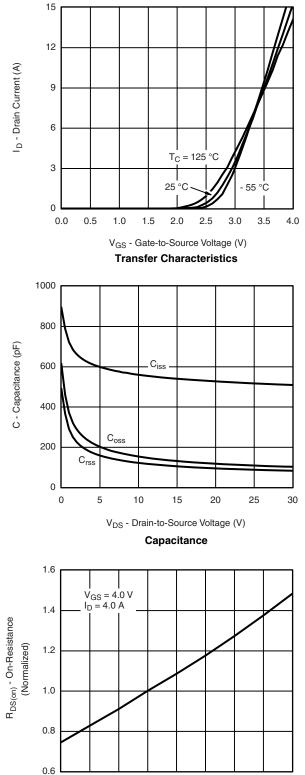


## Si2343DS

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#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



- 50

- 25

0

25

50

T<sub>J</sub> - Junction Temperature (°C)

**On-Resistance vs. Junction Temperature** 

75

100

125

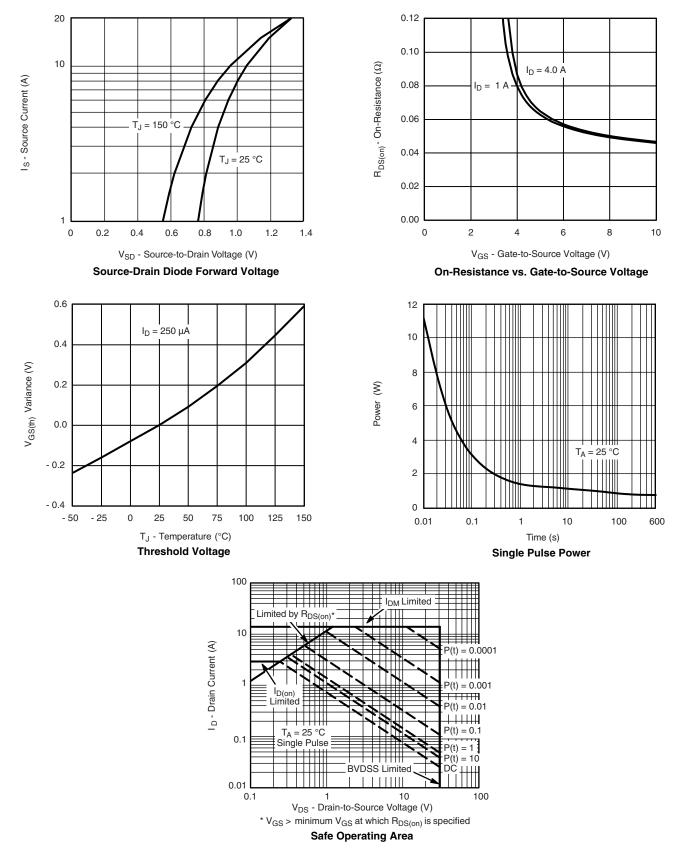
150

## Si2343DS

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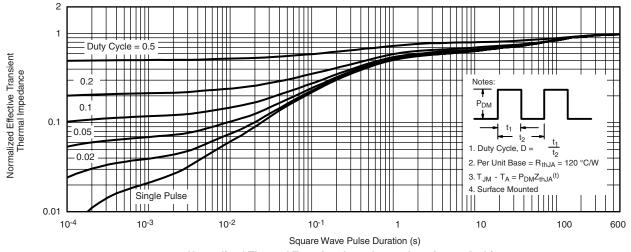
#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





Si2343DS Vishay Siliconix

#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <u>www.vishay.com/ppg?72079</u>.



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