International Rectifier

IRFL9110PbF

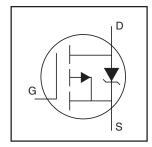
HEXFET® Power MOSFET

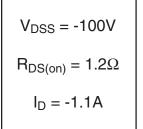
- Surface Mount
- Available in Tape & Reel
- Dynamic dv/dt Rating
- Repetitive Avalanche Rated
- P-Channel
- Fast Switching
- Ease of Paralleling
- Lead-Free

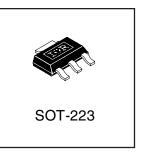


Third Generation HEXFETs from International Rectifier provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The SOT-223 package is designed for surface-mount using vapor phase, infra red, or wave soldering techniques. Its unique package design allows for easy automatic pick-and-place as with other SOT or SOIC packages but has the added advantage of improved thermal performance due to an enlarged tab for heatsinking. Power dissipation of grreater than 1.25W is possible in a typical surface mount application.







Absolute Maximum Ratings

	Parameter	Max.	Units	
I _D @ Tc = 25°C	Continuous Drain Current, V _{GS} @ -10 V	-1.1		
I _D @ Tc = 100°C	Continuous Drain Current, V _{GS} @ -10 V	-0.69		
I _{DM}	Pulsed Drain Current ①	-8.8	Α	
P _D @Tc = 25°C	Power Dissipation	3.1		
P _D @T _A = 25°C	Power Dissipation (PCB Mount)**	2.0	W	
	Linear Derating Factor	0.025		
	Linear Derating Factor (PCB Mount)**	0.017	W/°C	
V _{GS}	Gate-to-Source Voltage	-/+20	V	
E _{AS}	Single Pulse Avalanche Energy®	100	mJ	
I _{AR}	Avalanche Current①	-1.1	А	
E _{AR}	Repetitive Avalanche Energy①	0.31	mJ	
dv/dt	Peak Diode Recovery dv/dt ③	-5.5	V/ns	
T _J , T _{STG}	Junction and Storage Temperature Range	-55 to + 150	- ℃	
	Soldewring Temperature, for 10 seconds	300 (1.6mm from case)		

Thermal Resistance

	Parameter	Тур.	Max.	Units
$R_{\theta JC}$	Junction-to-PCB		40	°C/W
$R_{\theta JA}$	Junction-to-Ambient. (PCB Mount)**	_	60	- C/ VV

^{**} When mounted on 1" SQUARE pcb (FR-4 or G-10 Material).
For recommended footprint and soldering techniques refer to application note #AN-994.

Document Number: 91196

05/26/04 www.vishay.com



Electrical Characteristics @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Тур.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source Breakdown Voltage	-100			V	$V_{GS} = 0V, I_D = 250\mu A$
$\Delta V_{(BR)DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient		-0.091		V/°C	Reference to 25°C, I _D = 1mA
R _{DS(on)}	Static Drain-to-Source On-Resistance			1.2	Ω	$V_{GS} = -10V, I_D = 0.66A$ ④
V _{GS(th)}	Gate Threshold Voltage	-2.0		-4.0	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$
g _{fs}	Forward Transconductance	0.82			S	$V_{DS} = -50V, I_D = 0.66 A$ ④
I _{DSS}	Drain-to-Source Leakage Current			-100	μΑ	$V_{DS} = -100V, V_{GS} = 0V$
DSS	Brail to Gourde Leakage Guiterit			-500	μΑ	$V_{DS} = -80V, V_{GS} = 0V, T_{J} = 125^{\circ}C$
I _{GSS}	Gate-to-Source Forward Leakage			-100	nA	$V_{GS} = -20V$
'GSS	Gate-to-Source Reverse Leakage			100	IIA	V _{GS} = 20V
Qg	Total Gate Charge			8.7		$I_D = -4.0A$
Q _{gs}	Gate-to-Source Charge			2.2	nC	$V_{DS} = -80V$
Q_{gd}	Gate-to-Drain ("Miller") Charge			4.1		V_{GS} = -10V, See Fig. 6 and 13 \oplus
t _{d(on)}	Turn-On Delay Time		10			$V_{DD} = -50V$
t _r	Rise Time		27		ns	$I_D = -4.0A$
t _{d(off)}	Turn-Off Delay Time		15		115	$R_G = 24 \Omega$
t _f	Fall Time		17			$R_D = 11 \Omega$, See Fig. 10 @
L _D	Internal Drain Inductance		4.0		nH	Between lead, 6mm(0.25in) from package and center
L _S	Internal Source Inductance		6.0			of die contact.
C _{iss}	Input Capacitance		200			$V_{GS} = 0V$
Coss	Output Capacitance		94		pF	$V_{DS} = 25V$
C _{rss}	Reverse Transfer Capacitance		18			f = 1.0MHz, See Fig. 5

Source-Drain Ratings and Characteristics

	Parameter	Min.	Тур.	Max.	Units	Conditions
Is	Continuous Source Current					MOSFET symbol
	(Body Diode)		- -1.1	.1 A	showing the	
I _{SM}	Pulsed Source Current			0.0] ^	integral reverse G
	(Body Diode) ①			-8.8		p-n junction diode.
V _{SD}	Diode Forward Voltage			-5.5	V	$T_J = 25^{\circ}C$, $I_S = -1.1A$, $V_{GS} = 0V$ ④
t _{rr}	Reverse Recovery Time		80	160	ns	$T_J = 25^{\circ}C$, $I_F = -4.0A$
Q _{rr}	Reverse RecoveryCharge		0.15	0.30	μC	di/dt = 100A/μs @
t _{on}	Forward Turn-On Time	Intrinsic turn-on time is negligible (turn-on is dominated by L_S+L_D)				

Notes:

- ① Repetitive rating; pulse width limited by max. junction temperature. (See fig. 11)
- $\begin{tabular}{ll} @V_{DD=}$-25V, starting $T_J=25^\circ$C, $L=7.7$ mH $R_G=25\Omega$, $I_{AS}=-4.4A$. (See Figure 12) \\ \end{tabular}$
- 4 Pulse width \leq 300 μ s; duty cycle \leq 2%.

Document Number: 91196 www.vishay.com

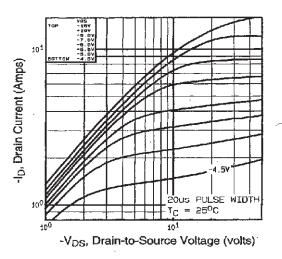


Fig 1. Typical Output Characteristics,

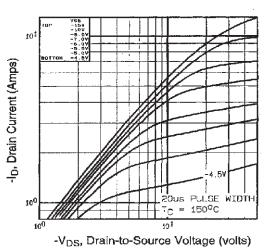


Fig 2. Typical Output Characteristics,

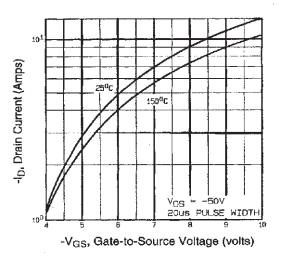


Fig 3. Typical Transfer Characteristics

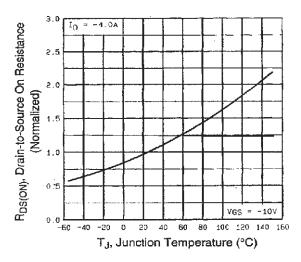


Fig 4. Normalized On-Resistance Vs. Temperature

Document Number: 91196

www.vishay.com

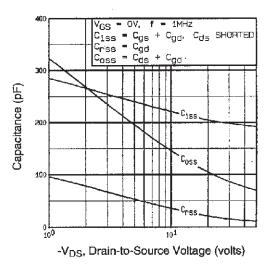


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

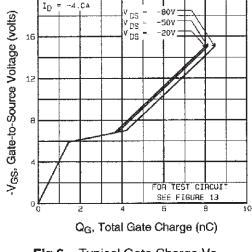


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

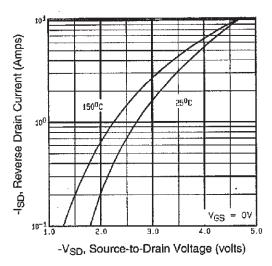


Fig 7. Typical Source-Drain Diode Forward Voltage

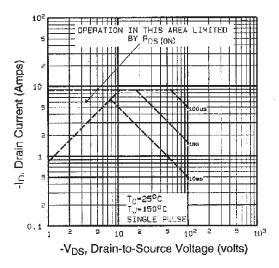


Fig 8. Maximum Safe Operating Area

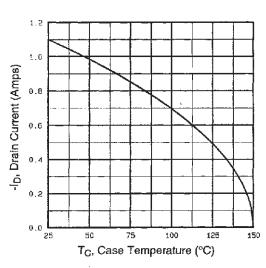


Fig 9. Maximum Drain Current Vs. Case Temperature

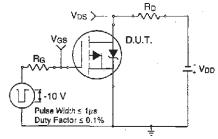


Fig 10a. Switching Time Test Circuit

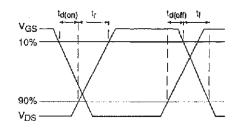


Fig 10b. Switching Time Waveforms

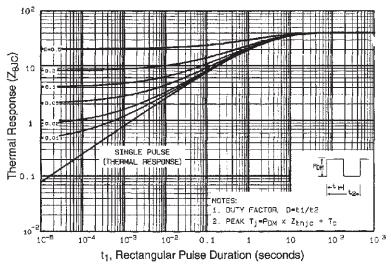


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

Document Number: 91196

www.vishay.com

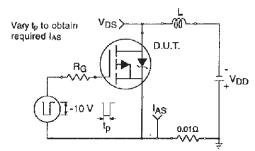


Fig 12a. Unclamped Inductive Test Circuit

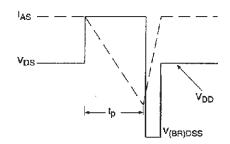


Fig 12b. Unclamped Inductive Waveforms

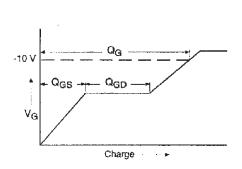


Fig 13a. Basic Gate Charge Waveform

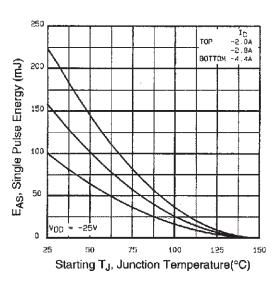


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

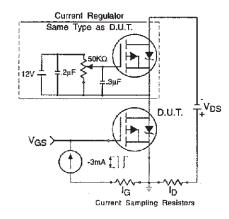
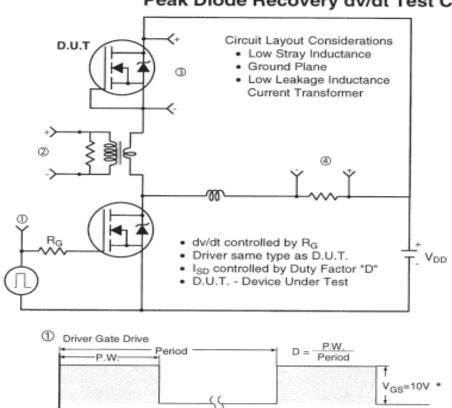


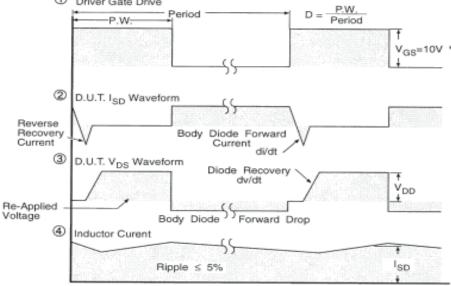
Fig 13b. Gate Charge Test Circuit

Document Number: 91196

www.vishay.com

Peak Diode Recovery dv/dt Test Circuit





* V_{GS} = 5V for Logic Level Devices

Fig 14. For N-Channel HEXFETS

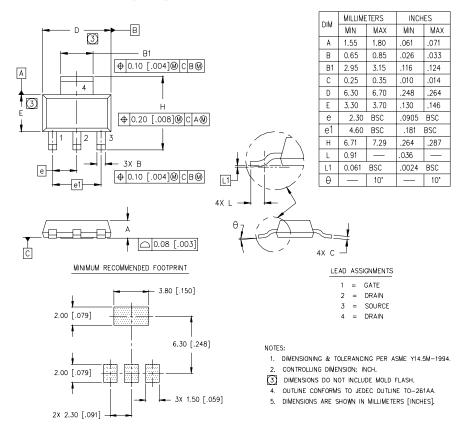
Document Number: 91196 www.vishay.com

International

TOR Rectifier

SOT-223 (TO-261AA) Package Outline

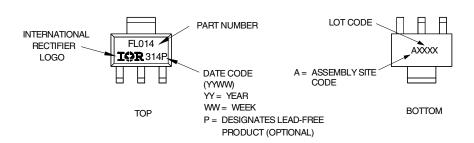
Dimensions are shown in milimeters (inches)



SOT-223 (TO-261AA) Part Marking Information

HEXFET PRODUCT MARKING

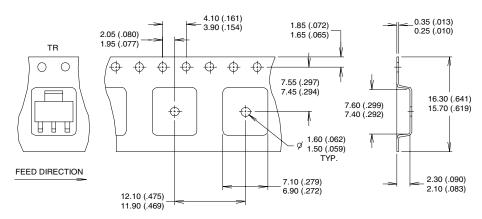
EXAMPLE: THIS IS AN IRFL014



Document Number: 91196 www.vishay.com

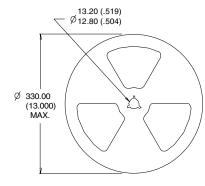
SOT-223 (TO-261AA) Tape & Reel Information

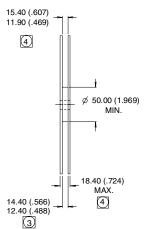
Dimensions are shown in milimeters (inches)



NOTES:

- 1. CONTROLLING DIMENSION: MILLIMETER.
- 2. OUTLINE CONFORMS TO EIA-481 & EIA-541.
- 3. EACH Ø330.00 (13.00) REEL CONTAINS 2,500 DEVICES.





NOTES:

- OUTLINE COMFORMS TO EIA-418-1
- 2. CONTROLLING DIMENSION: MILLIMETER..
- 3 DIMENSION MEASURED @ HUB.
- INCLUDES FLANGE DISTORTION @ OUTER EDGE.

Data and specifications subject to change without notice.



IR WORLD HEADQUARTERS: 233 Kansas St., El Segundo, California 90245, USA Tel: (310) 252-7105

TAC Fax: (310) 252-7903

05/04

Document Number: 91196 www.vishay.com



Vishay

Notice

The products described herein were acquired by Vishay Intertechnology, Inc., as part of its acquisition of International Rectifier's Power Control Systems (PCS) business, which closed in April 2007. Specifications of the products displayed herein are pending review by Vishay and are subject to the terms and conditions shown below.

Specifications of the products displayed herein are subject to change without notice. Vishay Intertechnology, Inc., or anyone on its behalf, assumes no responsibility or liability for any errors or inaccuracies.

Information contained herein is intended to provide a product description only. No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document. Except as provided in Vishay's terms and conditions of sale for such products, Vishay assumes no liability whatsoever, and disclaims any express or implied warranty, relating to sale and/or use of Vishay products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright, or other intellectual property right.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications. Customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Vishay for any damages resulting from such improper use or sale.

International Rectifier[®], IR[®], the IR logo, HEXFET[®], HEXSense[®], HEXDIP[®], DOL[®], INTERO[®], and POWIRTRAIN[®] are registered trademarks of International Rectifier Corporation in the U.S. and other countries. All other product names noted herein may be trademarks of their respective owners.

Document Number: 99901 www.vishay.com
Revision: 12-Mar-07 1