

ROHS COMPLIANT

HALOGEN

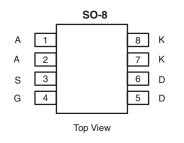
Available

Vishay Siliconix

N-Channel 30-V (D-S) MOSFET with Schottky Diode

MOSFET PRODUCT SUMMARY					
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A)	Q _g (Typ.)		
30 -	0.035 at V _{GS} = 10 V	7.4	4.2 nC		
	0.052 at V _{GS} = 4.5 V	6.1	4.2110		

SCHOTI	SCHOTTKY PRODUCT SUMMARY					
V _{KA} (V)	V _F (V) Diode Forward Voltage	I _F (A) ^a				
30	0.470 at 3 A	3				



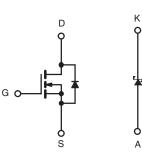
Ordering Information: Si4620DY-T1-E3 (Lead (Pb)-free) Si4620DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- LITTLE FOOT[®] Plus Power MOSFET
- Compliant to RoHS Directive 2002/95/EC

APPLICATIONS

- Load Switch for Portable Applications
 Ideal for Boost Circuits
- HDD Driver



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T _A = 25 ° Parameter	0, 0000 000	Symbol	Limit	Unit	
Drain-Source Voltage (MOSFET)	-	30	Onit		
5 ()	V _{DS}				
Reverse Voltage (Schottky)	V _{KA}	30	V		
Gate-Source Voltage (MOSFET)	V _{GS}	± 20			
	T _C = 25 °C		7.5		
Continuous Drain Current (T ₁ = 150 °C) (MOSFET)	T _C = 70 °C	I _D	6		
	T _A = 25 °C	U.	6		
	T _A = 70 °C		4.8		
Pulsed Drain Current (MOSFET)	•	I _{DM}	40	A	
Continuous Source Current (MOSEET Diade Conduction)	T _C = 25 °C	la la	2.6		
Continuous Source Current (MOSFET Diode Conduction)	T _A = 25 °C	I _S	1.7 ^{a, b}		
Average Forward Current (Schottky)	١ _F	3			
Pulsed Forward Current (Schottky)	I _{FM}	8			
	T _C = 25 °C		3.1		
Maximum Power Dissipation (MOSEET)	T _C = 70 °C		2		
Maximum Power Dissipation (MOSFET)	T _A = 25 °C		2 ^{a, b}		
	T _A = 70 °C	PD	1.3 ^{a, b}	w	
	T _C = 25 °C	'D	3	vv	
Maximum Dawar Dissination (Schottlar)	T _C = 70 °C		1.9		
Maximum Power Dissipation (Schottky)	T _A = 25 °C		1.8		
	T _A = 70 °C	F	1.1		
Operating Junction and Storage Temperature Range	T _J , T _{stg}	- 55 to 150			
Soldering Recommendations (Peak Temperature)		260			

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THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient (MOSFET) ^{a, c}	R _{thJA}	53	62.5			
Maximum Junction-to-Foot (Drain) (MOSFET)	R _{thJF}	30	40	°C 44/		
Maximum Junction-to-Ambient (Schottky)	R _{thJA}	55	65	°C/W		
Maximum Junction-to-Foot (Drain) (Schottky)	R _{thJF}	32	42			

Notes:

a. Surface Mounted on FR4 board.

b. $t \leq$ 10 s.

c. Maximum under Steady State conditions for MOSFETS is 110 $^\circ\text{C/W}.$

d. Maximum under Steady State conditions for Schottky is 115 °C/W.

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static			•		•		
Drain-Source Breakdown Voltage	V _{DS}	V_{GS} = 0 V, I_D = 250 μ A	30			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS/TJ}$	I _D = 250 μA		32.5		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)/TJ}$	$I_D = 250 \mu A$		- 5.3			
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	1.2		2.5	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zara Cata Valtaga Drain Current		$V_{DS} = 30 \text{ V}, V_{GS} = 0 \text{ V}$			1		
Zero Gate Voltage Drain Current	IDSS	V_{DS} = 30 V, V_{GS} = 0 V, T_{J} = 55 °C			10	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le 5 \text{ V}, \text{ V}_{GS} = 10 \text{ V}$	30			А	
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 6 \text{ A}$		0.028	0.035	Ω	
		$V_{GS} = 4.5 \text{ V}, I_D = 4.9 \text{ A}$		0.041	0.052		
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 6 \text{ A}$		12		S	
Dynamic ^b				1			
Input Capacitance	C _{iss}			520	1040	pF	
Output Capacitance	C _{oss}	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$		115	230		
Reverse Transfer Capacitance	C _{rss}			55	110		
Tatal Cata Charma	Qg	$V_{DS} = 15 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 6 \text{ A}$		8.6	13		
Total Gate Charge				4.2	6.5	nC	
Gate-Source Charge	Q _{gs}	$V_{DS} = 15 \text{ V}, \text{ V}_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 6 \text{ A}$		1.8			
Gate-Drain Charge	Q _{gd}			1.5			
Gate Resistance	Rg	f = 1 MHz		2.8		Ω	
Turn-On Delay Time	t _{d(on)}			16	30		
Rise Time	t _r	V_{DD} = 15 V, R_L = 3.1 Ω		36	54		
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong$ 4.8 A, V_GEN = 4.5 V, R_g = 6 Ω		21	40		
Fall Time	t _f			17	40		



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SPECIFICATIONS T _J = 25 °C, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Drain-Source Body Diode Characteristics							
Continuous Source-Drain Diode Current	۱ _S	T _C = 25 °C	2.6	А			
Pulse Diode Forward Current	I _{SM}				40	A	
Body Diode Voltage	V _{SD}	I _S = 1.7 A, V _{GS} = 0 V		0.8	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}	I _F = 1.7 A, dl/dt = 100 A/μs, T _J = 25 °C		20	40	ns	
Body Diode Reverse Recovery Charge	Q _{rr}			14	30	nC	
Reverse Recovery Fall Time	t _a			14		ns	
Reverse Recovery Rise Time	t _b			6			

Notes:

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

SCHOTTKY SPECIFICATIONS $T_J = 25 \text{ °C}$, unless otherwise noted							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Forward Valtage Drep	V _F	I _F = 3 A		0.39	0.470	- V mA	
Forward Voltage Drop		I _F = 3 A, T _J = 125 °C		0.35	0.420		
	I _{rm}	$V_r = 5 V$		0.1	0.2		
		$V_r = 5 V, T_J = 85 °C$		3.5	17.5		
Mauimum Davana Laskana Oumant		V _r = 5 V, T _J = 106 °C		12	60		
Maximum Reverse Leakage Current		V _r = 30 V		0.22	0.5		
		$V_{r} = 30 \text{ V}, \text{ T}_{J} = 85 ^{\circ}\text{C}$		10	50		
		$V_{r} = 30 \text{ V}, \text{ T}_{J} = 125 ^{\circ}\text{C}$		40	200		
Junction Capacitance	CT	V _r = 15 V		100		pF	

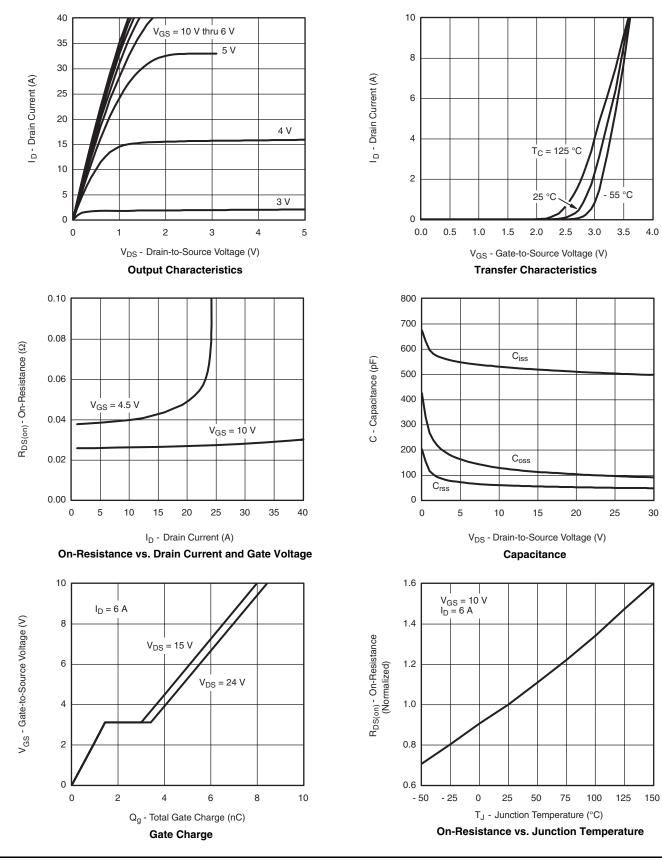
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Si4620DY

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



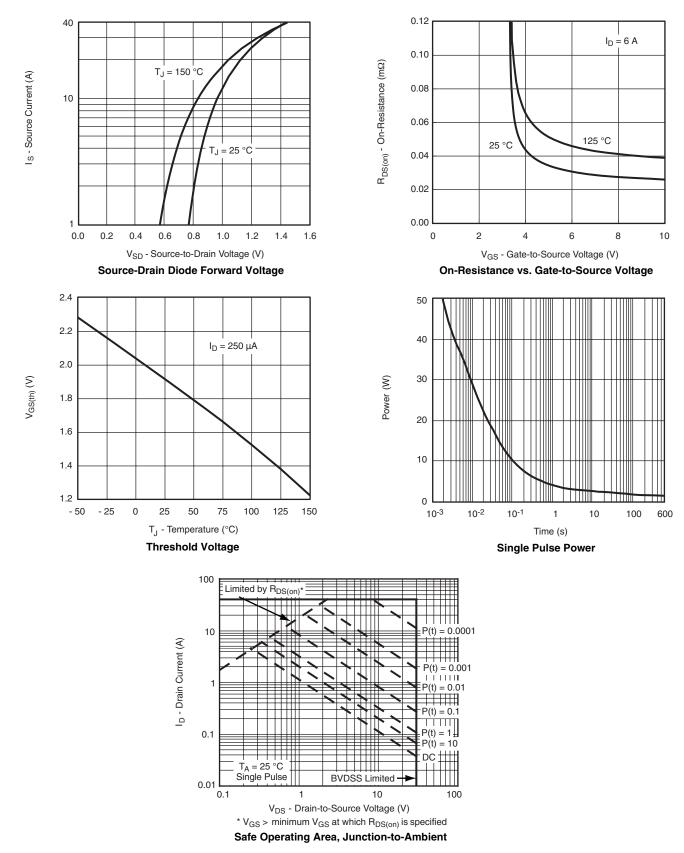
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



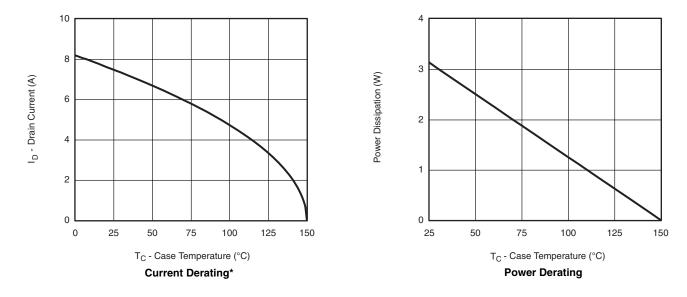
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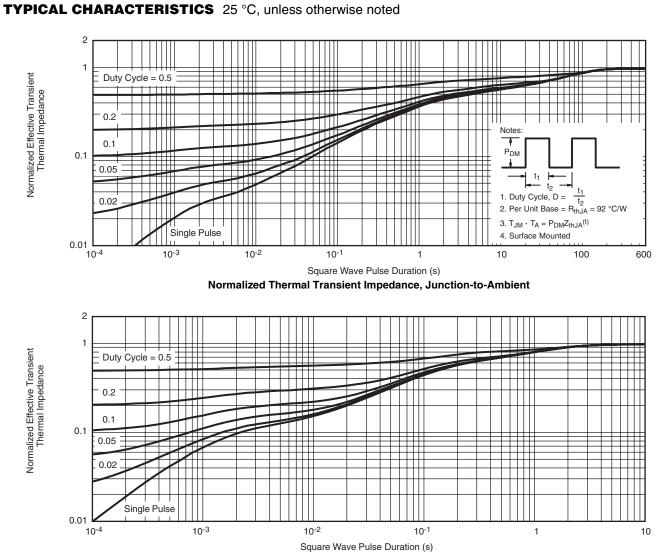
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.



Normalized Thermal Transient Impedance, Junction-to-Foot

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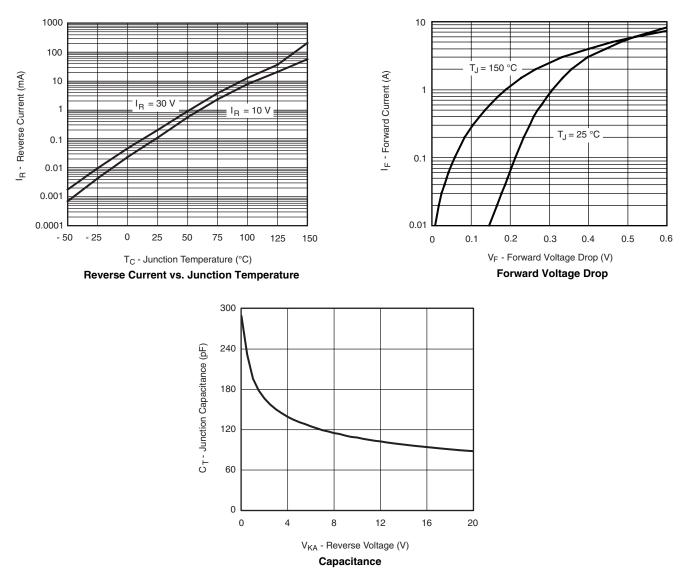
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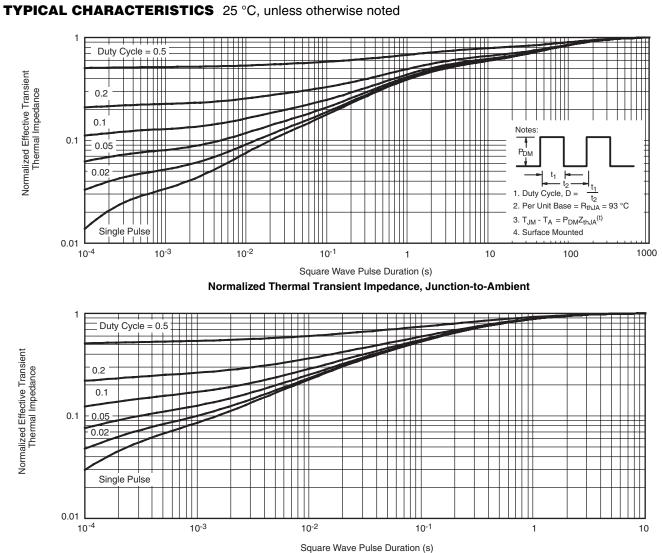
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





Normalized Thermal Transient Impedance, Junction-to-Foot

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