TOSHIBA Multichip Discrete Device

HN7G05FU

Power Management Switch Applications, Inverter Circuit Applications, Driver Circuit Applications and Interface Circuit Applications

Q1 (transistor): RN2301 equivalent

Q2 (MOSFET): 2SK1830 equivalent

Q1 (Transistor) Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	V _{CBO}	-50	V
Collector-emitter voltage	V _{CEO}	-50	V
Emitter-base voltage	V _{EBO}	-10	V
Collector current	Ι _C	-100	mA

Q2 (MOSFET) Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Drain-source voltage	V _{DS}	20	V
Gate-source voltage	V _{GSS}	10	V
Drain current	I _D	50	mA

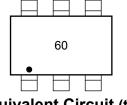
Q1, Q2 Common Ratings (Ta = 25°C)

Note 1: Total rating

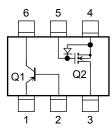
Characteristic	Symbol	Rating	Unit
Power dissipation	P (Note 1)	200	mW
Junction temperature	Тј	150	°C
Storage temperature range	T _{stg}	-55~150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



Equivalent Circuit (top view)



Unit: mm

15±0.05

2.1±0.1 1.25±0.1

0~0.1

2-2J1E

EMITTER

2. BASE

DRAIN

GATE COLLECTOR

SOURCE

1.

3.

4. 5.

6.

Weight: 0.0068 g (typ.)

0.65

2.0±0.2 1.3±0.1

US6

JEDEC

JEITA

Marking

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Q1 (Transistor) Electrical Characteristics (Ta = 25°C)

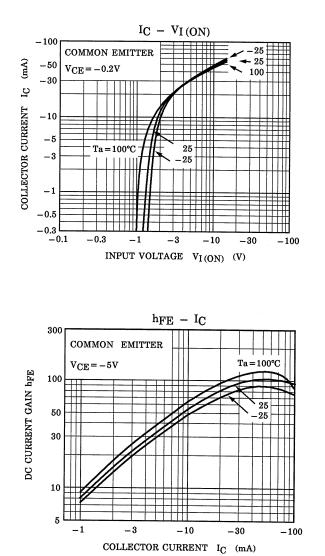
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cutoff current	I _{CBO}	$V_{CB} = -50 \text{ V}, \text{ I}_{E} = 0$		_	-100	nA
	ICEO	$V_{CE} = -50 \text{ V}, I_E = 0$	_	_	-500	nA
Emitter cutoff current	I _{EBO}	$V_{EB} = -5 \text{ V}, \text{ I}_{C} = 0$	-0.82	_	-1.52	mA
DC current gain	h _{FE}	$V_{CE} = -5 \text{ V}, \text{ I}_{C} = -10 \text{ mA}$	30	_	—	
Collector-emitter saturation voltage	V _{CE (sat)}	$I_C = -5$ mA, $I_B = -0.25$ mA	_	-0.1	-0.3	V
Input voltage (ON)	V _{I(ON)}	$V_{CE} = -0.2 \text{ V}, \text{ I}_{C} = -5 \text{ mA}$	-1.1	_	-2.0	V
Input voltage (OFF)	V _{I(OFF)}	$V_{CE} = -5 \text{ V}, \text{ I}_{C} = -0.1 \text{ mA}$	-1.0	_	-1.5	V
Input resistor	R1	—	3.29	4.7	6.11	kΩ
Resistor ratio	R1/R2	—	0.9	1.0	1.1	

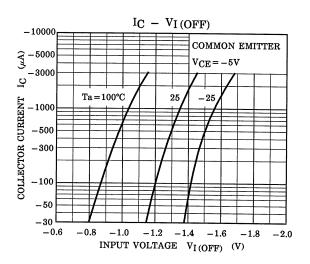
Q2 (MOSFET) Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I _{GSS}	$V_{GS}=10~V,~V_{DS}=0$	_	_	1	μA
Drain-source breakdown voltage	V (BR) DSS	$I_D = 100 \ \mu A, \ V_{GS} = 0$	20	_	_	V
Drain cutoff current	I _{DSS}	$V_{DS} = 20 V, V_{GS} = 0$	_	_	1	μA
Gate threshold voltage	V _{th}	$V_{DS} = 3 \text{ V}, \text{ I}_{D} = 0.1 \text{ mA}$	0.5	_	1.5	V
Forward transfer admittance	Y _{fs}	$V_{DS} = 3 \text{ V}, \text{ I}_{D} = 10 \text{ mA}$	20	_	_	mS
Drain-source ON-resistance	R _{DS (ON)}	$I_D = 10 \text{ mA V}_{GS} = 2.5 \text{ V}$	_	20	40	Ω

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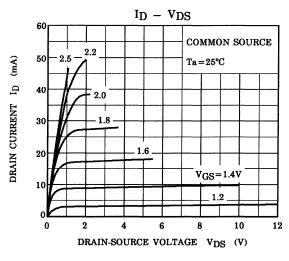
Q1 (Transistor)

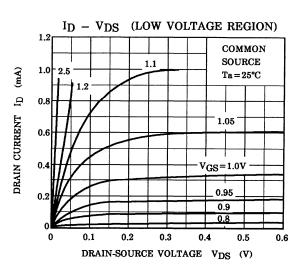


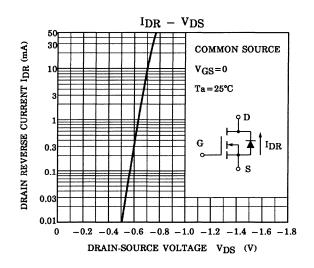


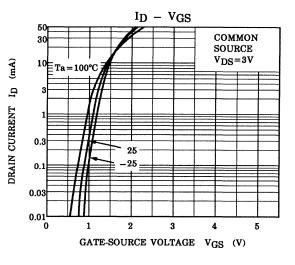
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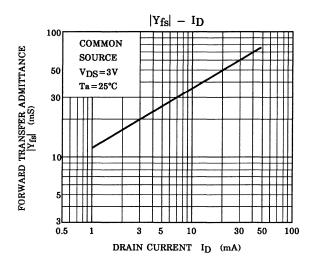
Q2 (S-MOS)

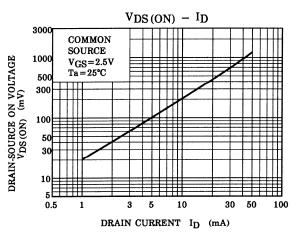






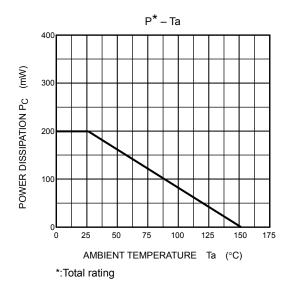






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Q1, Q2 common



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