## International I $\boldsymbol{O} \boldsymbol{R}$ Rectifier

## 50WQ10FNPbF

SCHOTTKY RECTIFIER

$$
\begin{aligned}
\mathrm{I}_{\mathrm{F}(\mathrm{AV})} & =5.5 \mathrm{Amp} \\
\mathrm{~V}_{\mathrm{R}} & =100 \mathrm{~V}
\end{aligned}
$$

| Characteristics | Values | Units |
| :---: | :---: | :---: |
| $I_{\text {F(AV) }}$ Rectangular waveform | 5.5 | A |
| $\mathrm{V}_{\text {RRM }}$ | 100 | V |
| $\mathrm{I}_{\text {FSM }}$ @tp $=5 \mu \mathrm{~s}$ sine | 330 | A |
| $V_{F} @$ ¢ ${ }^{\text {Apk, }} \mathrm{T}_{\mathrm{J}}=125^{\circ} \mathrm{C}$ | 0.63 | V |
| $T_{J} \quad$ range | -40 to 150 | ${ }^{\circ} \mathrm{C}$ |

## Description/ Features

The 50WQ10FNPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, free-wheeling diodes, battery charging, and reverse battery protection.

- Popular D-PAK outline
- Small foot print, surface moutable
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Lead-Free ("PbF" suffix)
Case Styles

Voltage Ratings

| Part number | 50WQ10FNPbF |
| :---: | :---: |
| $\mathrm{V}_{\mathrm{R}}$ Max. DC Reverse Voltage $(\mathrm{V})$ | 100 |
| $\mathrm{~V}_{\mathrm{RWM}}$ Max. Working Peak Reverse Voltage $(\mathrm{V})$ |  |

## Absolute Maximum Ratings

|  | Parameters | 50WQ... | Units | Conditions |  |
| :---: | :---: | :---: | :---: | :---: | :---: |
| $I_{\text {F(AV) }}$ | Max. Average Forward Current *See Fig. 5 | 5.5 | A | $50 \%$ duty cycle @ $\mathrm{T}_{\mathrm{C}}=135^{\circ} \mathrm{C}$, rectangular wave form |  |
| FSM | Max. Peak One Cycle Non-Repetitive | 330 | A | $5 \mu \mathrm{~s}$ Sine or $3 \mu \mathrm{~s}$ Rect. pulse | Following any rated load condition and with rated $\mathrm{V}_{\text {RRM }}$ applied |
|  | Surge Current *See Fig. 7 | 110 |  | 10 ms Sine or 6ms Rect. pulse |  |
| $\mathrm{E}_{\text {AS }}$ | Non-Repetitive Avalanche Energy | 6.0 | mJ | $\mathrm{T}_{\mathrm{J}}=25^{\circ} \mathrm{C}, \mathrm{I}_{\text {AS }}=0.5 \mathrm{Amps}, \mathrm{L}=40 \mathrm{mH}$ |  |
| $\mathrm{I}_{\text {AR }}$ | Repetitive Avalanche Current | 0.5 | A | Current decaying linearly to zero in $1 \mu \mathrm{sec}$ Frequency limited by $\mathrm{T}_{\mathrm{J}}$ max. $\mathrm{V}_{\mathrm{A}}=1.5 \mathrm{xV}_{\mathrm{R}}$ typical |  |

## Electrical Specifications

| Parameters | 50WQ... | Units | Conditions |  |
| :---: | :---: | :---: | :---: | :---: |
| $V_{\text {FM }}$ Max. Forward Voltage Drop | 0.77 | v | @ 5A | $\mathrm{T}_{\mathrm{j}}=25^{\circ} \mathrm{C}$ |
| * See Fig. 1 | 0.91 | V | @ 10A |  |
|  | 0.63 | V | @ 5A | $\mathrm{T}_{\mathrm{J}}=125^{\circ} \mathrm{C}$ |
|  | 0.74 | V | @ 10A |  |
| $\mathrm{I}_{\text {RM }} \quad$ Max. Reverse Leakage Current | 1 | mA | $\mathrm{T}_{\mathrm{J}}=25^{\circ} \mathrm{C}$ | $\mathrm{V}_{\mathrm{R}}=$ rated $\mathrm{V}_{\mathrm{R}}$ |
| * See Fig. 2 | 4 | mA | $\mathrm{T}_{\mathrm{J}}=125^{\circ} \mathrm{C}$ |  |
| $\mathrm{V}_{\text {F(T)) }}$ Threshold Voltage | 0.47 | V | $\mathrm{T}_{\mathrm{J}}=\mathrm{T}_{\mathrm{j}}$ max. |  |
| $\mathrm{r}_{\mathrm{t}} \quad$ Forward Slope Resistance | 21.46 | $\mathrm{m} \Omega$ |  |  |  |
| $\mathrm{C}_{\mathrm{T}}$ Typical Junction Capacitance | 183 | pF | $\mathrm{V}_{\mathrm{R}}=5 \mathrm{~V}_{\mathrm{DC}}$ (test signal range 100 Khz to 1 Mhz ) $25^{\circ} \mathrm{C}$ |  |
| $\mathrm{L}_{\text {s }}$ Typical Series Inductance | 5.0 | nH | Measured lead to lead 5 mm from package body |  |

(1) Pulse Width < 300 $\mu \mathrm{s}$, Duty Cycle $<2 \%$

## Thermal-Mechanical Specifications

|  | Parameters | 50WQ... | Units | Conditions |
| :---: | :---: | :---: | :---: | :---: |
|  | Max. JunctionTemperature Range(*) | -40 to 150 | ${ }^{\circ} \mathrm{C}$ |  |
| $\mathrm{T}_{\text {stg }}$ | Max. Storage Temperature Range | -40 to 150 | ${ }^{\circ} \mathrm{C}$ |  |
| $\mathrm{R}_{\text {thuc }}$ | Max. Thermal Resistance Junction to Case | 3.0 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ | DC operation *See Fig. 4 |
|  | Approximate Weight | 0.3 (0.01) | g(oz.) |  |
|  | Case Style | D-PAK |  | Similar to TO-252AA |
|  | Device Marking | 50WQ10FN |  |  |

${ }^{(*)} \frac{\mathrm{dPtot}}{\mathrm{dTj}}<\frac{1}{\mathrm{Rth}(\mathrm{j}-\mathrm{a})}$ thermal runaway condition for a diode on its own heatsink


Fig. 1-Maximum Forward Voltage Drop Characteristics


Fig. 2-Typical Values of Reverse Current Vs. Reverse Voltage


Fig. 3-Typical Junction Capacitance Vs. Reverse Voltage


Fig. 4-Maximum Thermal Impedance $\mathrm{Z}_{\text {thJc }}$ Characteristics

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Bulletin PD-21095 rev. B 08/06


Fig. 5-Maximum Allowable Case Temperature Vs. Average Forward Current

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Fig. 6-Forward Power Loss Characteristics


Fig. 7 -Maximum Non-Repetitive Surge Current
(2) Formula used: $T_{C}=T_{J}-\left(P d+P d_{R E V}\right) \times R_{\text {thJC }}$;
$P d=$ Forward PowerLoss $=I_{F(A V)} \times V_{F M} @\left(I_{F(A V)} / D\right.$ ) (seeFig. 6);
$\mathrm{Pd}_{\mathrm{REV}}=$ Inverse Power Loss $=\mathrm{V}_{\mathrm{R} 1} \mathrm{x} \mathrm{I}_{\mathrm{R}}(1-\mathrm{D}) ; \mathrm{I}_{\mathrm{R}} @ \mathrm{~V}_{\mathrm{R} 1}=80 \%$ rated $\mathrm{V}_{\mathrm{R}}$

Outline Table


Part Marking Information

| EXAMPLE: | THIS IS A 50WQ10FN <br> LOT CODE 8024 <br> ASSEMBLED ON WW 02, 2003 |  |
| :---: | :---: | :---: |

Tape \& Reel Information


Ordering Information Table


Data and specifications subject to change without notice. This product has been designed and qualified for AEC Q101 Level and Lead-Free. Qualification Standards can be found on IR's Web site.

## International <br> ISR Rectifier

IR WORLD HEADQUARTERS: 233 Kansas St., El Segundo, California 90245, USA Tel: (310) 252-7105 TAC Fax: (310) 252-7309 08/06

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