Vishay Semiconductors

Schottky Rectifier, 10 A

Base cathode

Ó 1

Anode

Q 4, 2

D-PAK (TO-252AA)

10 A

45 V

0.57 V

15 mA at 125 °C

175 °C

Single die

20 mJ

ÓЗ

Anode



- Popular D-PAK outline
- Small foot print, surface mountable
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Compliant to RoHS Directive 2002/95/EC
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 $^\circ\text{C}$

DESCRIPTION

The VS-STPS1045BPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	10	A	
V _{RRM}		45	V	
I _{FSM}	t _p = 5 μs sine	390	А	
V _F	10 Apk, T _J = 125 °C	0.57	V	
TJ	Range	- 40 to 175	°C	

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-STPS1045BPbF	UNITS		
Maximum DC reverse voltage	V _R	45	V		
Maximum working peak reverse voltage	V _{RWM}	45	v		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T_C = 151 °C, rectangular waveform		10	
Maximum peak one cycle non-repetitive surge current See fig. 7		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	390	A
	IFSM	10 ms sine or 6 ms rect. pulse		75	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 3.0 A, L = 4.40 mH		20	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		3.0	А

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D-PAK (TO-252AA)

PRODUCT SUMMARY Package

> I_{F(AV)} V_R

V_F at I_F

 I_{RM}

T_{.1} max.

Diode variation

 E_{AS}

Vishay Semiconductors

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	10 A	T _J = 25 °C	0.63	V
Maximum forward voltage drop		20 A		0.84	
See fig. 1		10 A	• T _J = 125 °C	0.57	
		20 A		0.72	
Maximum reverse leakage current See fig. 2	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	0.2	mA
		T _J = 125 °C		15	
Typical junction capacitance	CT	$V_{\rm R}$ = 5 $V_{\rm DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		760	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		5.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 40 to 175	°C	
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	3.0	°C/W	
Maximum thermal resistance, junction to ambient	R _{thJA}		50	C/W	
Approximate weight			0.3	g	
			0.01	oz.	
Marking device		Case style D-PAK (similar to TO-252AA)	STPS	STPS1045B	

Note

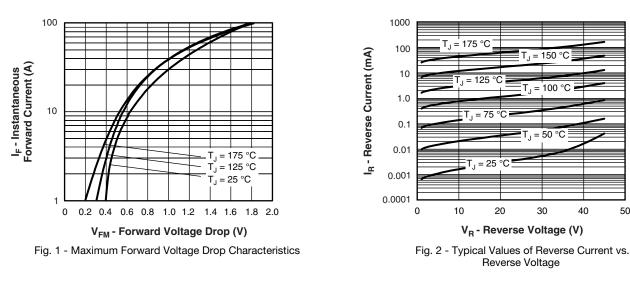
(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink



Schottky Rectifier, 10 A

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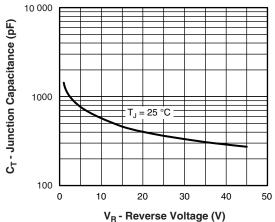


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

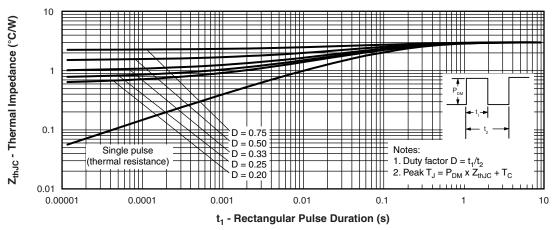


Fig. 4 - Maximum Thermal Impedance ZthJC Characteristics

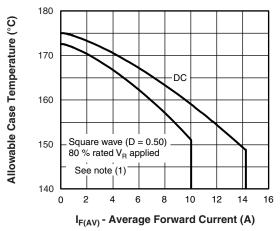
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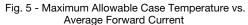
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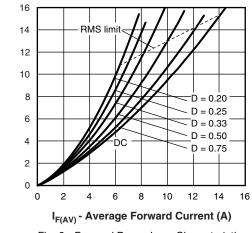
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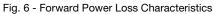
Average Power Loss (W)

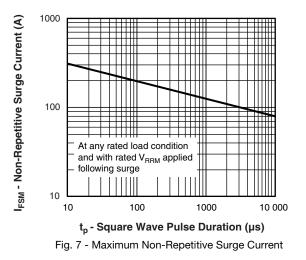












Note

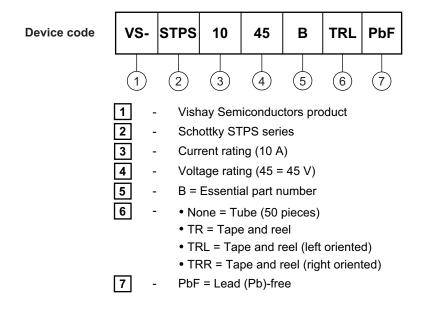
- (1)
- $\begin{array}{l} \mbox{Formula used: } T_C = T_J \mbox{ } (Pd + Pd_{REV}) \ x \ R_{thJC}; \\ Pd = \mbox{Forward power loss} = I_{F(AV)} \ x \ V_{FM} \ at \ (I_{F(AV)}/D) \ (see \ fig. \ 6); \\ Pd_{REV} = \ Inverse \ power \ loss = V_{R1} \ x \ I_R \ (1 \ D); \ I_R \ at \ V_{R1} = 80 \ \% \ rated \ V_R \end{array}$



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LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95016			
Part marking information	www.vishay.com/doc?95059			
Packaging information	www.vishay.com/doc?95033			



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