

MBRA120TRPbF

SCHOTTKY RECTIFIER

1.0 Amp

$$I_{F(AV)} = 1.0 Amp$$

 $V_R = 40 V$

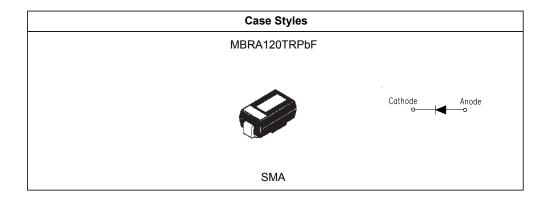
Major Ratings and Characteristics

Characte	eristics	Value	Units
I _{FAV} Rec	t. Waveform	1.0	А
V _{RRM}		20	V
I _{FSM} @tp	= 5 µs sine	310	А
V _F @1.	0Apk, T _J =125°C	0.34	V
T _J rang	ie .	- 65 to 150	°C

Description/ Features

The MBRA120TRPbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, free-wheeling diodes, battery charging, and reverse battery protection.

- Small foot print, surface mountable
- Low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Lead-Free ("PbF" suffix)



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MBRA120TRPbF

Bulletin PD-20776 07/04

International TOR Rectifier

Voltage Ratings

Part number	MBRA120TR
V _R Max. DC Reverse Voltage (V)	
V _{RWM} Max. Working Peak Reverse Voltage (V)	20

Absolute Maximum Ratings

	Parameters	Value	Units	Conditions	
I _{F(AV)}	Max. Average Forward Current	1.0	Α	50% duty cycle @ T _L = 136°C, i	rectangular wave form
I _{FSM}	Max. Peak One Cycle Non-Repetitive	310		5μs Sine or 3μs Rect. pulse	Following any rated load condition and
	Surge Current	40		10ms Sine or 6ms Rect. pulse	with rated V _{RRM} applied
E _{AS}	Non Repetitive Avalanche Energy	2.0	mJ	$T_J = 25 ^{\circ}\text{C}, I_{AS} = 1A, L = 4\text{mH}$	
I _{AR}	Repetitive Avalanche Current	1.0	Α		

Electrical Specifications

	Parameters	Тур.	Max.	Units	Conditio	ns
V _{FM}	Max. Forward Voltage Drop (1)	0.42	0.45	V	@ 1A	T = 25 °C
			0.52	V	@ 2A	$T_J = 25 ^{\circ}\text{C}$
		0.33	0.37	V	@ 1A	T - 400 °C
		0.39	0.45	V	@ 2A	T _J = 100 °C
		0.30	0.35	V	@ 1A	T - 405 °C
		0.36	0.43	V	@ 2A	T _J = 125 °C
I _{RM}	Max. Reverse Leakage Current(1)	0.015	0.2	mA	T _J = 25 °C	
		2.0	6.0	mA	T _J = 100 °C	V _R = rated V _R
		7.0	20	mA	T _J = 125 °C	
C _T	Typical Junction Capacitance	110	-	pF	V _R = 5V _{DC} (test signal range 100kHz to	
					1Mhz), @ 25°C	
L _S	Typical Series Inductance	2.0	-	nH	Measured lead to lead 5mm from package body	
dv/dt	dv/dt Max. Voltage Rate of Change		10000	V/ µs	(Rated V _R)	

⁽¹⁾ Pulse Width < 300µs, Duty Cycle < 2%

Thermal-Mechanical Specifications

	Parameters	Value	Units	Conditions
T _J	T _J Max. Junction Temperature Range(*)		°C	
T _{stg}	Max. Storage Temperature Range	-65 to 150	°C	
R _{thJL}	Max. Thermal Resistance Junction to Lead (**)	35	°C/W	DC operation
R _{thJA}	Max. Thermal Resistance Junction to Ambient	80	°C/W	
Wt	Approximate Weight	0.07(0.002)	gr(oz)	
	Case Style	SMA		Similar D-64
	Device Marking		A	

 $[\]frac{\text{(*)}}{\text{dTi}} < \frac{\text{dPtot}}{\text{Rth(j-a)}} + \frac{1}{\text{Rth(j-a)}} \quad \text{thermal runaway condition for a diode on its own heatsink}$

(**) Mounted 1 inch square PCB

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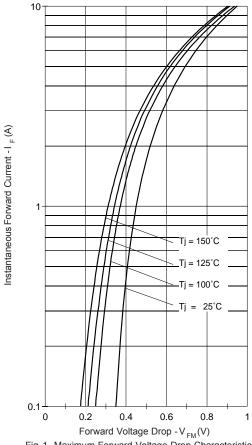


Fig. 1 - Maximum Forward Voltage Drop Characteristics

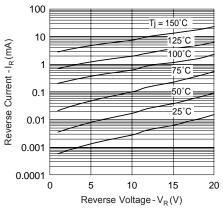


Fig. 2-Typical Peak Reverse Current Vs. Reverse Voltage

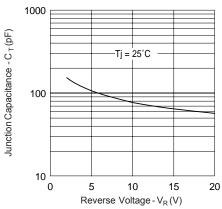


Fig. 3 - Typical Junction Capacitance Vs. Reverse Voltage

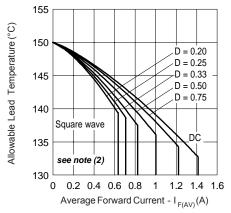


Fig. 4 - Maximum Average Forward Current Vs. Allowable Lead Temperature

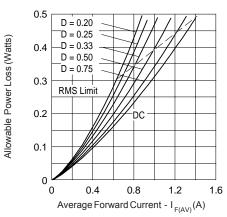


Fig. 5 - Maximum Average Forward Dissipation Vs. Average Forward Current

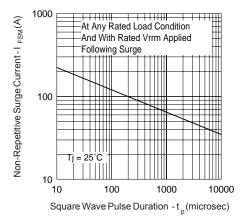


Fig. 6 - Maximum Peak Surge Forward Current Vs. Pulse Duration

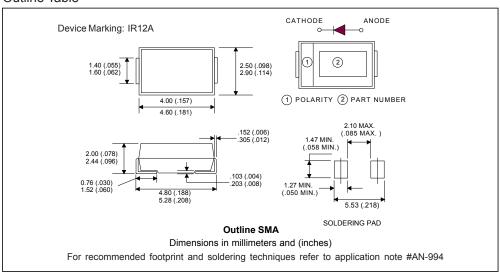
 $\begin{tabular}{ll} \textbf{(2)} \ \ Formula \ used: $T_C = T_J - (Pd + Pd_{REV})$ x R_{thJC}; \\ $Pd = Forward \ Power \ Loss = I_{F(AV)}$ x $V_{FM}@(I_{F(AV)}/D)$ (see Fig. 6); \\ $Pd_{REV} = Inverse \ Power \ Loss = V_{R1}$ x $I_R(1-D)$ \\ \end{tabular}$

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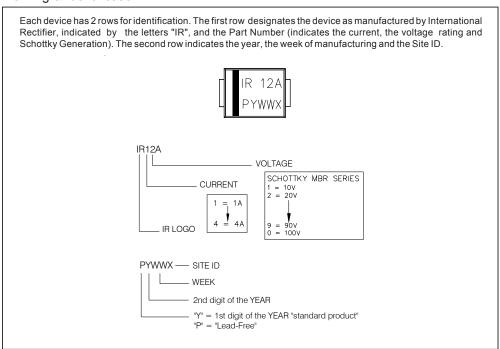
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Outline Table

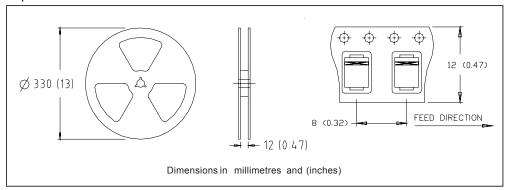


Marking & Identification

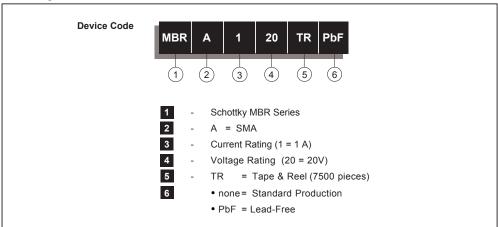


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Tape & Reel Information



Ordering Information Table



Data and specifications subject to change without notice. This product has been designed and qualified for Industrial Level and Lead-Free.

Qualification Standards can be found on IR's Web site.



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