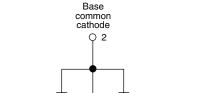
RoHS COMPLIANT

Vishay High Power Products

Schottky Rectifier, 2 x 20 A



TO-247AC

SHA

Anode Common cathode

PRODUCT SUMMARY				
I _{F(AV)} 2 x 20 A				
V _R 40 V				
I _{RM}	60 mA at 100 °C			

FEATURES

- 150 °C T_J operation
- Center tap TO-247 package
- Very low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for industrial level

DESCRIPTION

The STPS40L40CWPbF center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	40	A		
V _{RRM}		40	V		
I _{FSM}	t _p = 5 μs sine	3500	A		
V _F	20 Apk, T_J = 125 °C (per leg)	0.43	V		
TJ		- 55 to 150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	STPS40L40CWPbF	UNITS	
Maximum DC reverse voltage	V _R	40	V	
Maximum working peak reverse voltage	V _{RWM}	40	v	

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T_{C} = 120 °C, rectangular waveform		40		
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	3500	A	
non-repetitive surge current per leg See fig. 7	IFSM		10 ms sine or 6 ms rect. pulse	V_{RRM} applied	430	
Non-repetitive avalanche energy per leg	E _{AS}	T _J = 25 °C, I _{AS} = 4 A, L = 3.4 mH		27	mJ	
Repetitive avalanche current per leg	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical 4		А		

* Pb containing terminations are not RoHS compliant, exemptions may apply



STPS40L40CWPbF

Vishay High Power Products Schottky Rectifier, 2 x 20 A



ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	20 A	T _J = 25 °C	0.49	V
		40 A		0.59	
		20 A	• T _J = 125 °C	0.43	
		40 A		0.56	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	$T_J = 25 \ ^{\circ}C$	V_{R} = Rated V_{R}	0.8	mA
See fig. 2		T _J = 100 °C		60	
Maximum junction capacitance per leg	CT	$V_{R} = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		1850	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		7.5	nH
Maximum voltage rate of change	dV/dt	Rated V _R 10 000		V/µs	

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range)	T _J , T _{Stg}		- 55 to 150	°C	
Maximum thermal resistance, junction to case per leg		Р	DC operation See fig. 4	1.25		
Maximum thermal resistance, junction to case per package		R _{thJC}	DC operation	0.63	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.24		
Approvimato woight				6	g	
Approximate weight				0.21	oz.	
Mounting torque	minimum		Non-lubricated threads	6 (5)	kgf ⋅ cm	
	maximum		Non-Iublicated tilleaus	12 (10)	(lbf ⋅ in)	
Marking device			Case style TO-247AC (JEDEC)	STPS40L40CW		



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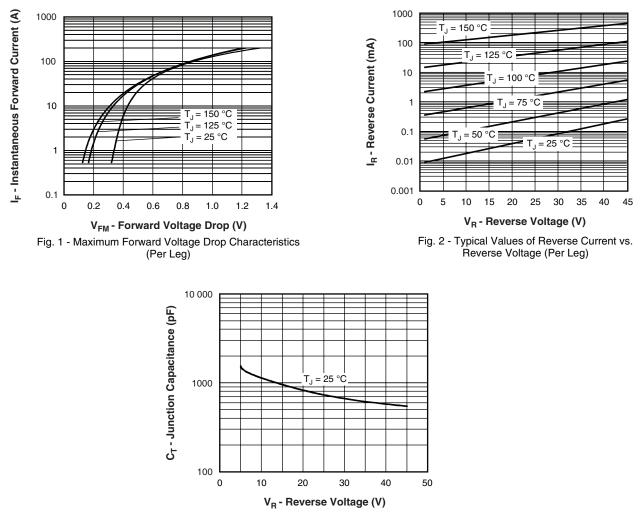


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

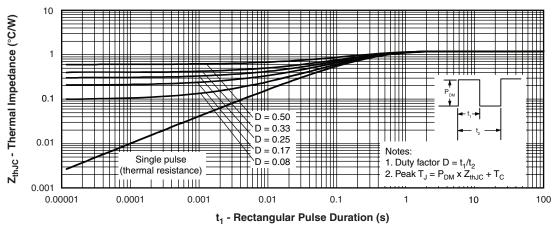
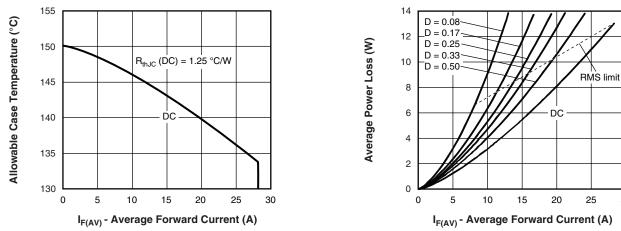


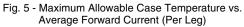
Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

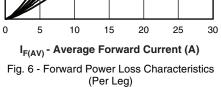
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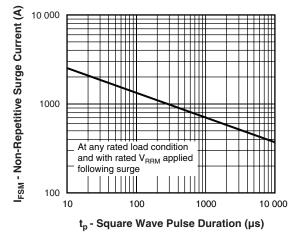
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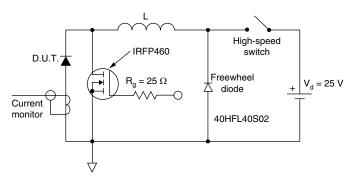
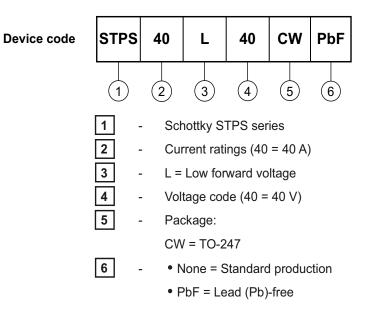


Fig. 8 - Unclamped Inductive Test Circuit



Schottky Rectifier, 2 x 20 A Vishay High Power Products

ORDERING INFORMATION TABLE



Tube standard pack quantity: 25 pieces

LINKS TO RELATED DOCUMENTS			
Dimensions http://www.vishay.com/doc?95223			
Part marking information http://www.vishay.com/doc?95226			



Vishay

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