Not for New Design - End of Life - Last Available Purchase Date is 31-August-2011

# SHAY

## VS-82CNQ030A, VS-82CNQ030ASM, VS-82CNQ030ASL

**Vishay Semiconductors** 

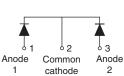
## **Schottky Rectifier** New Generation 3 D-61 Package, 2 x 40 A

VS-82CNQ030A



VS-82CNQ030ASM





Base

62

3

Anode

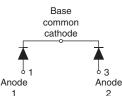
2

D-61-8-SM

VS-82CNQ030ASL



D-61-8-SL



PRODUCT SUMMARY			
I <sub>F(AV)</sub> 2 x 40 A			
V <sub>R</sub>	30 V		

## **FEATURES**

- 150 °C T<sub>J</sub> operation
- Dual center tap module
- · Very low forward voltage drop
- · High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- · Guard ring for enhanced ruggedness and long term reliability
- New fully transfer-mold low profile, small footprint, high current package
- · Designed and qualified for industrial level

## DESCRIPTION

The center tap Schottky rectifier module has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I <sub>F(AV)</sub>	Rectangular waveform	80	А		
V <sub>RRM</sub>		30	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	5100	А		
V <sub>F</sub>	40 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg)	0.37	V		
TJ	Range	- 55 to 150	°C		

VOLTAGE RATINGS					
PARAMETER SYMBOL		VS-82CNQ030A	UNITS		
Maximum DC reverse voltage	V <sub>R</sub>	30	N/		
Maximum working peak reverse voltage	V <sub>RWM</sub>		v		

Document Number: 93402 For technical questions within your region, please contact one of the following: Revision: 03-Mar-11 DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com

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ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current See fig. 5	$I_{F(AV)}$ 50 % duty cycle at T <sub>C</sub> = 119 °C, rectangular waveform		80		
Maximum peak one cycle non-repetitive surge current per leg	I <sub>FSM</sub>	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V <sub>RRM</sub> applied	5100	A
See fig. 7		10 ms sine or 6 ms rect. pulse		880	
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 8 A, L = 1.12 mH		36	mJ
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		8	А

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	L TEST CONDITIONS VALUES		UNITS	
	V <sub>FM</sub> <sup>(1)</sup>	40 A	T <sub>J</sub> = 25 °C	0.47	V
Maximum forward voltage drop per leg		80 A		0.55	
See fig. 1		40 A	- T <sub>J</sub> = 125 °C	0.37	
		80 A		0.47	
Maximum reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	5	mA
See fig. 2		T <sub>J</sub> = 125 °C		280	
Maximum junction capacitance per leg	CT	$V_{R}$ = 5 $V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C		3700	pF
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 mm from package body 5.5		nH	
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000 V/		V/µs	

#### Note

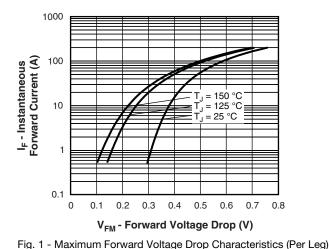
 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2  $\,\%$ 

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 150	°C	
Maximum thermal resistance, junction to case per leg		Р	DC operation See fig. 4	0.85		
Maximum thermal resistance, junction to case per package		R <sub>thJC</sub>	DC operation	0.42	°C/W	
Typical thermal resistance, case to heatsink (D-61-8 only)		R <sub>thCS</sub>	Mounting surface, smooth and greased Device flatness < 5 mils	0.30		
Approximate weight				7.8	g	
				0.28	oz.	
Mounting torque	minimum			40 (35)	kgf · cm	
(D-61-8 only)	maximum			58 (50)	(lbf $\cdot$ in)	
Marking device			Case style D-61-8	82CN0	Q030A	
			Case style D-61-8-SM	82CNQ	30ASM	
			Case style D-61-8-SL	82CNQ	030ASL	



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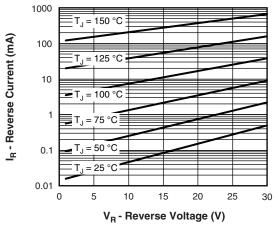


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

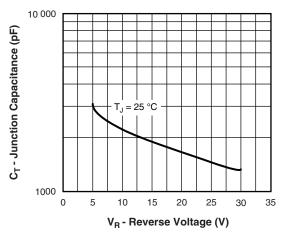


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

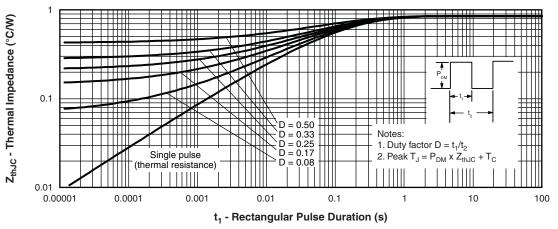
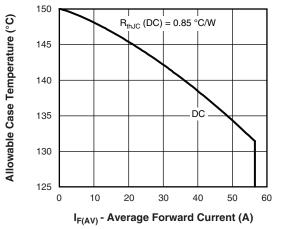


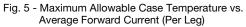
Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics (Per Leg)

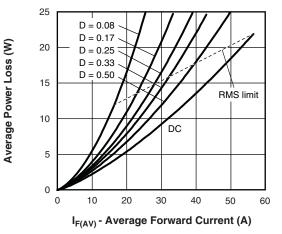
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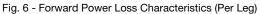
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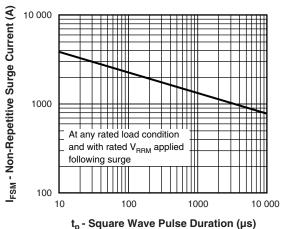


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

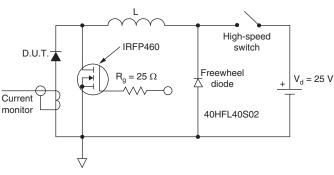


Fig. 8 - Unclamped Inductive Test Circuit

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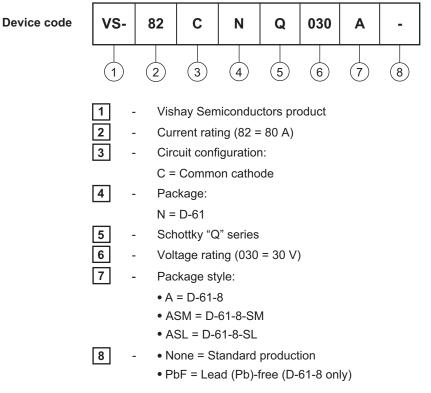
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**Vishay Semiconductors** 

### **ORDERING INFORMATION TABLE**

**VISHAY**®



Standard pack quantity: A = 10 pieces; ASM/ASL = 20 pieces

LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?94354				
Part marking information	www.vishay.com/doc?94356			



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