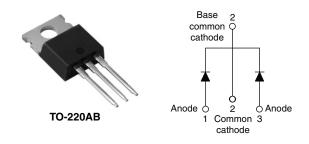


Vishay High Power Products

Schottky Rectifier, 2 x 15 A



PRODUCT SUMMARY				
I _{F(AV)}	2 x 15 A			
V_{R}	50/60 V			

FEATURES

- 150 °C T_J operation
- · Center tap configuration
- Very low forward voltage drop
- · High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified for industrial level

DESCRIPTION

This center tap Schottky rectifier has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL	CHARACTERISTICS	VALUES	UNITS	
I _{F(AV)}	Rectangular waveform	30	Α	
V _{RRM}		50/60	V	
I _{FSM}	t _p = 5 μs sine	1000	Α	
V _F	15 Apk, T _J = 125 °C (per leg)	0.56	V	
TJ	Range	- 55 to 150	°C	

VOLTAGE RATINGS					
PARAMETER	SYMBOL	30CTQ050	30CTQ060	UNITS	
Maximum DC reverse voltage	V _R	50	60	V	
Maximum working peak reverse voltage	V_{RWM}	50	60	V	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	DL TEST CONDITIONS VA		VALUES	UNITS
Maximum average per device forward current		50 % duty cycle at T_C = 105 °C, rectangular waveform		30	
See fig. 5 per leg	I _{F(AV)}			15	Α
Maximum peak one cycle non-repetitive surge current per leg	I _{FSM}	5 µs sine or 3 µs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	1000	
See fig. 7		10 ms sine or 6 ms rect. pulse		260	
Non-repetitive avalanche energy per leg E _{AS}		T _J = 25 °C, I _{AS} = 1.50 A, L = 11.5 mH		13	mJ
Repetitive avalanche current per leg I _{AR}		Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5 \text{ x } V_R$ typical		1.50	Α

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30CTQ... Series

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg	V _{FM} ⁽¹⁾	15 A	T _J = 25 °C	0.62	V
		30 A		0.82	
See fig. 1		15 A	T _J = 125 °C	0.56	
		30 A		0.71	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	T _J = 25 °C	- V _R = Rated V _R	08.0	- mA
See fig. 2	'RM '''	T _J = 125 °C		45	
Threshold voltage	V _{F(TO)}	$T_J = T_J$ maximum		0.39	V
Forward slope resistance	r _t			8.47	mΩ
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		720	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		8.0	nΗ
Maximum voltage rate of change	dV/dt	Rated V _R 10 00		10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range)	T _J , T _{Stg}		- 55 to 150	°C	
Maximum thermal resistance, junction to case per leg		٥		3.25		
Maximum thermal resistance, junction to case per package		R_{thJC}	DC operation	1.63	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased			
Approximate weight				2	g	
Approximate weight				0.07	OZ.	
Mounting torque ——	minimum			6 (5)	kgf · cm	
	maximum			12 (10)	(lbf · in)	
Madda a dada			Coop of the TO 200AP	30CTQ050		
ivial killig device	Marking device		Case style TO-220AB		30CTQ060	



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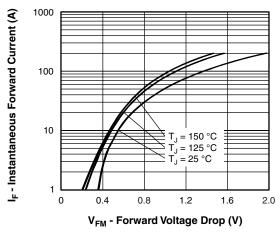


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

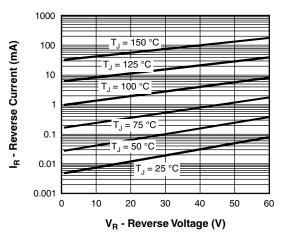


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

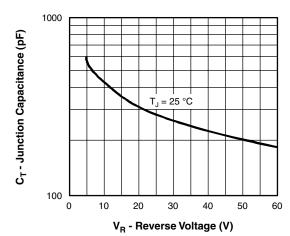


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

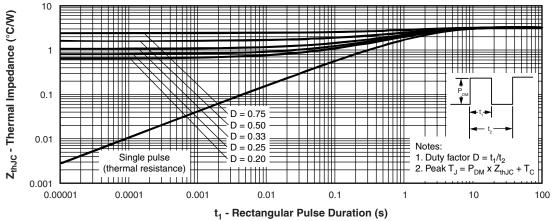
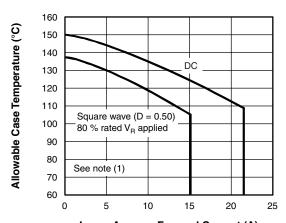


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

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I_{F(AV)} - Average Forward Current (A)

Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

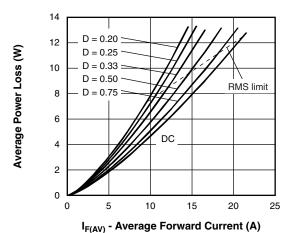


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

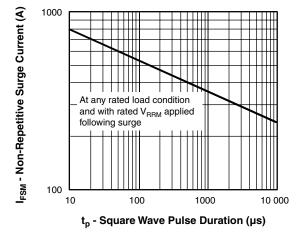


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

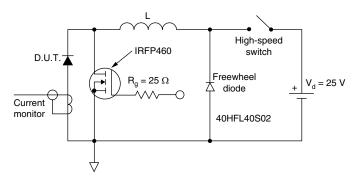


Fig. 8 - Unclamped Inductive Test Circuit

Note

Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 10 \text{ V}$

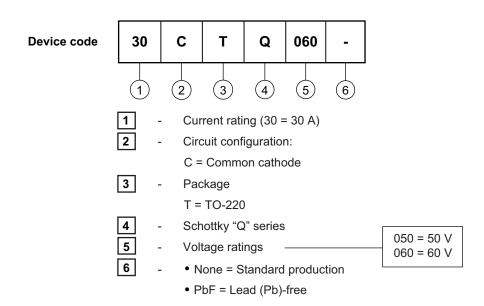
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ORDERING INFORMATION TABLE



Tube standard pack quantity: 50 pieces

LINKS TO RELATED DOCUMENTS				
Dimensions	http://www.vishay.com/doc?95222			
Part marking information	http://www.vishay.com/doc?95225			

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Revision: 18-Jul-08

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