



WJA1030

+5V Active-Bias InGaP HBT Gain Block



Product Features

- Cascadable gain block
- 50 – 4000 MHz
- 15 dB Gain @ 1.9GHz
- +18 dBm P1dB @ 1.9GHz
- +37 dBm OIP3 @ 1.9GHz
- Operates from +5V @ 80mA
- 0.2dB gain flatness from 0.3-2.5GHz
- Robust 1000V ESD, Class 1C
- RoHS-compliant SOT-89 package

Applications

- Wireless Infrastructure
- General Purpose
- Cellular GSM, PCS, UMTS
- W-CDMA, TD-SCDMA, WiMAX

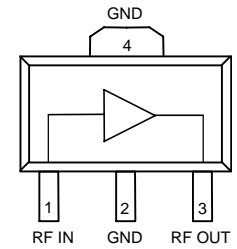
Product Description

The WJA1030 is a cascadable gain block that offers high linearity in a low-cost surface-mount package. At 1.9 GHz, the WJA1030 typically provides 15 dB gain, +37 dBm OIP3, and +18 dBm P1dB. The device is housed in a RoHS-compliant SOT-89 industry-standard SMT package using a NiPdAu plating to eliminate the possibility of tin whiskering.

The WJA1030 consists of Darlington pair amplifiers using a high reliability InGaP/GaAs HBT process technology. The MMIC amplifier is internally matched to 50Ω and only requires DC-blocking capacitors and a bias inductor for operation. An internal active bias is designed to enable stable performance over temperature. A dropping bias resistor is not required allowing the device to be biased directly from a +5V supply voltage.

The broadband amplifier can be directly applied to various current and next generation wireless technologies such as GSM, CDMA, W-CDMA, WiBro, and WiMAX. The WJA1030 is ideal for general purpose applications such as LO buffering, IF amplification and pre-driver stages within the 50 to 4000 MHz frequency range.

Functional Diagram



Function	Pin No.
Input	1
Output/Bias	3
Ground	2, 4

Specifications ⁽¹⁾

Parameter	Units	Min	Typ	Max
Operational Bandwidth	MHz	50		4000
Test Frequency	MHz		1900	
Gain	dB	13.2	14.9	16.2
Input Return Loss	dB		12	
Output Return Loss	dB		16	
Output P1dB	dBm		+18.3	
Output IP3 ⁽²⁾	dBm	+34	+37	
Output IP2	dBm		+44	
Noise Figure	dB		6.1	
Device Voltage	V		5	
Device Current	mA	71	80	91

1. Test conditions: 25 °C, Supply Voltage = +5 V, 50 Ω System.. S-parameters and 3OIP measured at device pins. All other specifications measured on evaluation board.
2. 3OIP measured with two tones at an output power of 3 dBm/tone separated by 1 MHz. The suppression on the largest IM3 product is used to calculate the 3OIP using a 2:1 rule.

Typical Performance ⁽³⁾

Parameter	Units	Typical				
Frequency	MHz	500	900	1900	2140	2500
S21	dB	14.4	14.4	14.4	14.4	14.3
S11	dB	-13	-15	-30	-36	-33
S22	dB	-16	-19	-13	-12	-13
Output P1dB	dBm	+19.4	+19.3	+18.3	+17.5	+16
Output IP3 ⁽²⁾	dBm	+37.9	+37	+36.6	+36.3	+33.9
Output IP2	dBm	+49.4	+48.3	+44	+41.4	+39.2
Noise Figure	dB	5.3	5.5	6.1	6.3	6.5

3. Listed typical performance parameters measured on evaluation board.

Absolute Maximum Rating

Parameter	Rating
Storage Temperature	-55 to +150 °C
Supply Voltage	+6.5 V
Input Power	+24 dBm
θ _{jc} (junction to paddle)	80.6 °C / W
Maximum Junction Temperature	150 °C

Operation of this device above any of these parameters may cause permanent damage.

Ordering Information

Part No.	Description
WJA1030	+5V Active Bias InGaP HBT Gain Block (lead-free/green/RoHS-compliant SOT-89 Package)
WJA1030-PCB	50 – 4000 MHz Fully Assembled Eval. Board

Standard tape / reel size = 1000 pieces on a 7" reel

Specifications and information are subject to change without notice



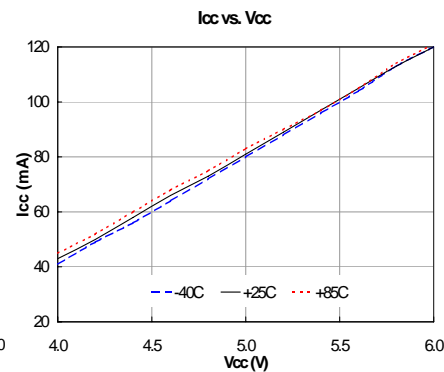
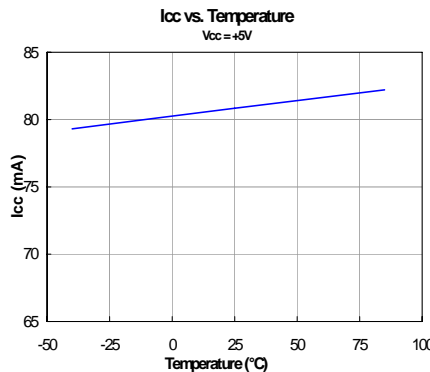
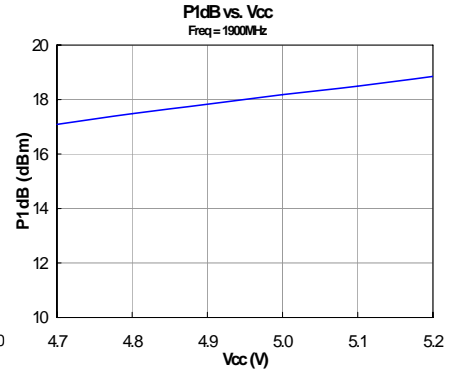
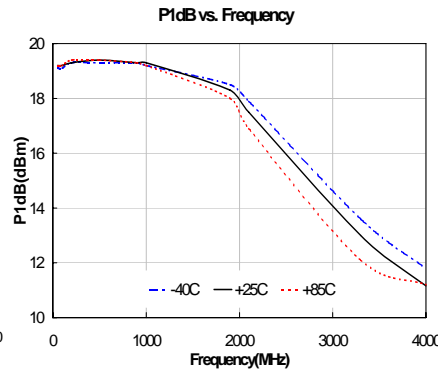
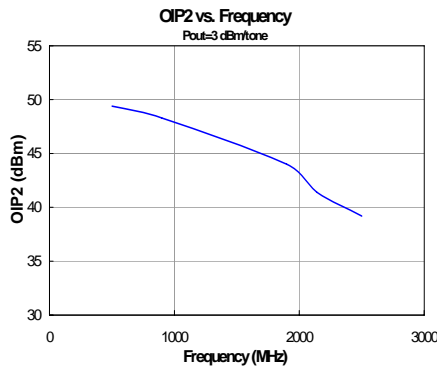
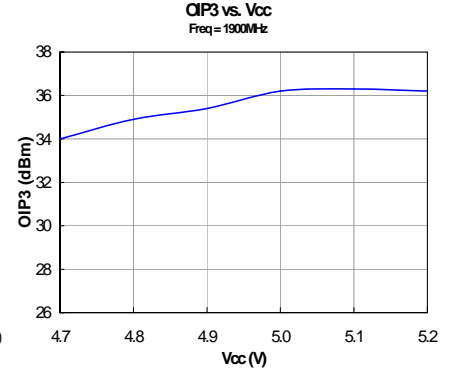
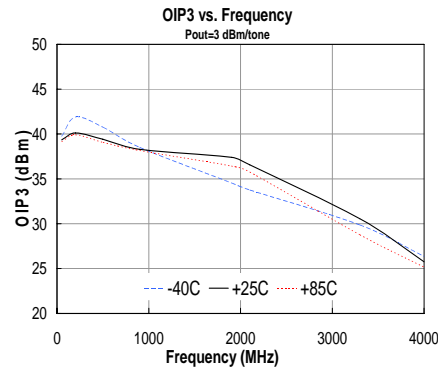
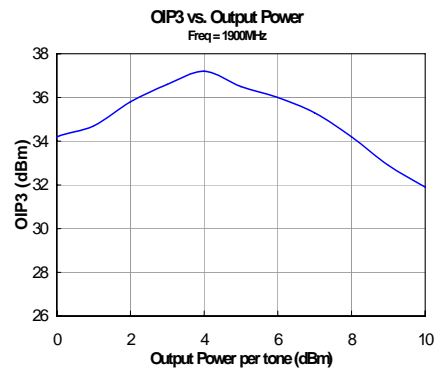
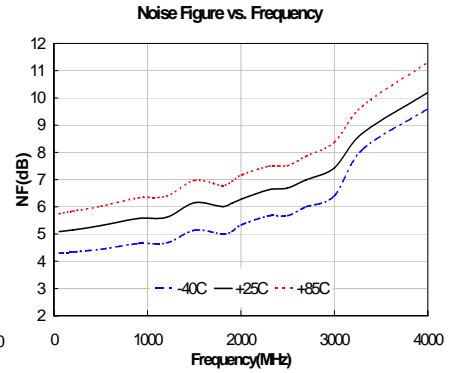
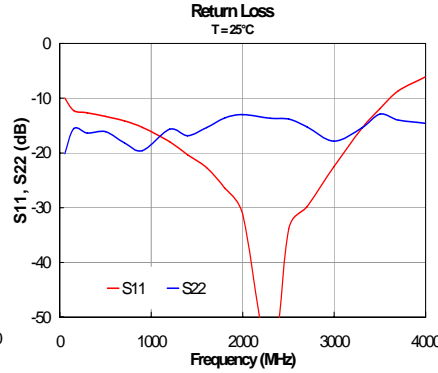
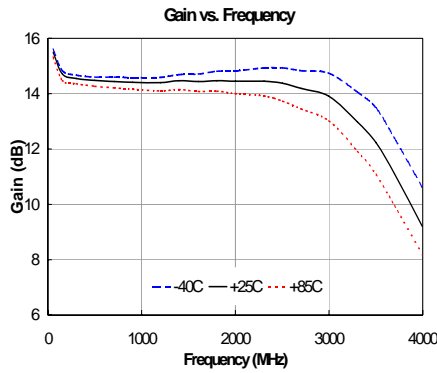
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Typical Evaluation Board RF Performance

Supply Bias = +5 V, $I_{CC} = 80$ mA



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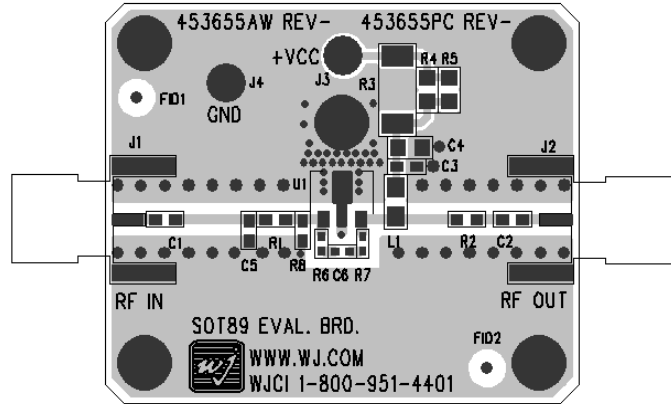
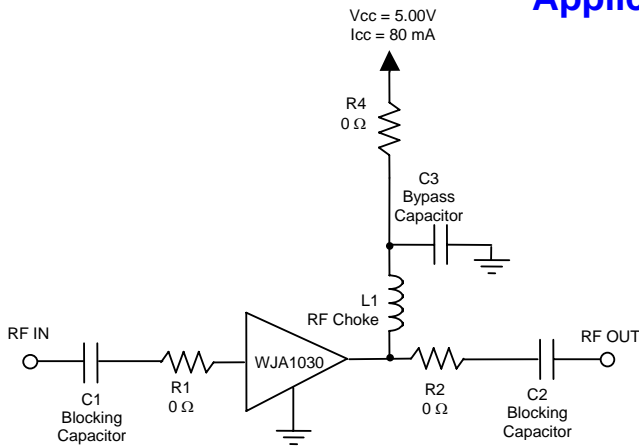


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Application Circuit



Recommended Component Values ⁽¹⁾

Ref. Name	Value / Type	Size
L1	470 nH ferrite core wire wound inductor ⁽²⁾	0805
C1, C2	1000 pF NPO chip capacitor	0603
C3	0.018 μF chip capacitor	0603
R1, R2, R4	0 Ω ⁽³⁾	0603
C4, C5, C6, R3, R5, R6, R7, R8	Do Not Place ⁽³⁾	

1. The listed values are contained on the evaluation board to achieve optimal broadband performance
2. For lower cost and performance (500 – 4000 MHz) option use 18 nH air core wire wound inductor.
3. Place holders for the 0Ω resistors and “Do Not Place” references are not needed for final design.

Typical Device Data

S-Parameters ($V_{device} = +5 V$, $I_{CC} = 80 mA$, $T = 25 ^\circ C$, calibrated to device leads)

Freq (GHz)	S11 (dB)	S11 (ang)	S21 (dB)	S21 (ang)	S12 (dB)	S12 (ang)	S22 (dB)	S22 (ang)
10	-10.83	-58.08	19.46	164.89	-22.65	22.89	-7.78	-42.25
50	-12.23	-141.31	15.67	162.49	-19.17	8.66	-13.74	-117.02
100	-12.34	-161.16	14.97	166.07	-18.94	3.19	-15.40	-144.57
200	-12.35	-173.41	14.68	163.99	-18.79	-1.22	-16.18	-163.35
400	-12.20	178.57	14.70	155.01	-18.77	-6.50	-16.66	-176.66
600	-11.88	173.95	14.68	144.15	-18.79	-11.24	-16.93	174.89
800	-11.82	169.03	14.66	133.15	-18.73	-15.56	-17.11	171.49
1000	-11.96	160.89	14.73	122.08	-18.71	-19.47	-16.77	172.56
1200	-12.09	151.00	14.71	109.83	-18.72	-23.66	-16.57	175.62
1400	-12.00	141.47	14.72	98.56	-18.56	-28.45	-16.37	178.74
1600	-12.08	132.47	14.81	86.09	-18.56	-33.24	-16.52	179.44
1800	-12.41	124.96	14.89	73.48	-18.41	-37.07	-16.66	179.88
2000	-13.68	114.29	14.98	61.01	-18.32	-42.56	-16.10	-177.54
2200	-15.85	96.26	14.98	47.36	-18.25	-47.72	-14.74	-177.03
2400	-18.90	65.65	14.84	33.24	-18.25	-53.56	-12.91	-177.60
2600	-21.35	20.69	14.76	18.24	-18.22	-59.41	-11.52	-179.71
2800	-20.80	-41.45	14.49	3.96	-18.35	-66.15	-10.38	176.84
3000	-16.35	-85.03	14.02	-12.00	-18.44	-72.34	-9.38	170.50
3200	-12.38	-108.85	13.47	-27.18	-18.79	-78.26	-8.30	162.73
3400	-9.61	-124.63	12.65	-42.43	-19.07	-84.90	-7.42	153.42
3600	-7.75	-139.93	11.69	-56.65	-19.52	-89.74	-6.60	145.55
3800	-6.55	-155.31	10.76	-69.84	-19.79	-93.89	-6.01	140.59
4000	-5.60	-170.19	9.64	-82.81	-20.16	-98.32	-5.79	134.97

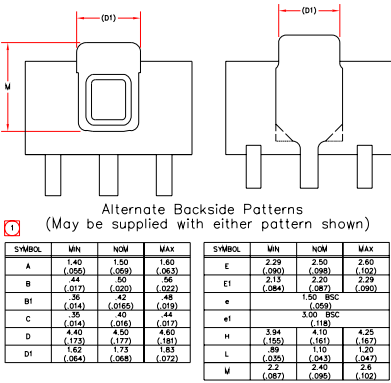
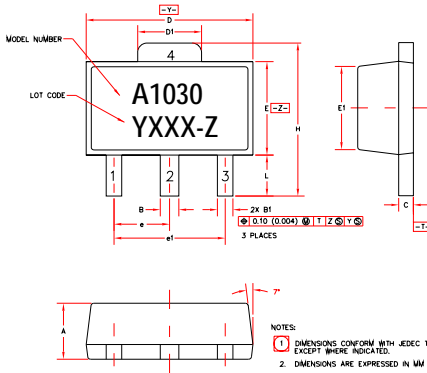
Device S-parameters are available for Download from the website at: <http://www.wj.com>



Mechanical Information

This package is lead-free/Green/RoHS-compliant. It is compatible with both lead-free (maximum 260 °C reflow temperature) and leaded (maximum 245 °C reflow temperature) soldering processes. The plating material on the leads is NiPdAu.

Outline Drawing



Product Marking

The WJA1030 will be marked with an "A1030" designator with a lot code marked below the part designator. The "Y" represents the last digit of the year the part was manufactured, the "XXX" is an auto-generated number, and "Z" refers to a wafer number in a lot batch.

Tape and reel specifications for this part are located on the website in the "Application Notes" section.

MSL / ESD Rating



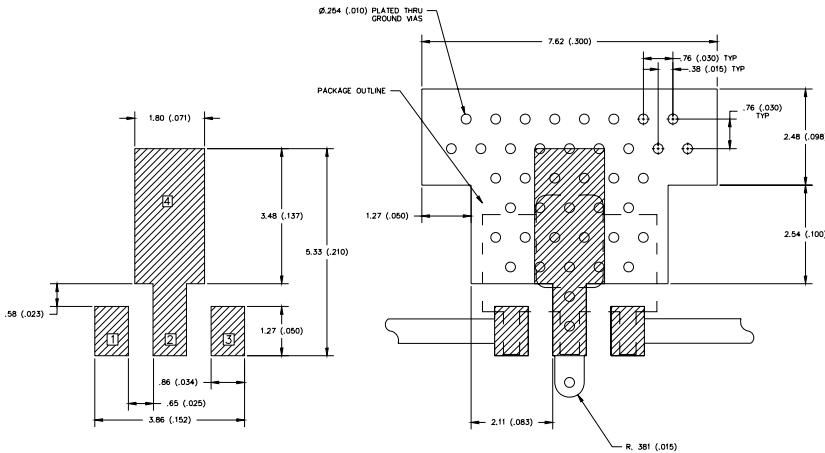
Caution! ESD sensitive device.

ESD Rating: Class 1C
Value: Passes $\geq 1000V$ min.
Test: Human Body Model (HBM)
Standard: JEDEC Standard JESD22-A114

ESD Rating: Class IV
Value: Passes $\geq 1000V$ min.
Test: Charged Device Model (CDM)
Standard: JEDEC Standard JESD22-C101

MSL Rating: Level 3 at +260 °C convection reflow
Standard: JEDEC Standard J-STD-020

Land Pattern



Mounting Config. Notes

1. Ground / thermal vias are critical for the proper performance of this device. Vias should use a .35mm (#80 / .0135") diameter drill and have a final plated thru diameter of .25 mm (.010").
2. Add as much copper as possible to inner and outer layers near the part to ensure optimal thermal performance.
3. Mounting screws can be added near the part to fasten the board to a heatsink. Ensure that the ground / thermal via region contacts the heatsink.
4. Do not put solder mask on the backside of the PC board in the region where the board contacts the heatsink.
5. RF trace width depends upon the PC board material and construction.
6. Use 1 oz. Copper minimum.
7. All dimensions are in millimeters (inches). Angles are in degrees.