

July 2009

# FOD420, FOD4208, FOD4216, FOD4218 6-Pin DIP Triac Drivers

### **Features**

- 300mA on-state current
- High blocking voltage
  - -800V (FOD4208, FOD4218)
  - -600V (FOD420, FOD4216)
- High trigger sensitivity
  - -1.3mA (FOD4216, FOD4218)
  - -2mA (FOD420, FOD4208)
- High static dv/dt (10,000V/µs)
- 6 pin DIP dual in-line package

   available with surface mount leadform.
- Lead free assembly
- UL, VDE, FIMKO and C-UL approved

### **Applications**

- Solid-state relays
- Industrial controls
- Lighting controls
- Static power switches
- AC motor starters

### Description

The FOD420, FOD4208, FOD4216 and FOD4218 devices consist of an infrared emitting diode coupled to a hybrid random phase triac formed with two inverse parallel SCRs which form the triac function capable of driving discrete triacs. The FOD4216 and FOD4218 utilize a high efficiency infrared emitting diode which offers an improved trigger sensitivity. These devices are housed in a standard 6-pin dual in-line (DIP) package.

# Package Schematic ANODE 1 ANODE 2 ANOT CONNECT (TRIAC SUBSTRATE)

©2004 Fairchild Semiconductor Corporation FOD420, FOD4208, FOD4216, FOD4218 Rev. 1.4.0

# **Absolute Maximum Ratings** (T<sub>A</sub> = 25°C unless otherwise noted)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameters	Device	Value	Units
TOTAL DI	EVICE			
T <sub>STG</sub>	Storage Temperature	All	-55 to +150	°C
T <sub>OPR</sub>	Operating Temperature	All	-55 to +100	°C
T <sub>SOL</sub>	Lead Solder Temperature (Wave)	All	260 for 10 sec	°C
TJ	Junction Temperature Range	All	125	°C
$V_{ISO}$	Isolation Test Voltage <sup>(1)</sup> (rms AC voltage, 60Hz, 1 min. duration)	All	5000	Vac(rms)
P <sub>DTOTAL</sub>	Total Device Power Dissipation @ 25°C	All	500	mW
	Derate above 40°C		6.6	mW/°C
EMITTER				
IF	Continuous Forward Current	All	30	mA
V <sub>R</sub>	Reverse Voltage	All	6	V
P <sub>DE</sub>	Total Power Dissipation 25°C Ambient	All	50	mW
	Derate above 40°C		0.71	mW/°C
DETECTO	PR			
V <sub>DRM</sub>	Off-State Output Terminal Voltage	FOD420, FOD4216	600	V
		FOD4208, FOD4218	800	1
I <sub>TSM</sub>	Peak Non-Repetitive Surge Current (single cycle 60Hz sine wave)	All	3	А
I <sub>TM</sub>	Peak On-State Current	All	300	mA
P <sub>DDET</sub>	Total Power Dissipation @ 25°C Ambient	All	450	mW
	Derate above 40°C		5.9	mW/°C

### Note

1. Isolation voltage, V<sub>ISO</sub>, is an internal device dielectric breakdown rating. For this test, Pins 1, 2 and 3 are common, and Pins 4, 5 and 6 are common. 5,000 VRMS for 1 minute duration is equivalent to 6,000 VRMS for 1 second duration.

# **Electrical Characteristics** (T<sub>A</sub> = 25°C Unless otherwise specified)

### **Individual Component Characteristics**

Symbol	Parameters	Test Cond	litions	Device	Min.	Тур.*	Max	Units
EMITTER								
V <sub>F</sub>	Input Forward Voltage	I <sub>F</sub> = 20mA		All		1.28	1.5	V
I <sub>R</sub>	Reverse Leakage Current	V <sub>R</sub> = 6V		All		0.01	10	μA
DETECTO	)R							
I <sub>DRM</sub>	Peak Blocking Current, Either Direction	$I_F = 0,$ $T_A = 100^{\circ}C^{(2)}$	V <sub>D</sub> = 800V	FOD4208, FOD4218		3	100	μA
			V <sub>D</sub> = 600V	FOD420, FOD4216				
I <sub>R(RMS)</sub>	Reverse Current	T <sub>A</sub> = 100°C	V <sub>D</sub> = 800V	FOD4208, FOD4218		3	100	μA
			V <sub>D</sub> = 600V	FOD420, FOD4216				
dv/dt	Critical Rate of Rise of Off-State Voltage	$I_F = 0^{(4)}$ (Fig. 11)			10,000			V/µs

### **Transfer Characteristics**

Symbol	DC Characteristics	Test (	Conditions	Device	Min.	Тур.*	Max.	Units
I <sub>FT</sub>	LED Trigger Current	Main Terminal Volt	age = 5V <sup>(3)</sup>	FOD420, FOD4208		0.75	2.0	mA
				FOD4216, FOD4218		0.75	1.3	
V <sub>TM</sub>	Peak On-State Voltage, Either Direction	I <sub>TM</sub> = 300 mA peal	k, I <sub>F</sub> = rated I <sub>FT</sub>	All		2.2	3	V
I <sub>H</sub>	Holding Current, Either Direction	V <sub>T</sub> = 3V		All		200	500	μA
ΙL	Latching Current	V <sub>T</sub> = 2.2V		All		5		mA
t <sub>ON</sub>	Turn-On Time	PF = 1.0,	$V_{RM} = V_{DM} = 565 \text{ VAC}$	FOD4208		60		μs
		I <sub>T</sub> = 300mA	V <sub>RM</sub> = V <sub>DM</sub> = 424 VAC	FOD420, FOD4216, FOD4218				
t <sub>OFF</sub>	Turn-Off Time		V <sub>RM</sub> = V <sub>DM</sub> = 565 VAC	FOD4208		52		μs
			$V_{RM} = V_{DM} = 424 \text{ VAC}$	FOD420, FOD4216, FOD4218				
dv/dt <sub>crq</sub>	Critical Rate of Rise of	$V_{D} = 0.67 V_{DRM},$	T <sub>j</sub> = 25°C	All	10,000			V/µs
	Voltage at Current Commutation	di/dt <sub>crq</sub> ≤ 15 A/ms	T <sub>j</sub> = 80°C		5,000			
di/dt <sub>cr</sub>	Critical Rate of Rise of On-State Current			All			8	A/µs
dV(IO)/dt	Critical Rate of Rise of Coupled Input/Output Voltage	$I_T = 0A,$ $V_{RM} = V_{DM} = 424$	IVAC	All		10,000		V/µs

## **Isolation Characteristics**

Symbol	Characteristics	Test Conditions	Min.	Typ.*	Max.	Units	
V <sub>ISO</sub>	Input-Output Isolation Voltage	f = 60Hz, t = 1 min. <sup>(5)</sup>	5000			Vac(rms)	

<sup>\*</sup>Typical values at T<sub>A</sub> = 25°C

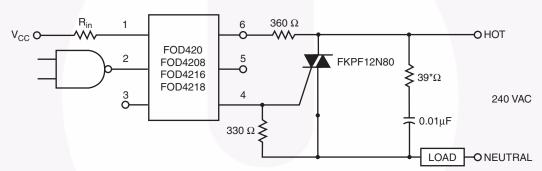
### Notes:

- 2. Test voltage must be applied within dv/dt rating.
- 3. All devices are guaranteed to trigger at an  $I_F$  value less than or equal to max  $I_{FT}$ . Therefore, recommended operating  $I_F$  lies between max  $I_{FT}$  (2mA for FOD420 and FOD4208 and 1.3mA for FOD4216 and FOD4218 and the absolute max  $I_F$  (30mA).
- 4. This is static dv/dt. See Figure 11 for test circuit. Commutating dv/dt is a function of the load-driving thyristor(s) only.
- 5. Isolation voltage, V<sub>ISO</sub>, is an internal device dielectric breakdown rating. For this test, Pins 1, 2 and 3 are common, and Pins 4, 5 and 6 are common.

### **Typical Application**

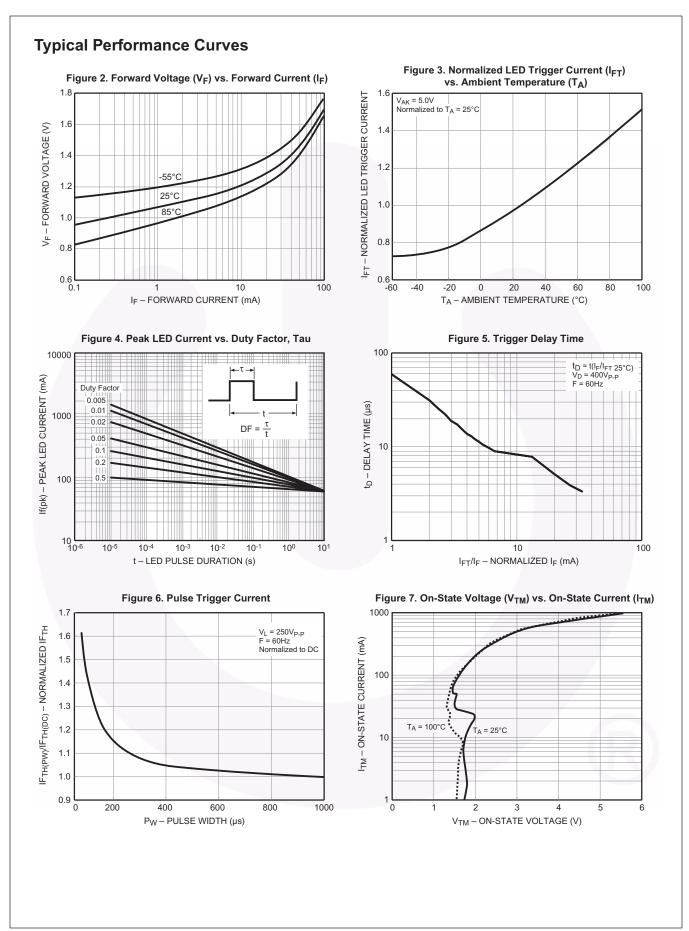
Typical circuit for use when hot line switching is required. In this circuit the "hot" side of the line is switched and the load connected to the cold or neutral side. The load may be connected to either the neutral or hot line.

 $R_{in}$  is calculated so that  $I_F$  is equal to the rated  $I_{FT}$  of the part, 2mA for FOD420 and FOD4208, 1.3mA for FOD4216 and FOD4218. The  $39\Omega$  resistor and  $0.01\mu F$  capacitor are for snubbing of the triac and may or may not be necessary depending upon the particular triac and load use.



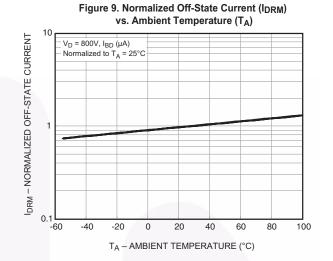
<sup>\*</sup> For highly inductive loads (power factor < 0.5), change this value to 360 ohms.

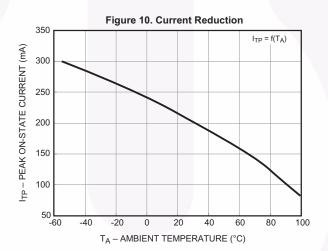
Figure 1. Hot-Line Switching Application Circuit

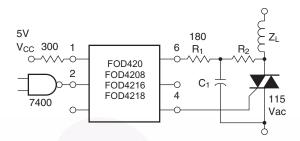


## **Typical Performance Curves** (Continued)

Figure 8. Normalized Holding Current (I<sub>H</sub>) vs. Ambient Temperature (T<sub>A</sub>) 2.2 Normalized to T<sub>A</sub> = 25°C IH - NORMALIZED HOLDING CURRENT 2.0 1.8 1.6 1.4 1.2 1.0 8.0 -60 -40 -20 20 40 60 80 100 T<sub>A</sub> – AMBIENT TEMPERATURE (°C)







NOTE: Circuit supplies 25mA drive to gate of triac at  $V_{in} = 25V$  and  $T_A < 70^{\circ}C$ 

TRIAC				
I <sub>GT</sub>	R <sub>2</sub>	С		
15 mA	2400	0.1		
30 mA	1200	0.2		
50 mA	800	0.3		

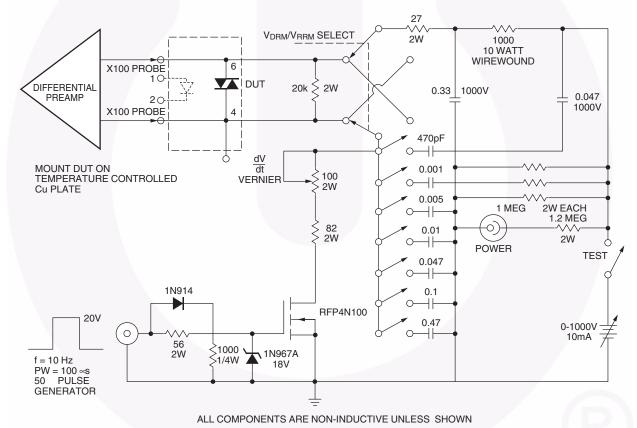


Figure 11. Circuit for Static  $\frac{dV}{dt}$  Measurement of Power Thyristors

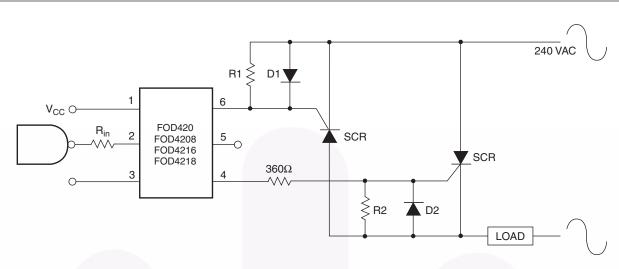


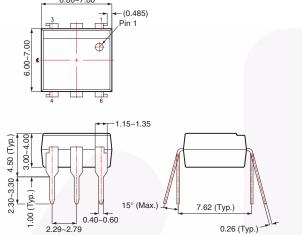
Figure 12. Inverse-Parallel SCR Driver Circuit

Suggested method of firing two, back-to-back SCR's with a Fairchild triac driver. Diodes can be 1N4001; resistors, R1 and R2, are optional  $330\Omega$ .

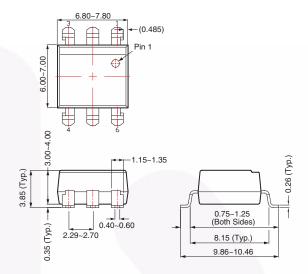
Note: This optoisolator should not be used to drive a load directly. It is intended to be a discrete triac driver device only.

# **Package Dimensions**

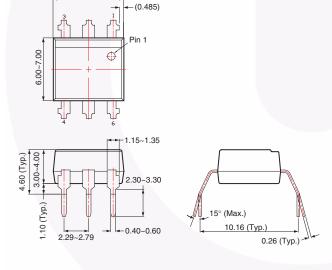
### **Through Hole**



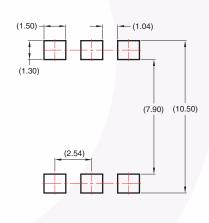
### **Surface Mount**



### 0.4" Lead Spacing



# Recommended Pad Layout for Surface Mount Leadforms



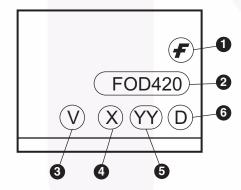
### Note:

All dimensions are in millimeters.

# **Ordering Information**

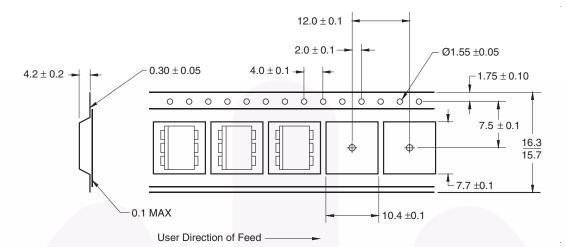
Option	Order Entry Identifier (example)	Description
None	FOD420	Standard Through Hole Device
S	FOD420S	Surface Mount Lead Bend
SD	FOD420SD	Surface Mount; Tape and reel
Т	FOD420T	0.4" Lead Spacing
V	FOD420V	IEC60747-5-2 certification
TV	FOD420TV	IEC60747-5-2 certification, 0.4" Lead Spacing
SV	FOD420SV	IEC60747-5-2 certification, Surface Mount
SDV	FOD420SDV	IEC60747-5-2 certification, Surface Mount, Tape & Reel

# **Marking Information**



Definiti	Definitions				
1	Fairchild logo				
2	Device number				
3	VDE mark indicates IEC60747-5-2 certified (Note: Only appears on parts ordered with VDE option – See order entry table)				
4	One digit year code, e.g., '7'				
5	Two digit work week ranging from '01' to '53'				
6	Assembly package code				

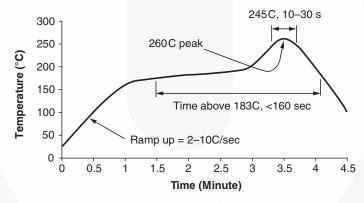
# **Carrier Tape Specifications**



### Note:

All dimensions are in inches (millimeters).

### **Reflow Profile**



- Peak reflow temperature: 260 C (package surface temperature)
   Time of temperature higher than 183 C for 160 seconds or less
   One time soldering reflow is recommended





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Definition of Terms					
Datasheet Identification	Product Status	Definition			
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