

Vishay Semiconductors

AUTOMOTIVE

COMPLIANT HALOGEN

FREE

Silicon NPN Phototransistor



DESCRIPTION

VEMT2500X01 series are silicon NPN epitaxial planar phototransistors in a miniature dome lens, clear epoxy package for surface mounting. The device is sensitive to visible and near infrared radiation.

FEATURES

• Package type: surface mount



- Dimensions (L x W x H in mm): 2.3 x 2.3 x 2.8
- AEC-Q101 qualified
- · High radiant sensitivity
- Suitable for visible and near infrared radiation
- Fast response times
- Angle of half sensitivity: $\varphi = \pm 15^{\circ}$
- Package matched with IR emitter series VSMB2000X01
- Floor life: 4 weeks, MSL 2a, acc. J-STD-020
- Lead (Pb)-free reflow soldering
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition

APPLICATIONS

- Detector in automotive applications
- Photo interrupters
- Miniature switches
- Counters
- Encoders
- Position sensors

PRODUCT SUMMARY				
COMPONENT	I _{ca} (mA)	φ (deg)	λ _{0.1} (nm)	
VEMT2500X01	6	± 15	470 to 1090	
VEMT2520X01	6	± 15	470 to 1090	

Note

· Test condition see table "Basic Characteristics"

ORDERING INFORMATION					
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM		
VEMT2500X01	Tape and reel	MOQ: 6000 pcs, 6000 pcs/reel	Reverse gullwing		
VEMT2520X01	Tape and reel	MOQ: 6000 pcs, 6000 pcs/reel	Gullwing		

Note

· MOQ: minimum order quantity

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Collector emitter voltage		V _{CEO}	20	V	
Emitter collector voltage		V _{ECO}	7	V	
Collector current		I _C	50	mA	
Power power dissipation	T _{amb} ≤ 75 °C	P _V	100	mW	
Junction temperature		T _j	100	°C	
Operating temperature range		T _{amb}	- 40 to + 100	°C	

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VEMT2500X01, VEMT2520X01

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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)				
PARAMETER TEST CONDITION SYMBOL VALUE				UNIT
Storage temperature range		T _{stg}	- 40 to + 100	°C
Soldering temperature	Acc. reflow profile fig. 8	T _{sd}	260	°C
Thermal resistance junction/ambient	Acc. J-STD-051	R _{thJA}	250	K/W

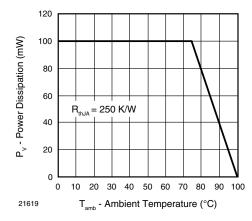


Fig. 1 - Power Dissipation Limit vs. Ambient Temperature

BASIC CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Collector emitter breakdown voltage	I _C = 0.1 mA	V _{CEO}	20			V
Collector dark current	V _{CE} = 5 V, E = 0	I _{CEO}		1	100	nA
Collector emitter capacitance	$V_{CE} = 0 \text{ V, f} = 1 \text{ MHz, E} = 0$	C _{CEO}		25		pF
Collector light current	E_e = 1 mW/cm ² , λ = 950 nm, V_{CE} = 5 V	I _{CA}	3	6	9	mA
Angle of half sensitivity		φ		± 15		deg
Wavelength of peak sensitivity		λ_{p}		850		nm
Range of spectral bandwidth		λ _{0.1}		470 to 1090		nm
Collector emitter saturation voltage	$I_{C} = 0.05 \text{ mA}$	V _{CEsat}			0.4	V
Temperature coefficient of I _{ca}	$E_{e}=1~\text{mW/cm}^{2},\lambda=950~\text{nm},\\ V_{CE}=5~\text{V}$	Tk _{lca}		1.1		%/K

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BASIC CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

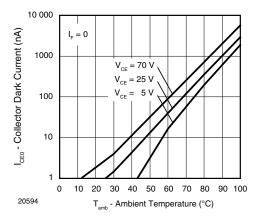


Fig. 2 - Collector Dark Current vs. Ambient Temperature

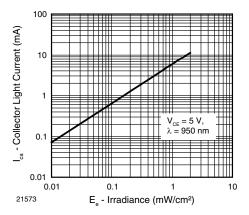


Fig. 3 - Collector Light Current vs. Irradiance

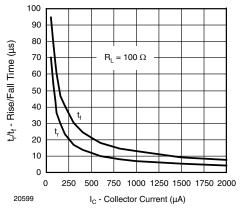


Fig. 4 - Rise/Fall Time vs. Collector Current

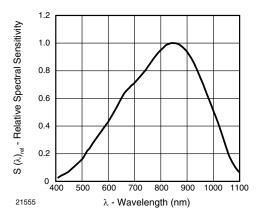


Fig. 5 - Relative Spectral Sensitivity vs. Wavelength

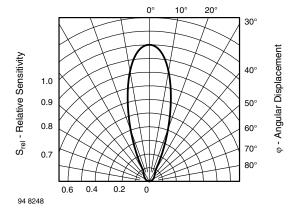


Fig. 6 - Relative Radiant Sensitivity vs. Angular Displacement

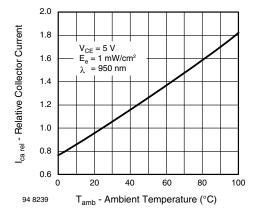


Fig. 7 - Relative Collector Current vs. Ambient Temperature



REFLOW SOLDER PROFILE

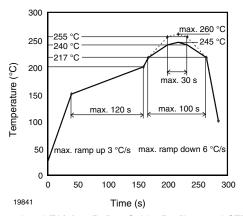


Fig. 8 - Lead (Pb)-free Reflow Solder Profile acc. J-STD-020

DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

FLOOR LIFE

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

Floor life: 4 weeks

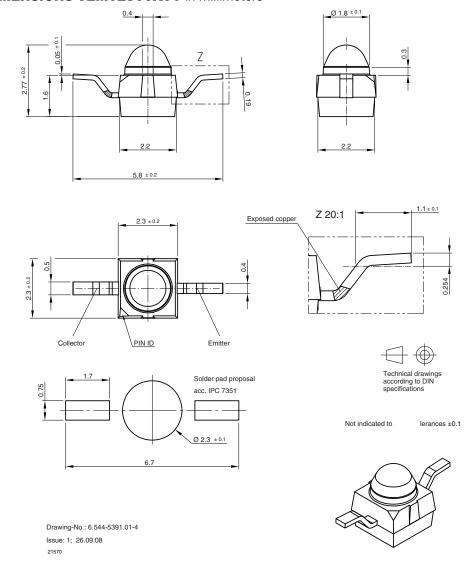
Conditions: T_{amb} < 30 °C, RH < 60 %

Moisture sensitivity level 2a, acc. to J-STD-020.

DRYING

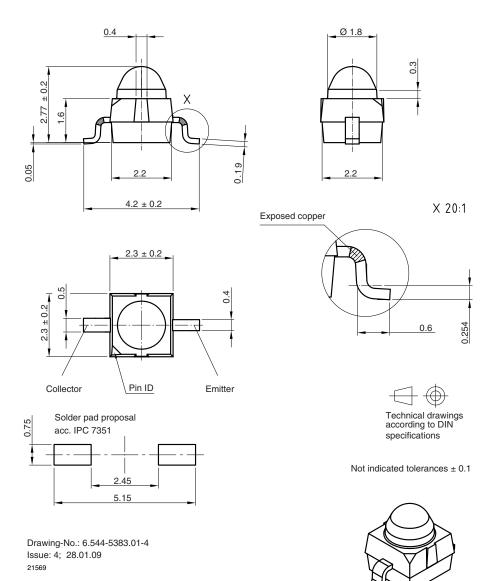
In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or label. Devices taped on reel dry using recommended conditions 192 h at 40 $^{\circ}$ C (+ 5 $^{\circ}$ C), RH < 5 $^{\circ}$ M.

PACKAGE DIMENSIONS VEMT2500X01 in millimeters



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PACKAGE DIMENSIONS VEMT2520X01 in millimeters

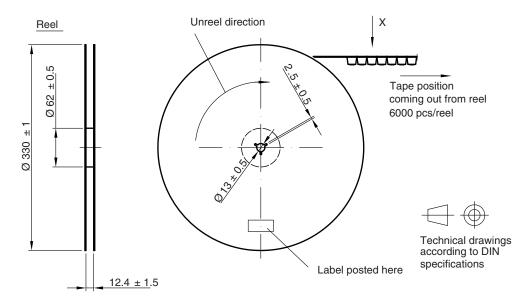


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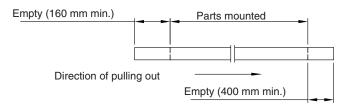
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TAPE AND REEL DIMENSIONS VEMT2500X01 in millimeters

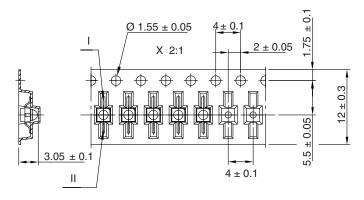


Leader and trailer tape:



Terminal position in tape

Devicce	Lead I	Lead II
VEMT2000		
VEMT2500	Collector	Emitter
VEMD2000		
VEMD2500	Cathode	Anode
VSMB2000	Calriode	Anode
VSMG2000		
VSMY2850RG	Anode	Cathode



Drawing-No.: 9.800-5100.01-4

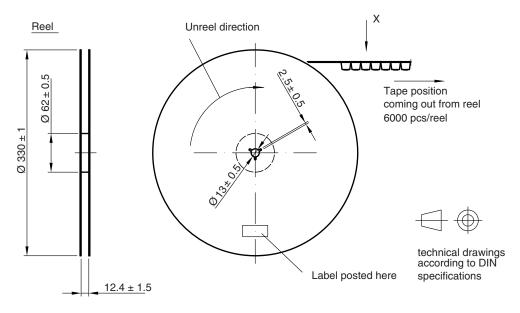
Issue: 2; 18.03.10

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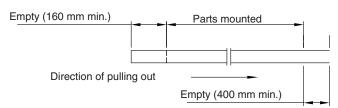


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TAPE AND REEL DIMENSIONS VEMT2520X01 in millimeters

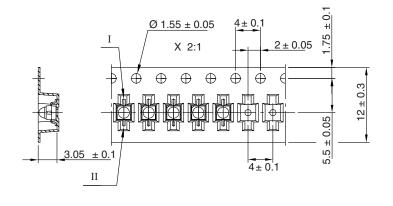


Leader and trailer tape:



Terminal position in tape

Devicce	Lead I	Lead II
VEMT2020		
VEMT2520	Collector	Emitter
VSMB2020		
VSMG2020	0-4	A I -
VEMD2020	Cathode	Anode
VEMD2520		
VSMY2850G	Anode	Cathode



Drawing-No.: 9.800-5091.01-4

Issue: 3; 18.03.09

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