3.5X3.5mm SMD LED WITH CERAMIC SUBSTRATE

Reddish-Orange

PRELIMINARY SPEC

ATTENTION OBSERVE PRECAUTIONS FOR HANDLING ELECTROSTATIC DISCHARGE SENSITIVE DEVICES

Features

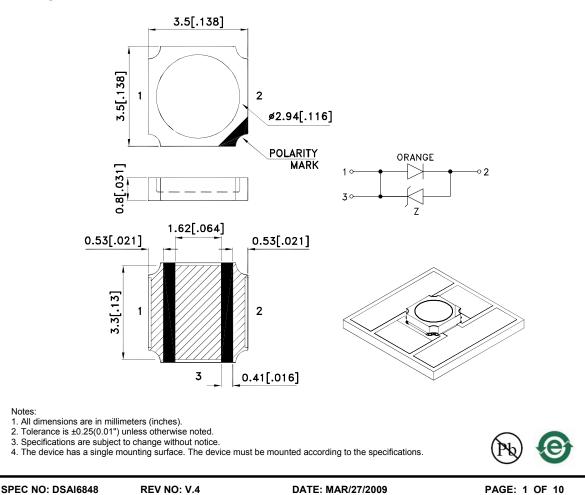
- 1.Dimensions : 3.5mm X 3.5mm X 0.8mm.
- 2. Higher brightness .
- 3.Small package with high efficiency .
- 4.Surface mount technology .
- 5.ESD protection .
- 6.Moisture sensitivity level : level 2a.
- 7.Soldering methods: IR reflow soldering.
- 8.RoHS compliant.

Material as follows:

Package : Ceramics Encapsulating resin : Silicone resin Electrodes : Ag plating

Part Number: AT3535SE9ZS-RV

Package Dimensions



DRAWN: D.M.Su

ERP: 1212000014



APPROVED: WYNEC

CHECKED: Allen Liu

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Selection Guide

Part No.	Dice	Φν (lm) [2] @ 350mA			Viewing Angle [1]
		Code.	Min.	Max.	201/2
AT3535SE9ZS-RV		B1	10	12	120°
		B2	12	14	
	Reddish-Orange (AlGaInP)	В3	14	17	
		B4	17	20	
		B5	20	24	

Notes:

01/2 is the angle from optical centerline where the luminous intensity is 1/2 the optical centerline value.
Luminous intensity / luminous flux: +/-15%.

Absolute Maximum Ratings at TA = 25°C

Parameter	Symbol	Value	Unit
DC Forward Current [1]	lF	350	mA
Peak Forward Current [2]	Іғм	500	mA
Power dissipation	Pt	1.05	W
Operating Temperature	Тор	-40 To +100	°C
Storage Temperature	Tstg	-40 To +120	°C
Junction temperature[1]	TJ	120	°C
Thermal resistance [1] (Junction/ambient)	Rth j-a	95	°C/W
Thermal resistance [1] (Junction/solder point)	Rth j-s	30	°C/W

Notes:

1. Results from mounting on metal core PCB, mounted on pc board-metal core PCB is recommend

for lowest thermal resistance. 2. 1/10 Duty Cycle, 0.1ms Pulse Width.

Electrical / Optical Characteristics at TA = 25°C

Parameter	Symbol	Value	Unit
Forward Voltage IF = 350mA [Min.]		2.0	
Forward Voltage IF = 350mA [Typ.]	VF [2]	2.5	V
Forward Voltage IF = 350mA [Max.]		3.0	
Luminous Flux IF = 350mA [Typ.]	Φν	16	lm
Wavelength at peak emission IF = 350mA [Typ.]	λpeak	635	nm
Dominant Wavelength IF = 350mA [Typ.]	λ dom [1]	623	nm
Spectral bandwidth at 50% $\Phi_{\text{REL MAX}}$ IF = 350mA [Typ.]	Δλ	22	nm
Temperature coefficient of $\lambda peak$ Ir = 350mA, - $10^{\circ}C \leq T \leq 100^{\circ}C$ [Typ.]	TCλpeak	0.1	nm/°C
Temperature coefficient of λ dom IF = 350mA, - 10°C $\leq T \leq 100°C$ [Typ.]	TCλdom	0.08	nm/°C
Temperature coefficient of VF IF = 350mA, - $10^{\circ}C \le T \le 100^{\circ}C$ [Typ.]	TCv	-3.0	mV/°C

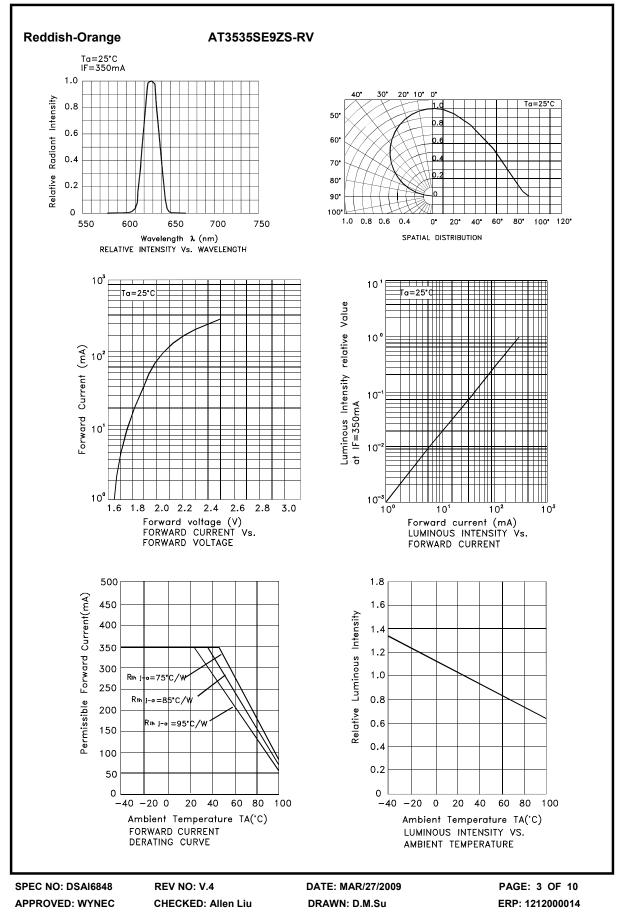
Notes:

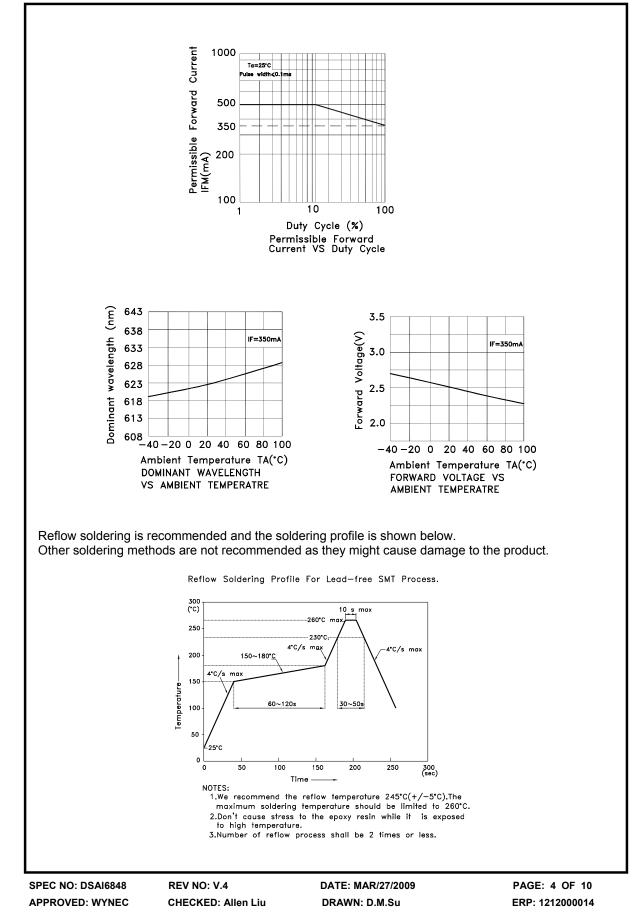
1.Wavelength : + / -1nm.

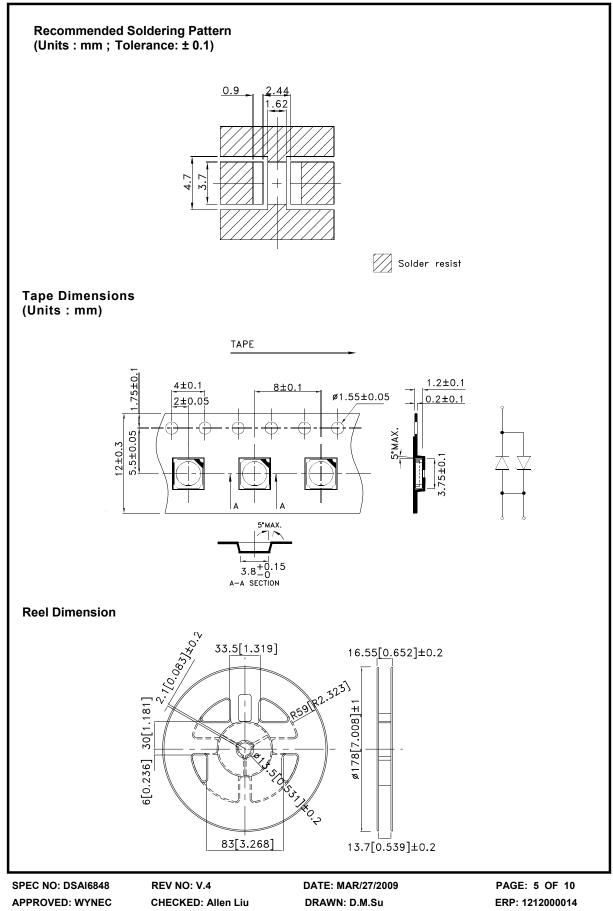
2. Forward Voltage : + / - 0.1V.

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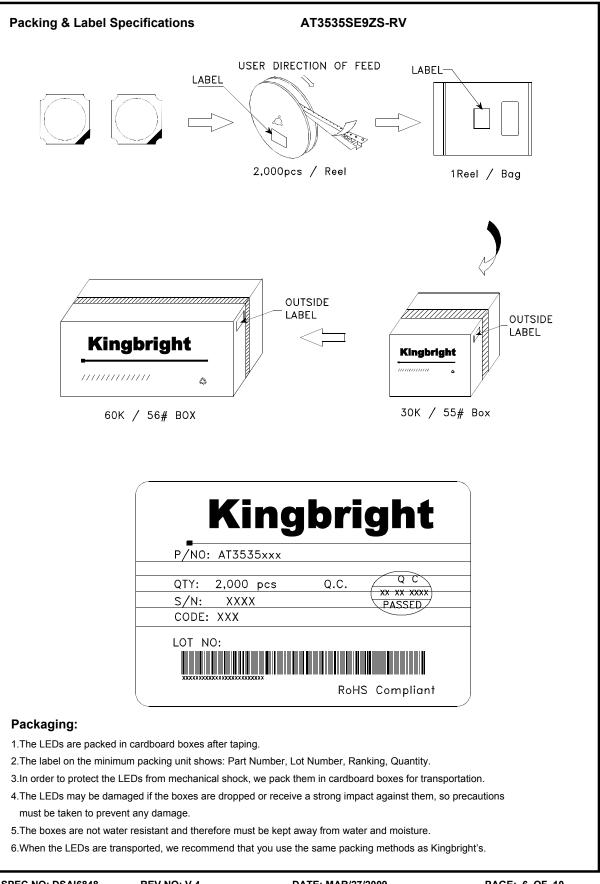
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JEDEC Moisture Sensitivity:

Level	Floor	Floor Life		Soak Requirements			
Level	FIOU	Life	Standard Accelerated I		ated Equivalent		
	Time	Conditions	Time (hours)	Conditions	Time (hours)	Conditions	
2a	4 weeks	\leq 30 °C / 60% RH	696 ² + 5 / - 0	30 °C / 60% RH	120 + 1 / - 0	60 °C / 60% RH	

Notes:

1. CAUTION - The "accelerated equivalent" soak requirements shall not be used until correlation of damage response, including electrical, after soak and reflow is established with the "standard" soak requirements or if the known activation energy for diffusion is 0.4 - 0.48 eV. Accelerated soak times may vary due to material properties, e.g., mold compound, encapsulant, etc. JEDEC document JESD22-A120 provides a method for determining the diffusion coefficient.

2. The standard soak time includes a default value of 24 hours for semiconductor manufacturer's exposure time (MET) between bake and bag and includes the maximum time allowed out of the bag at the distributor's facility.

If the actual MET is less than 24 hours the soak time may be reduced. For soak conditions of 30 $^{\circ}$ C/60% RH the soak time is reduced by one hour For each hour the MET is less than 24 hours. For soak conditions of 60 $^{\circ}$ C/60% RH, the soak time is reduced by one hour for each five hours the MET is less than 24 hours.

If the actual MET is greater than 24 hours the soak time must be increased. If soak conditions are 30 $^{\circ}$ C/60% RH, the soak time is increased one Hour for each hour that the actual MET exceeds 24 hours. If soak conditions are 60 $^{\circ}$ C/60% RH, the soak time is increased one hour for each five Hours that the actual MET exceeds 24 hours.

3. Supplier may extend the soak times at their own risk.

ESD Protection During Production

Electric static discharge can result when static-sensitive products come in contact with the operator or other conductors.

The following procedures may decrease the possibility of ESD damage:

1. Minimize friction between the product and surroundings to avoid static buildup.

2.All production machinery and test instruments must be electrically grounded.

3.Operators must wear anti-static bracelets.

4.Wear anti-static suit when entering work areas with conductive machinery.

5.Set up ESD protection areas using grounded metal plating for component handling.

6.All workstations that handle IC and ESD-sensitive components must maintain an electrostatic potential of 150V or less.

7. Maintain a humidity level of 50% or higher in production areas.

8.Use anti-static packaging for transport and storage.

9.All anti-static equipment and procedures should be periodically inspected and evaluated for proper functionality.

Heat Generation:

- 1.Thermal design of the end product is of paramount importance. Please consider the heat generation of the LED when making the system design. The coefficient of temperature increase per input electric power is affected by the thermal resistance of the circuit board and density of LED placement on the board ,as well as other components. It is necessary to avoid intense heat generation and operate within the maximum ratings given in this specification.
- 2.Please determine the operating current with consideration of the ambient temperature local to the LED and refer to the plot of Permissible Forward current vs. Ambient temperature on CHARACTERISTICS in this specification. Please also take meas ures to remove heat from the area near the LED to improve the operational characteristics on the LED.

3. The equation ① indicates correlation between T_j and T_a ,and the equation ② indicates correlation between T_j and T_s

Tj = Ta + Rthj-a *W	 1
Tj = Ts + Rthj-s *W	 2

Tj = dice junction temperature: °C

Ta = ambient temperature:°C

Ts = solder point temperature:°C

Rthj-a = heat resistance from dice junction temperature to ambient temperature : °C/ W

Rthj-s = heat resistance from dice junction temperature to Ts measuring point : °C/ W

W = inputting power (IFx VF) : W

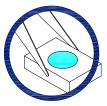
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Handling Precautions

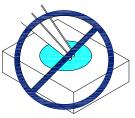
Compare to epoxy encapsulant that is hard and brittle, silicone is softer and flexible. Although its characteristic significantly reduces thermal stress, it is more susceptible to damage by external mechanical force. As a result, special handling precautions need to be observed during assembly using silicone encapsulated LED products. Failure to comply might leads to damage and premature failure of the LED.

1. Handle the component along the side surfaces by using forceps or appropriate tools.

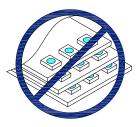


2. Do not directly touch or handle the silicone lens surface. It may damage the internal circuitry.

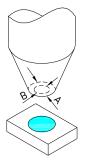




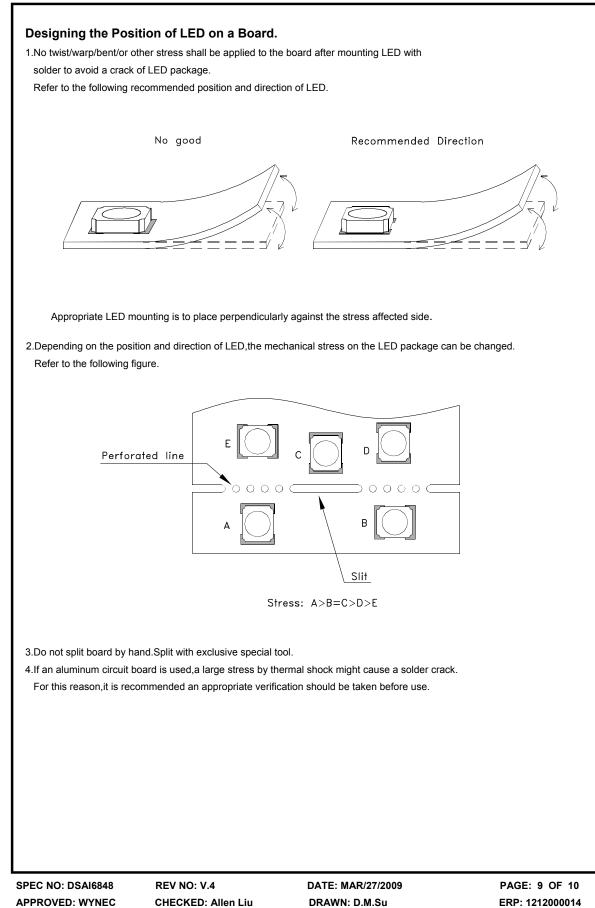
3. Do not stack together assembled PCBs containing exposed LEDs. Impact may scratch the silicone lens or damage the internal circuitry.



- 4. The outer diameter of the SMD pickup nozzle should not exceed the size of the LED to prevent air leaks. The inner diameter of the nozzle should be as large as possible.
- 5. A pliable material is suggested for the nozzle tip to avoid scratching or damaging the LED surface during pickup.
- 6. The dimensions of the component must be accurately programmed in the pick-and-place machine to insure precise pickup and avoid damage during production.



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Reliability Test Items And Conditions

The reliability of products shall be satisfied with items listed below

Lot Tolerance Percent Defective (LTPD): 10%

No.	Test Item	Test Conditions	Test Times / Cycles	Number of Damaged
1	Continuous Operating Test	T_a = 25 $^\circ C$, I_F = 350 mA Tested with standard circuit board $^\circ$	1000 hrs	0/22
2	High Temperature Operating Test	T _a = 100 °C , I _F = 60 mA (note)	1000 hrs	0/22
3	Low Temperature Operating Test	T_a = -40 °C , I_F = 350 mA Tested with standard circuit board $^{\circ}$	1000 hrs	0/22
4	High Temperature and Humidity Storage Operating Test	T _a = 85 °C , RH = 85% , I _F = 110 mA (note)	1000 hrs	0/22
5	Temperature Cycling Test	High temp: +100 °C 30 mins \int R.T : 5 mins \int Low temp : -40 °C 30 mins \int R.T : 5 mins	10 cycles	0/22
6	Thermal Shock Test	High temp: +100 °C 5 mins ∫ Low temp: -40 °C 5 mins	1000 cycles	0/22
7	Soldering resistance Test	T _{sld} = 260 °C , 10 secs	10 secs	0/22

Note: Thermal resistance of LED with Kingbright circuit board : Rthj-a =95°C/W

Failure Criteria

ltem	Symbol	Test Conditions	Criteria for Judgement	
			Min.	n. Max.
Forward Voltage	VF	I⊧ = 350mA	-	Initial Level x 1.1
Luminous Flux	Φν	I⊧ = 350mA	Initial Level x 0.7	-
Note: The test is performed after	the board is cooled down to t	he room temperature.		

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