Messrs.									
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# LCD MODULE SPECIFICATION FOR CUSTOMER'S APPROVAL

CUSTOMER :

MODULE TYPE : <u>NMTG-S12864BFYHSGY-B</u>

APPROVED BY: (FOR CUSTOMER USE ONLY)							

Approved by	Check	Made by	
微端	微端	微端	微端
2008/06/18	2008/06/18	2008/06/18	2008/06/18
李剛	蔡宜夢	陳世文	陳雅靖



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# **SPECIFICATION FOR**

# LIQUID CRYSTAL DISPLAY MODULE

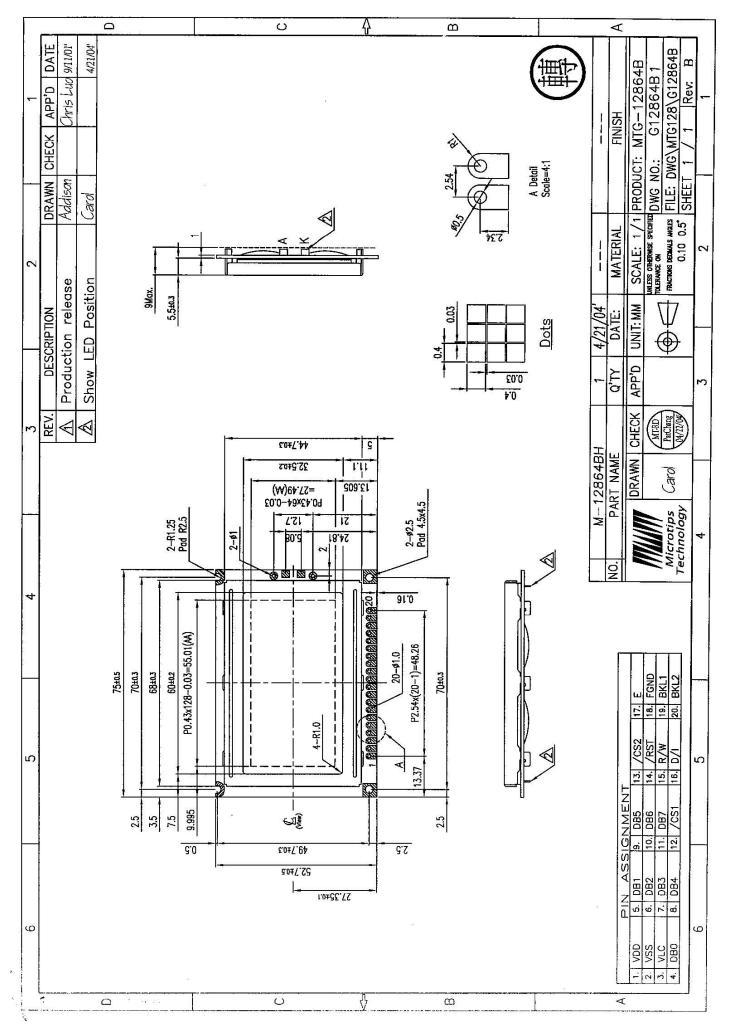
# MODEL NO. : <u>NMTG-S12864BFYHSGY-B</u>

View Direction	⊠ 6 O′clock				□12 O'clock					
	🗆 FSTN I	Positive	Ĵ			□ FS <sup>-</sup>	□ FSTN Negative			
LCD Type	□ stn g	ray		ST	N Yello	ow Gr	een		STN	Blue
Rear Polarizer	🗆 Reflective 🗹 1			Tr	ansflective				Trans	smissive
Backlight Type	☑ LED	🗆 EL			🗹 Inter	ternal Power			☑ 5V input	
backlight type			FL		🗆 Extei	rnal Power		r	□ 24V input	
Backlight Color	□ White	□ A	hmb	er	□ <sup>Blu</sup> Gr			Yell Gre	ow en	□ Other
Temperature Range	□ Normal			V	☑ Wide			□ Super Wide		
EL Driver IC	□ Build-in				☑ Not Build-in					
Touch Screen	□ With				🗹 Without					
LCD LSI	SBN6400	SBN640G21 / SBN0064G-D								

# **TO BE VERY CAREFUL !**

The LCD driver ICs are made by CMOS process, which are very easy to be damaged by static charge, make sure the user is grounded when handling the LCM.





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#### **GENERAL SPECIFICATION**

Item	Content
Display Resolution	128(W)×64(H)
Dimensional Outline(mm)	75.0(W)×52.7(H)×9.0max(D)
Dot Size	0.40(W)mm×0.40(H)mm
Dot Pitch	0.43(W)mm×0.43(H)mm
Display mode	Transflective/ Positive Type
Circuit	Common-Driver IC, Segment-driver IC with build-in SRAM
Interface	Data (D0~D7), D/I, R/W, E, RST, CS1, CS2, V <sub>EE</sub>

# **ABSOLUTE MAXIMUN RATING**

(1) Electrical Absolute Ratir	ngs				
Item	Symbol	Min.	Max.	Unit	Note
Power Supply for Logic	$V_{DD}$ - $V_{SS}$	-0.3	7.0	Volt	
Power Supply for LCD	$V_{DD}$ - $V_{EE}$	-0.3	19.0	Volt	-
Input Voltage	V <sub>1</sub>	-0.3	V <sub>DD</sub> +0.3	Volt	-
Current for LED backlight		_	200	mA	-
Static Electricity	-			-	Note 1

Note 1: Operator should be grounded during handling LCM.

#### (2) Environmental Absolute Maximum Ratings

	Normal Temperature				Wide Temperature			
Item	Operating		Storage		Operating		Storage	
	Max,	Min.	Max,	Min.	Max,	Min.	Max,	Min.
Ambient Temperature	0°C	+50°C	-20°C	+70°C	-20°C	+70°℃	-30°℃	+80°C
Humidity(without condensation)	Note 2,4		Note 3,5		Note 4,5		Note 4,6	

Note 2 Ta $\leq$  50°C: 80% RH max

Ta>50°C: Absolute humidity must be lower than the humidity of 85%RH at 50°C

Note 3 Ta at  $-20^{\circ}$ C will be<48hrs at  $70^{\circ}$ C will be <120hrs when humidity is higher than 75%.

Note 4 Background color changes slightly depending on ambient temperature. This phenomenon is reversible.

Note 5 Ta $\leq$ 70°C:75RH max

Ta>70°C: absolute humidity must be lower than the humidity of 75%RH at 70°C

Note 6 Ta at -30 $^{\circ}$ C will be <48hrs, at 80  $^{\circ}$ C will be <120hrs when humidity is higher than 75%.



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Item	Symbol	Condition	Min.	Тур	Max.	Unit	note
Power Supply for Logic	V <sub>DD</sub> -V <sub>SS</sub>	-	4.5	5.0	5.5	Volt	-
	V <sub>IL</sub>	L level	0		0.8	Volt	
Input Voltage	V <sub>IH</sub>	H level	V <sub>DD</sub> -2.2		$V_{DD}$	Volt	
LCM Recommend LCD Module		$T_a = -20^{\circ}C$	9.14	9.64	10.14		
	$V_{\text{DD}}$ – $V_{\text{EE}}$	$T_a = 25^{\circ}C$	8.60	50 9.10 9.60 Vo		Volt	-
Driving Voltage		$T_a = 70^{\circ}C$	8.06	8.56	9.06		
Power	I <sub>dd</sub> (LED B/L OFF)			1.5	1.8		
Supply Current for	I <sub>EE</sub>	$V_{DD} = 5.0V$ $T_a = 25^{\circ}C$		0.6	1.0	mA	
LCM	I <sub>LED</sub>	$V_{DD} - V_{EE} = 10.3V$		100	150		
Power Supply for EL Backlight	$V_{\text{EL}}$	$V_{LED}$ =5.0V	-	100V/ 400Hz	-		

#### **ELECTRICAL CHARACTERISTICS**

# **OPTICAL CHARACTERISTICS**

Item	Symbol	Condition	Min.	Тур	Max.	Unit	note	
Viewing	Front-Back	θ =0°	30	92	-	deg.	-	
Angle	Left-Right	θ =0°	60	90	-	deg.	-	
Rise Time	T <sub>r</sub>	V <sub>DD</sub> -V <sub>EE</sub>		90	220	ing C		
Fall Time	T <sub>f</sub>	=10.3V		210	200	mS		
Contrast	Cr	Ta=25℃	2.0	6.0	-		7	



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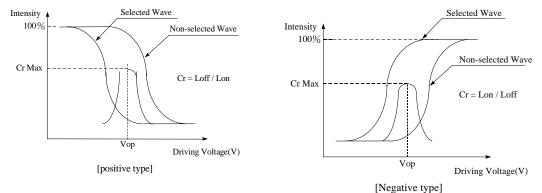
# **INTERFACE PIN ASSIGNMENT**

Pin No.	Pin Out	I/O	Description
1	$V_{\text{DD}}$		Logic supply voltage
2	V <sub>ss</sub>		GND
3	V <sub>ee</sub>		Supply Voltage for LCD panel.
4   11	DB0   DB7	I/O	Data bus. 3-state I/O common terminal.
12	/CS1	I	Chip-select for the left half of the display. Active LOW.
13	/CS2	I	Chip-select for the right half of the display. Active LOW.
14	/RST	I	<ul> <li>Setting the RES signal to Low level can initialize the following registers.</li> <li>1. ON/OFF register 0 set(Display off)</li> <li>2. Display start line register 0 set(display starts from line 0)</li> <li>After releasing reset, this condition can be changed only by software.</li> </ul>
15	R/W	I	Read/Write R/W=high : Data of DB0~DB7 can be read by CPU. R/W=low : Data of DB0~DB7 can be written into LCD driver IC at the falling edge of E when CS1 and CS2 is high.
16	D/I	I	Data/Instruction D/I=high:Indicates that data of DB0~DB7 is display data. D/I=low:Indicates that data of DB0~DB7 is instruction.
117	E	I	Enable When write(R/W=low) : Data of DB0~DB7 is latched at the fall of E When read(R/W=high) : Data is read while E is at high level.
18	FGND		Frame Ground
19	BKL <sub>A</sub>		Power supply for backlight. (4.2V/100~150 mA DC for LED backlight, 110V/400Hz AC
20	BKL <sub>K</sub>		for EL)

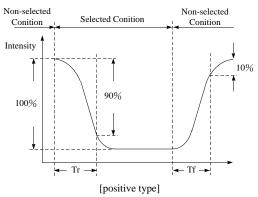




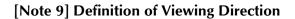
#### [Note 7] Definition of Operation Voltage (Vop)

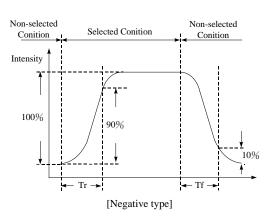


#### [Note 8] Definition of Response Time (Tr, Tf)

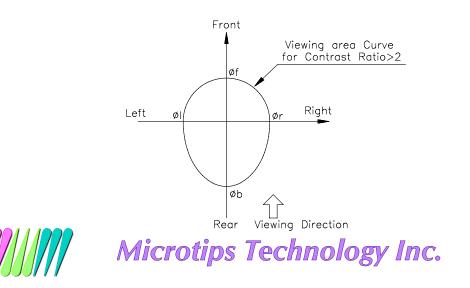


Conditions: Operating Voltage : Vop Frame Frequency : 64 Hz



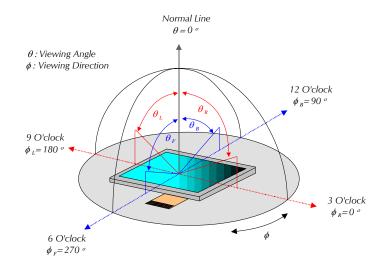


Viewing Angle( $\theta$ ,  $\varphi$ ): 0°, 0° Driving Wave form : 1/N duty, 1/a bias

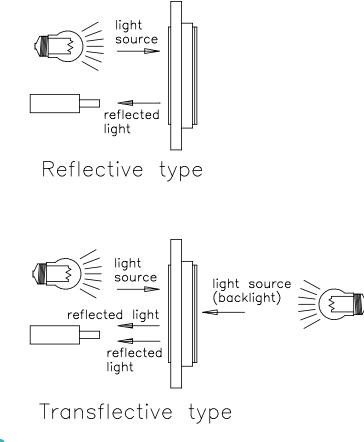


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#### [Note 10] Definition of viewing angle



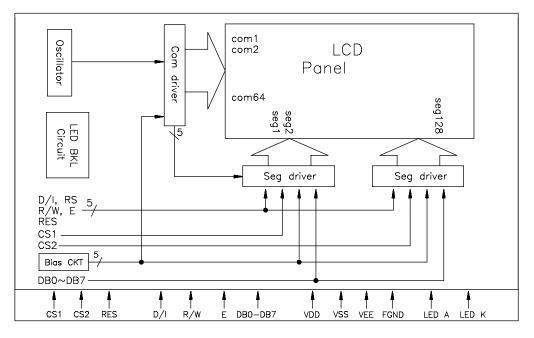
[Note 11] Description of Measuring Equipment



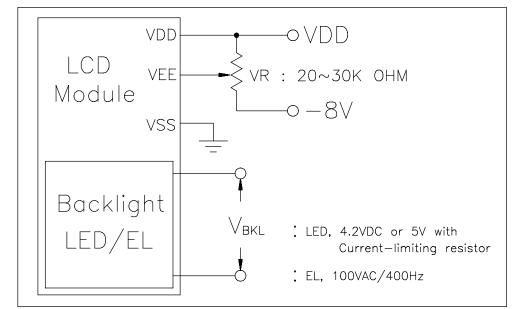


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Draduat Crastination	Model:	NMTG-S12864BFYHSGY-B	Rev. No.	Issued Date.	Page.
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# **BLOCK DIAGRAM**



# **POWER SUPPLY**





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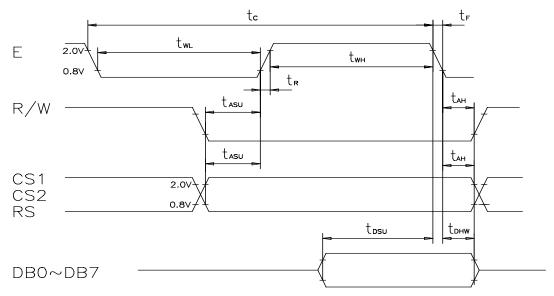
## **TIMING CHARACTERISTICS**

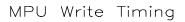
MPU interface timing: (V\_{SS} =0V, V\_{DD}=4.5V~5.5V, T\_a=-20 to  $60^{\circ}$ C)

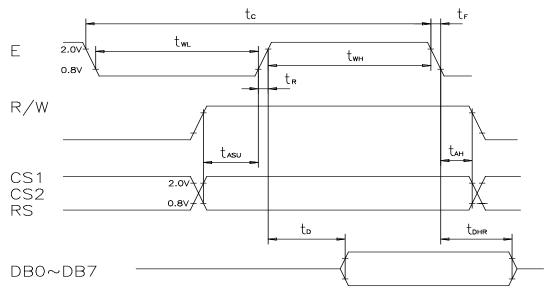
Item	Symbol	Min.	Тур.	Max.	Unit
E Cycle Time	t <sub>C</sub>	1000			ns
E High Level Width	t <sub>wH</sub>	450	-	-	ns
E Low Level Width	t <sub>WL</sub>	450	-	-	ns
E Rise Time	t <sub>R</sub>			25	ns
E Fall Time	t <sub>F</sub>			25	ns
Address Setup Time	t <sub>ASU</sub>	140	-	-	ns
Address Hold Time	t <sub>AH</sub>	10	-	-	ns
Data Setup Time	t <sub>DSU</sub>	200	-	-	ns
Data Delay Time	t <sub>D</sub>		-	320	ns
Data Hold Time(Write)	t <sub>DHW</sub>	10		-	ns
Data Hold Time(Read)	t <sub>DHR</sub>	20			ns

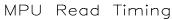














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# **DISPLAY COMMANDS**

The display commands shown below control the internal state of the LCD driver ICs. Commands are sent from CPU to LCD module for the display control.

Command	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Function	
Display ON/OFF	0	0	0	0	1	1	1	1	1	1/0	To control the display ON or OFF. The internal status and display RAM data are not affected. 0:OFF, 1:ON	
Set address (Y address)	0	0	0	1	V addross (0~63)						To set the Y address in the Y address counter.	
Set page (X address)	0	0	1	0	1	1	1	Pa	ıge(0∽	·7)	To set the X address at the X address register.	
Display Start Line	0	0	1	1	[	Displa	y Star	t Line	(0~63	To indicate the display data RAM displayed at the top of the screen.		
Status Read	0	1	Busy	0	ON/ Off	Rese t	0	0	0	0	To read status of the LCD controller IC: Busy 0:Ready, 1: In operation ON/OFF: 0:Display ON, 1:Display OFF Reset: 0:Normal, 1:Reset	
Write display data	1	0			Write Data						To write data into display data RAM. Y address is increased by 1 after this command.	
Read Display data	1	1		Read Data							To read data from display data RAM to the data bus.	



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## **MEMORY MAPPING**

	ationship betw					/	• •	14.0	-				
			<b>R</b> /	AM	Ya	ddress(Y	U ~\	(12)	7)				Data
	Line 0→	0 1 1	1	0	0		0	0	1	0	0	0	←DB0(LSB)
Ô	Line 1→	1 0 0	0	1	0		0	0	1	1	0	0	←DB1
page(X=0)	Line 2→	1 0 0	0	1	0		0	0	1	0	1	0	←DB2
ge	Line 3→	1 0 0	0	1	0		0	0	1	0	1	0	←DB3
ра	:	1 1 1	1	1	0		0	0	1	0	0	0	←DB4
st		1 0 0	0	1	0		1	1	1	0	0	0	←DB5
<u>_</u>		1 0 0	0	1	0		1	1	1	0	0	0	←DB6
	Line 7→	0 0 0	0	0	0		0	0	0	0	0	0	←DB7(MSB)
	Line 8→	1 1 1	1	0	0		0	1	1	1	0	0	←DB0(LSB)
=1)	Line 9→	1 0 0	0	1	0		0	1	0	0	1	0	←DB1
page(X=1)	Line 10→	1 0 0	0	1	0		0	1	0	0	1	0	←DB2
age		1 1 1	1	0	0		1	1	1	0	1	0	←DB3
bg		1 0 0	0	1	0		0	1	0	0	1	0	←DB4
q		1 0 0	0	1	0		0	1	0	0	1	0	←DB5
2nd		1 1 1	1	0	0		0	1	1	1	0	0	←DB6
	Line $15 \rightarrow$	0 0 0	0	0	0		0	0	0	0	0	0	←DB7(MSB)
		: :	:	: :			: :						
			-										
	Line 56→	1 0 0	0	1			0	0	0	0	0		←DB0(LSB)
		1 0 0		1	0		0	0	0	0		0	←DB1
page(X=7)		1 0 0	0	1	0		0	1	0	0	1	0	←DB2
age		1 1 1	1	1	0		1	0	1	0	1		←DB3
bg	÷	1 0 0	0	1	0		1	0	0	1	0	0	←DB4
Ļ		1 0 0	0	1	0		1	0	0	1	0	0	←DB5
8th	Line 62 $\rightarrow$	1 0 0	0	1	0		0	1	1	0	1	0	←DB6
	Line 63 $\rightarrow$	0 0 0	0	0	0								←DB7(MSB)

Relationship between RAM data and display



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# ADDRESS CONFIGURATION OF DISPLAY DATA RAM

			Y address		
	0 1	2	3126	127	_
	DB0				Line0
Х=0→	То		Page0		То
	DB7				Line7
	DB0				Line8
X=1→	То		Page1		То
	DB7				Line15
	DB0				Line16
X=2→	То		Page2		То
	DB7				Line23
	DB0				Lint24
X=3→	То		Page3		То
	DB7				Line31
	DB0				Line32
X=4→	То		Page4		То
	DB7				Line39
	DB0				Line40
$X=5 \rightarrow$	То		Page5		То
	DB7				Line47
	DB0				Line48
X=6→	То		Page6		То
	DB7				Line55
	DB0				Line56
X=7→	То		Page7		То
	DB7				Line63

Address configuration of Display Data RAM



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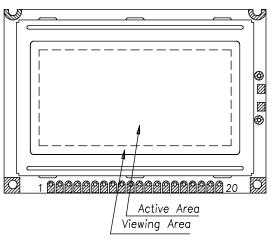
## **RELIABILITY TEST**

No	Item	Conditions		Note
1	High Temp. Operation	70°C	240 Hr	
2	High Temp. Storage	80°C	240 Hr	
3	Low Temp. Operation	-20°C	240 Hr	
4	Low Temp. Storage	-30°C	240 Hr	
5	High Temp./Humid Storage	60℃ 90%RH	240 Hr	
6	Thermal Shock	-20℃ ,30min +60℃ ,30min	10 cycles	
7	Vibration Test ( IEC-68-2-6 )	Frequency : 10~55 Hz Duration : 20 times, 6 min/time Amplitude : 0.75 mm	-	
8	Shock ( IEC 68-2-27)	Duration : 11 mS Acceleration : 100g	-	X, Y, Z direction

# **APPEARANCE CHECK**

#### CONDIITON OF APPEARANCE CHECK:

(1)Specimen shall be checked by eyes in distance of 30cm under 40w-fluorescence lamp. (2)Checking direction shall be in 45 degree from perpendicular line op specimen surface.





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#### HANDLING PRECAUTIONS

(1)Treat polarizer very carefully since it is easy to be damaged.

- (2)When cleaning the display surface, use soft cloth (e.g. gauss) with a solvent (recommended below) and wipe lightly.
  - ethyl alcohol
  - ♦ iso-prcolol

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface.

Do not use the following solvents:

- ♦ water
- ketone
- aromatics
- (3)Direct current causes electro-chemical reaction with remarkable degradation of the display quality. Give careful consideration to prevent direct current at ON/OFF timing and during operation.
- (4) Avoid strong shock and drop from the height.
- (5)To prevent LCD panels from degradation, do not operate or store them exposed directly to sunshine or high temperature/humidity.
- (6) Give careful consideration to avoid electrical static discharge with causes uneven contrast.
- (7)Even a small condensation on the contact pads (terminals) causes electro-chemical reaction which makes missing row and column. Give careful attention to avoid condensation. When assembling with zebra connector, clean the surface of the pads with alcohol and keep the air very clean.



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# LCD PRODUCT QUALITY STANDARD

DISPLAY APPEARANCE

No	Item	Criteria
1	inclusions (black spot, white spot, dust)	(1) round type diameter mm(a*)no of defect* neglect $a \le 0.20$ neglect $0.20 < a \le 0.35$ 5max $0.35 < a$ none(2) linear type length mm(l)width mm(W)no. of defect $na$ $W \le 0.03$ neglect $l \le 3$ $0.03 < W \le 0.08$ 6 $3 < l$ $0.08 < W$ none
2	scratch	1. scratch on protective film is permitted.2. scratch on polarizer shall be as follow: (1) round type diameter mm(a*) no of defect $a \le 0.15$ neglect $0.15 < a \le 0.20$ 2 max $0.20 < a$ none (2) linear type be judged by e 1(2) linear type
3	dent	diameter < 1.5mm
4	bubble	not exceeding 0.5mm average diameter is acceptable between glass and polarizing film
5	pin hole	$(a+b)/2 \leq 0.15$ mm maximum number: ignored $0.15 < (a+b)/2 \leq 0.20$ mm maximum number:10
6	dot defect	$(a+b)/2 \leq 0.20$ mm maximum number: ignored $0.20 < (a+b)/2 \leq 0.30$ mm maximum number:5 x=width
7	contrast irregularity(spot)	A induitdiameter specno of defect $a \le 0.50 \text{ mm}$ neglect $0.50 < a \le 0.75$ 5 $0.75 < a \le 1.00$ 3 $1.00 < a$ none
8	dot width	design width ±15%
9	color tone and uniformity	obvious uneven color is not permitted



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#### WARRANTY

This product has been manufactured to your company's specifications as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in medical devices, nuclear power control equipment, aerospace equipment, fire and security systems, or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required. If the product is to be used in any of the above applications, we will need to enter into a separate product liability agreement.

- 1 13 months guarantee starts from the date code.
- 2 We cannot accept responsibility for any defect, which may arise from additional manufacturing of the product (including disassembly and reassembly), after product delivery.
- 3 We cannot accept responsibility for any defect, which may arise after the application of strong external force to the product.
- 4 We cannot accept responsibility for any defect, which may arise due to the application of static electricity after the product has passed your company's acceptance inspection procedures.
- 5 We cannot accept responsibility for industrial property, which may arise through the use of your product, with exception to those issues relating directly to the structure or method of manufacturing of our product. Microtips-origin longer than one year from Microtips production.



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Messrs.					
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## **REVISION HISTORY**

Rev	Revision Content	Page	Date
А	Initial Release	1~20	2000/10/03
В	Change IC and Add Warranty	19	2008/06/18

