

ESDA8V2-1J

EOS and ESD Transil[™] protection for charger and battery port

Features

- Breakdown voltage V_{BR} = 8.2 V
- Unidirectional device
- High peak power dissipation: 500 W (8/20 µs waveform)
- ESD protection level better than IEC 61000-4-2, level 4: 30 kV contact discharge
- Low leakage current (< 0.5 µA @ 5 V)

Benefits

- High EOS and ESD protection level
- High integration
- Suitable for high density boards

Complies with the following standards:

- IEC 61000-4-2 level 4
 - ±15 kV (air discharge)
 - ±8 kV (contact discharge)
- MIL STD 883G Method 3015-7: class 3B
 − HBM (human body model): ≥8kV

Applications

This product is particularly recommended for the protection of power supply lines of portable devices, where EOS and ESD transient overvoltage protection in sensitive equipment is required, such as:

- Computers
- Printers
- Communication systems
- Cellular phone handsets and accessories
- Video equipment

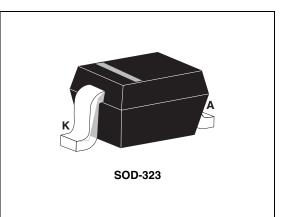
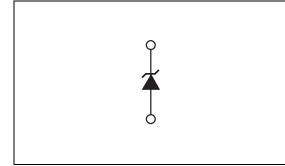


Figure 1. Functional diagram (top view)



Description

The ESDA8V2-1J is a unidirectional single line Transil diode designed specifically for the protection of integrated circuits in portable equipment and miniaturized electonic devices subject to EOS and ESD transient overvoltages.

TM: Transil is a trademark of STMicroelectronics

August 2009

1 Characteristics

Table 1.	Absolute	maximum	ratings	(T _{amb} = 25 °C)
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Symbol	Parameter	Value	Unit
V _{PP}	ESD discharge: IEC 61000-4-2 air discharge on input pin IEC 61000-4-2 contact discharge on input pin MIL STD 883G - Method 3015-7: class 3B	±30 ±30 ±30	kV
P _{PP}	Peak pulse power dissipation (8/20 μ s) ⁽¹⁾ $T_{j \text{ initial}} = T_{amb}$	500	W
I _{PP}	Peak pulse current (8/20 μs)	25	А
Тj	Junction temperature range	-40 to +125	°C
T _{stg}	Storage temperature range	- 55 to +150	°C
TL	Maximum lead temperature for soldering during 10 s	260	°C

1. For a surge greater than the maximum values, the diode will fail in short-circuit

Table 2. Electrical characteristics (definitions)

Symbol	Parameter	
V _{BR}	Breakdown voltage	
I _{RM}	Leakage current @ V _{RM}	
V _{RM}	Stand-of voltage	
V _{CL}	Clamping voltage	
I _{PP}	Peak pulse current	
С	Input capacitance	

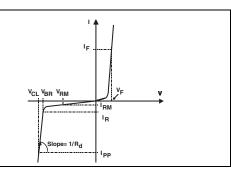


Table 3. Electrical characteristics (values, T_{amb} = 25 °C)

Symbol	Parameter	Test condition	Min.	Тур.	Max.	Unit
V _{BR}	Breakdown voltage	I _R = 1 mA	8.2	-	-	V
I _{RM}	Leakage current @ V _{RM}	V _{RM} = 5 V	-	0.1	0.5	μA
V _{CL}	Clamping voltage (8/20 µs waveform)	I _{PP} = 1 A	-	-	11	V
		I _{PP} = 5 A	-	-	13	V
		I _{pp} = 25 A	-	-	20	V
С	Input capacitance	V_{R} = 0 V, F _{osc} = 1 MHz, V_{osc} = 30 mV	-	210	250	pF



Figure 2. Relative variation of peak pulse power versus initial junction temperature

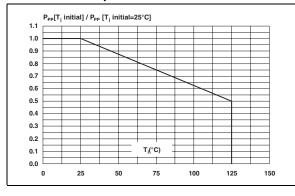
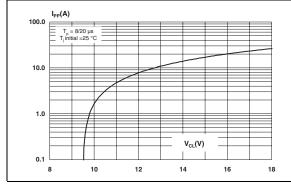


Figure 4. Clamping voltage versus peak pulse current (typical values, exponential waveform)



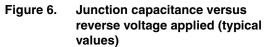


Figure 3. Peak pulse power versus exponential pulse duration

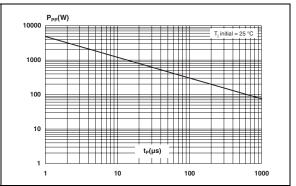


Figure 5. Forward voltage drop versus peak forward current (typical values)

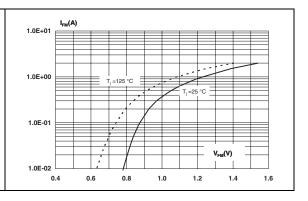
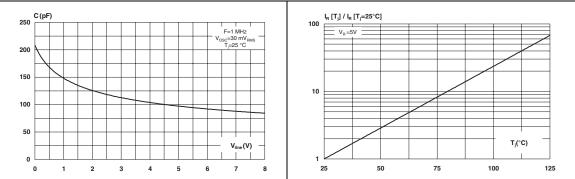
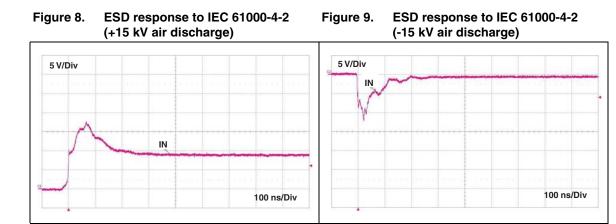


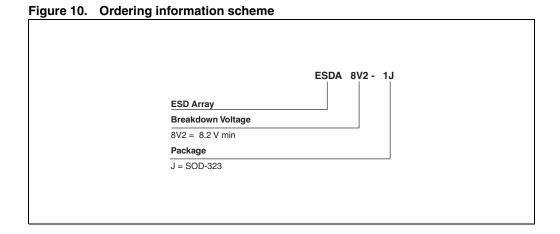
Figure 7. Relative variation of leakage current versus juntion temperature (typical values)



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2 Ordering information scheme



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3 Package information

- Epoxy meets UL94, V0
- Bar indicates cathode

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: <u>www.st.com</u>. ECOPACK[®] is an ST trademark.

Table 4. SOD-323 dimensions

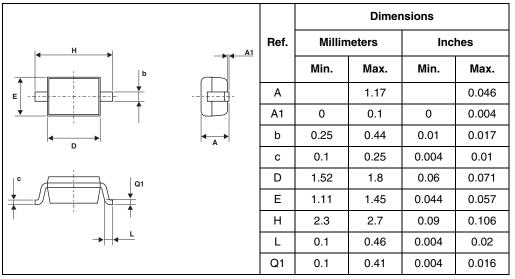
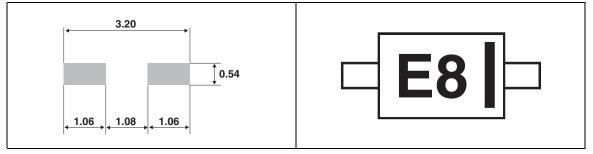




Figure 12. Marking



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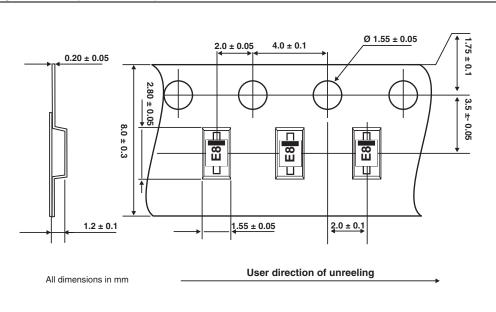


Figure 13. Tape and reel specifications

4 Ordering information

Table 5. Ordering information

Order code	Marking	Weight	Base qty	Delivery mode
ESDA8V2-1J	E8	5 mg	3000	Tape and reel

5 Revision history

Table 6.Document revision history

	Date	Revision	Changes
Ī	11-Aug-2009	1	Initial release



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