

#### **Features**

- Superior circuit protection
- Overcurrent and overvoltage protection
- Blocks surges up to rated limits
- High speed performance
- Small SMT package
- RoHS compliant\*
- Agency recognition: 🗫

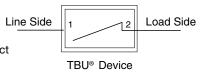
### **Applications**

- Set top box LNB ports
- Protection modules and dongles
- Process control equipment
- Test and measurement equipment
- General electronics

## TBU-KE Series - TBU® High Speed Protectors

#### **General Information**

The TBU-KE Series of Bourns® TBU® (Transient Blocking Unit) products are very low capacitance unidirectional high speed surge protection components designed to protect against faults caused by short circuits, AC power cross, induction and lightning surges.



Description
UL File Number: E315805

The TBU-KE is a unidirectional TBU® device: the TBU® protector will trip in less than 1  $\mu s$  when the current reaches the maximum value in one direction only, that is when Pin 1 is positive in voltage with respect to Pin 2. No current limiting exists in the opposite polarity, and the TBU® protector appears as resistive in nature. The reverse current should not exceed the maximum trip current level of the TBU® device. An external diode may be used to prevent reverse current in DC biased applications.

The TBU® protector blocks surges and provides an effective barrier behind which sensitive electronics will not be exposed to large voltages or currents during surge events. After the surge, the TBU® device resets when the voltage across the TBU® device falls to the V<sub>reset</sub> level. The TBU® device will automatically reset on lines which have no DC bias or have DC bias below V<sub>reset</sub> (such as unpowered signal lines).

The TBU® device is provided in a surface mount DFN package and meets industry standard requirements such as RoHS and Pb Free solder reflow profiles.

### Absolute Maximum Ratings (@ T<sub>A</sub> = 25 °C Unless Otherwise Noted)

Symbol	Parameter	Part Number	Value	Unit
		TBU-KE025-xxx-WH	250	
V <sub>imp</sub>	Peak impulse voltage withstand with duration less than 10 ms	TBU-KE040-xxx-WH	400	V
'		TBU-KE050-xxx-WH	500	
		TBU-KE025-xxx-WH	100	
V <sub>rms</sub>	Continuous A.C. RMS voltage	TBU-KE040-xxx-WH	200	V
		TBU-KE050-xxx-WH	250	
T <sub>op</sub>	Operating temperature range		-40 to +85	°C
T <sub>stq</sub>	Storage temperature range	-65 to +150	°C	

## **BOURNS**®

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<sup>\*</sup>RoHS Directive 2002/95/EC Jan 27, 2003 including Annex. Specifications are subject to change without notice. Customers should verify actual device performance in their specific applications.

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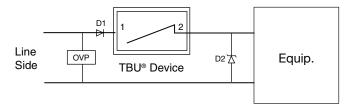
### Electrical Characteristics (@ $T_A$ = 25 °C Unless Otherwise Noted)

Symbol	Parameter	Min.	Тур.	Max.	Unit		
I <sub>trigger</sub>	Current required for the protected state	e device to go from operating state to	TBU-KExxx-050-WH TBU-KExxx-100-WH TBU-KExxx-200-WH TBU-KExxx-300-WH TBU-KExxx-500-WH	50 100 200 300 500	75 150 300 450 750	100 200 400 600 1000	mA
R <sub>device</sub>	Series resistance of the TBU <sup>®</sup> device	V <sub>imp</sub> = 250 V I <sub>trigger</sub> (min.) = 50 mA V <sub>imp</sub> = 250 V I <sub>trigger</sub> (min.) = 100 mA V <sub>imp</sub> = 250 V I <sub>trigger</sub> (min.) = 200 mA V <sub>imp</sub> = 250 V I <sub>trigger</sub> (min.) = 300 mA V <sub>imp</sub> = 250 V I <sub>trigger</sub> (min.) = 500 mA V <sub>imp</sub> = 400 V I <sub>trigger</sub> (min.) = 50 mA V <sub>imp</sub> = 400 V I <sub>trigger</sub> (min.) = 100 mA V <sub>imp</sub> = 400 V I <sub>trigger</sub> (min.) = 200 mA V <sub>imp</sub> = 400 V I <sub>trigger</sub> (min.) = 300 mA V <sub>imp</sub> = 400 V I <sub>trigger</sub> (min.) = 500 mA V <sub>imp</sub> = 400 V I <sub>trigger</sub> (min.) = 500 mA V <sub>imp</sub> = 500 V I <sub>trigger</sub> (min.) = 100 mA V <sub>imp</sub> = 500 V I <sub>trigger</sub> (min.) = 200 mA V <sub>imp</sub> = 500 V I <sub>trigger</sub> (min.) = 200 mA V <sub>imp</sub> = 500 V I <sub>trigger</sub> (min.) = 300 mA V <sub>imp</sub> = 500 V I <sub>trigger</sub> (min.) = 300 mA V <sub>imp</sub> = 500 V I <sub>trigger</sub> (min.) = 300 mA V <sub>imp</sub> = 500 V I <sub>trigger</sub> (min.) = 300 mA	TBU-KE050-200-WH		12.5 6.3 3.4 2.4 1.8 13.0 6.8 3.9 3.0 2.3 13.7 7.5 4.6 3.6 3.0	14.6 7.5 4.1 3.1 2.3 15.2 8.1 4.7 3.7 2.9 16.0 8.9 5.5 4.5 3.6	Ω
t <sub>block</sub>	Time for the device to	go from normal operating state to protect			1	μs	
IQ	Current through the tri	ggered TBU® device with 50 Vdc circuit	0.25	0.50	1.00	mA	
V <sub>reset</sub>	Voltage below which t state	he triggered TBU <sup>®</sup> device will transition t	12	16	20	V	
R <sub>th(j-l)</sub>	Junction to package p	oads - FR4 using recommended pad layo		116		°C/W	
R <sub>th(j-l)</sub>	Junction to package p	oads - FR4 using heat sink on board (6 cr		96		°C/W	

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#### **Reference Application**

The TBU® device can be used to protect against excessive voltage surges in DC biased equipment, as shown in the figure below. Diode D1 prevents reverse voltage surges from damaging the equipment, and the TBU® protector prevents any positive surges from causing damage. An overvoltage protection device, such as an MOV, may be used to provide additional overvoltage protection if the surge voltage is likely to be above the maximum rating of the TBU® device. D1 reverse voltage rating should be greater than that of the OVP device at the maximum surge current level. Typically, a 1N4007 is a suitable choice. D2 should be chosen to be above the normal working voltage of the protected device, but below its absolute maximum rating.



#### **Basic TBU Operation**

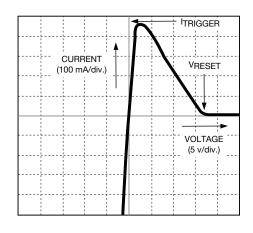
The TBU® device is a silicon-based, solid-state, resettable device which is placed in series with a signal path. The TBU® device operates in approximately 1 µs - once line current exceeds the TBU® device's trigger current I<sub>trigger</sub>. When operated, the TBU® device restricts line current to less than 1 mA typically. When operated, the TBU® device will block all system voltages and any other voltages including the surge up to rated limits.

After the surge, the TBU® device resets when the voltage across the TBU® device falls to the  $V_{reset}$  level. The TBU® device will automatically reset on lines which have no DC bias or have DC bias below  $V_{reset}$  (such as unpowered signal lines).

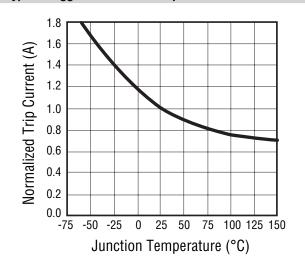
If the line has a normal DC bias above  $V_{reset}$ , the voltage across the TBU® device may not fall below  $V_{reset}$  after the surge. In such cases, special care needs to be taken to ensure that the TBU® device will reset, otherwise an automatic or manual power down will be required. Bourns application engineers can provide further assistance.

#### **Performance Graphs**

#### V-I Characteristic - TBU-KE050-300-WH (Pin 2-1)



#### **Typical Trigger Current vs. Temperature**



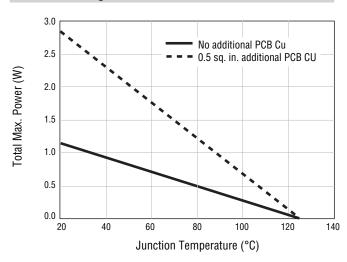
Specifications are subject to change without notice.

Customers should verify actual device performance in their specific applications.

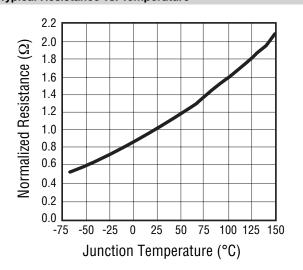
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#### **Performance Graphs (Continued)**

#### **Power Derating Curve**

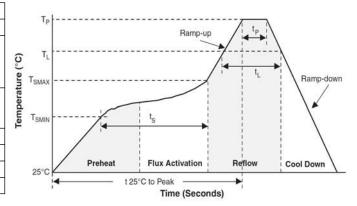


#### Typical Resistance vs. Temperature



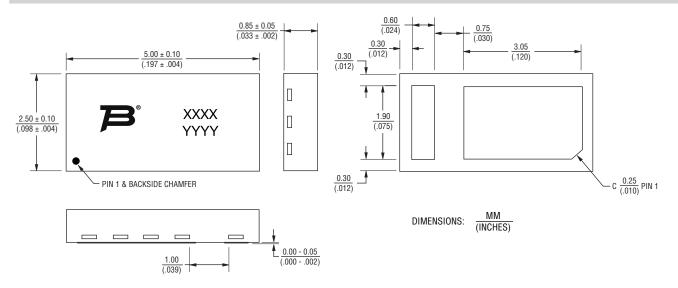
#### **Reflow Profile**

Profile Feature	Pb-Free Assembly
Average Ramp-Up Rate (Tsmax to Tp)	3 °C/sec. max.
Preheat - Temperature Min. (Tsmin) - Temperature Max. (Tsmax) - Time (tsmin to tsmax)	150 °C 200 °C 60-180 sec.
Time maintained above: - Temperature (TL) - Time (tL)	217 °C 60-150 sec.
Peak/Classification Temperature (Tp)	260 °C
Time within 5 °C of Actual Peak Temp. (tp)	20-40 sec.
Ramp-Down Rate	6 °C/sec. max.
Time 25 °C to Peak Temperature	8 min. max.



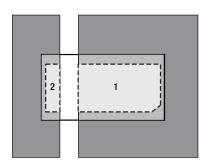
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#### **Product Dimensions**



#### **Recommended Pad Layout**

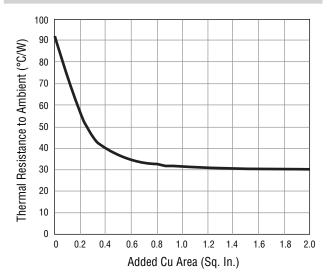
TBU® protectors have matte-tin termination finish. The suggested layout should use Non-Solder Mask Define (NSMD). The recommended stencil thickness is 0.10-0.12 mm (.004-.005 in.) with a stencil opening size 0.025 mm (.0010 in.) less than the device pad size. As when heat sinking any power device, it is recommended that wherever possible, extra PCB copper area is allowed. For minimum parasitic capacitance, do not allow any signal, ground or power signals beneath any of the pads of the device.

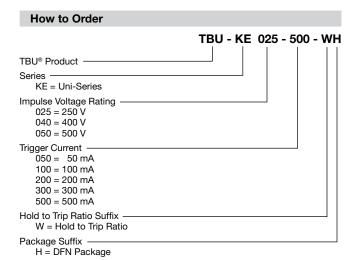


Pad Designation							
Pad #	Pin Out						
1	Line Side						
2	Load Side						

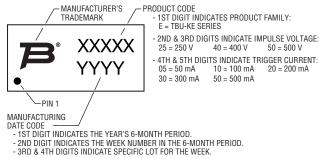
Dark grey areas show added PCB copper area for better thermal resistance.

#### Thermal Resistance vs. Additional PCB Cu Area





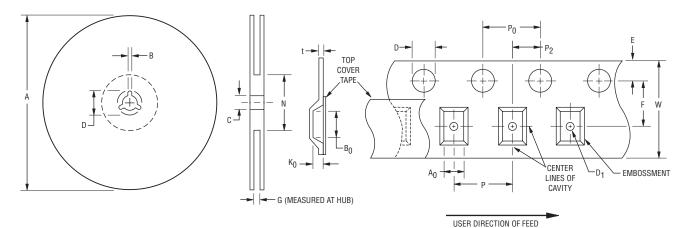
#### **Typical Part Marking**



#### 6-MONTH PERIOD CODES:

A = JAN-JUN 2009 B = JUL-DEC 2009 C = JAN-JUN 2010 D = JUL-DEC 2010 E = JAN-JUN 2011 F = JUL-DEC 2011

#### **Packaging Specifications**



#### QUANTITY: 3000 PIECES PER REEL

Α		В		С		D		G	N
Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Ref.	Ref.
176 (6.929)	<u>178</u> (7.008)	1.5 (.059)	2.5 (.098)	12.8 (.504)	13.5 (.531)	<u>20.2</u> (.795)	-	16.5 (.650)	102 (4.016)

A0		В0		D		D1		E		F	
Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	max.
4.2 (.165)	4.4 (.173)	6.65 (.262)	7.05 (.277)	1.5 (.059)	1.6 (.063)	1.5 (.059)	-	1.65 (.065)	1.85 (.073)	7.4 (.291)	7.6 (.299)
K <sub>0</sub>		Р		P0		P2		t		W	
Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.
1.1	1.3	7.9	8.1	3.9	4.1	1.9	2.1	0.25	0.35	15.7	16.3
(.043)	(.051)	(.311)	(.319)	(.159)	(.161)	(.075)	(.083)	(.010)	(.014)	(.618)	(.642)

DIMENSIONS:

MM (INCHES)

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