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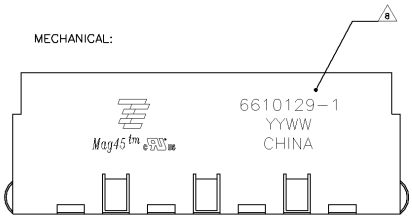
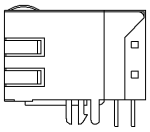
UOC	SHT
AA	22

REVISIONS

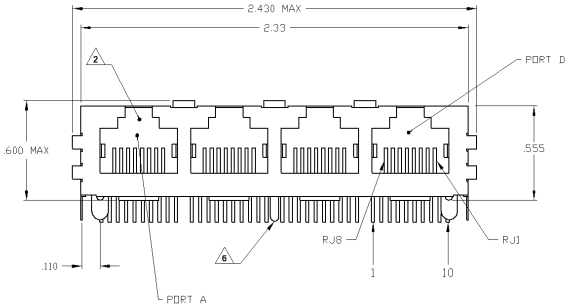
#	LTR	DESCRIPTION	DATE	DR	APPR
	C	REV PER ECO-08-019415	12APR2008	DC	TX

MECHANICAL:

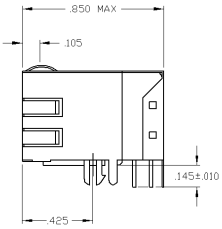


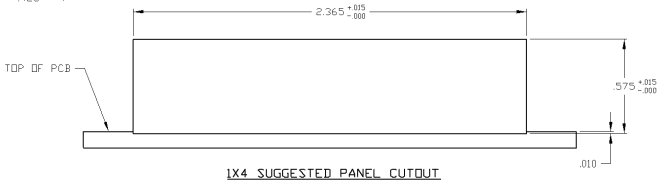


6610129-1 ONLY
NO BOTTOM TABS AND NO REAR GROUND PIN

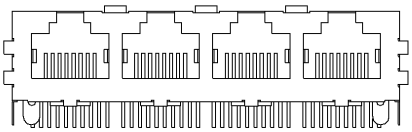


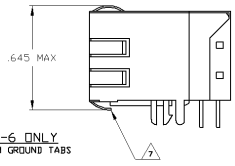
6610129-5 SHOWN
WITH REAR GROUND PIN





1X4 SUGGESTED PANEL CUTOUT





1610129-6 ONLY
WITH BOTTOM GROUND TABS

1. MATERIALS:
   - HOUSING - THERMOPLASTIC PET POLYESTER FLAMMABILITY RATING UL 94V-0.
   - SHIELD - .010" THICK C26800 BRASS PREPLATED WITH 38µINCH MIN SEMI-BRIGHT NICKEL; SOLDER TABS POST DIPPED WITH 100µINCH MIN SAC SOLDER.
   - MOD JACK CONTACTS - .0137" X .018", PHOSPHOR BRONZE, 50µINCH MIN OVERALL NICKEL UNDERPLATE, WITH SELECT 50µINCH MIN HARD GOLD FINISH PLATE. SOLDERTAILS WITH 1000µINCH MIN MATTE TIN AND/OR SAC SOLDER DIP.
2. RJ45 JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS PART 68, SUB PART F.
3. MAGNETICS:
   - APPLICATION: 10/100/1000 BASE-T
   - IMPEDANCE: 100 OHMS
   - TURNS RATIO (CHIP CABLE): 1:1 ALL FOUR PAIRS
   - OPEN CIRCUIT INDUCTANCE (OCL): 350µH MIN @100kHz, 0.1VRMS, 8mADC BIAS FROM 0°C TO 70°C, ALL FOUR PAIRS
   - ALL FOUR PAIRS BI-DIRECTIONAL
   - PERFORMANCE @ 25°C:
     INSERTION LOSS (IL): 1.1dB MAX FROM 0.5MHz TO 100MHz
     RETURN LOSS (RL): 18dB MIN FROM 0.5MHz TO 40MHz
     12-20LOG(F/80)dB MIN FROM 40.1MHz TO 100MHz
     CROSSTALK ATTENUATION: 35dB MIN FROM 0.5MHz TO 40MHz
     33-20LOG(F/50)dB MIN FROM 40.1MHz TO 100MHz
     COMMON MODE REJECTION RATIO (CMRR): 30dB MIN FROM 0.5MHz TO 100MHz
   - ISOLATION VOLTAGE: COMPLIES WITH IEEE802.3 2002, PARA 40.6.1.1, ITEM a.
4. OPERATING TEMPERATURE: FROM 0°C TO +70°C.
5. INDICATED MAGNETIC CONNECTIONS ARE SYMMETRICAL TO ACCOMODATE CONNECTIONS FOR EITHER NIC OR HUB CONFIGURATION, OR SUPPORT AUTO-MDI/MDIX.
6. REAR GROUND PIN ON 6610129-5 ONLY.
7. BOTTOM GROUND TABS ON 6610129-6 ONLY.
8. TYCO ELECTRONICS LOGO, PART NUMBER, DATE CODE, COUNTRY OF ORIGIN AND AGENCY APPROVAL MARKING IN APPROXIMATE LOCATION SHOWN.
9. THESE PARTS ARE RECOMMENDED FOR WAVE SOLDERING PROCESS. PEAK WAVE SOLDERING TEMPERATURE IS 265°C MAX 10 SECONDS MAX.

BOTTOM PANEL GROUND TAB	REAR PCB GROUND TAB	PART NUMBER
4	0	6610129-6
0	1	6610129-5
0	0	6610129-1

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DIMENSIONS: INCHES

TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ± -; 1 PLC ± .010; 2 PLC ± .005; 3 PLC ± .005; ANGLES ± -

MATERIAL: 1 FINISH: 1

DR R.GRZYBOWSKI; CHK D.FAROLE; APP D.FAROLE

PRODUCT SPEC 108-2100; APPLICATION SPEC –; WEIGHT –

Tyco Electronics Corporation, Harrisburg, Pa 17105-3608

NAME: 1X4 MAG45(TM) MODULAR JACK, 7C4 GIGABIT SCHEMATIC (10 PIN HORIZONTAL), 7G05 SERIES CIRCUIT, SHIELDED, NO LEDS

SIZE A1; CAGE CODE 00779; DRAWING NO C-6610129; RESTRICTED TO –

CUSTOMER DRAWING; SCALE 1:1; SHEET 1 OF 2; REV C

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LOC	SHT
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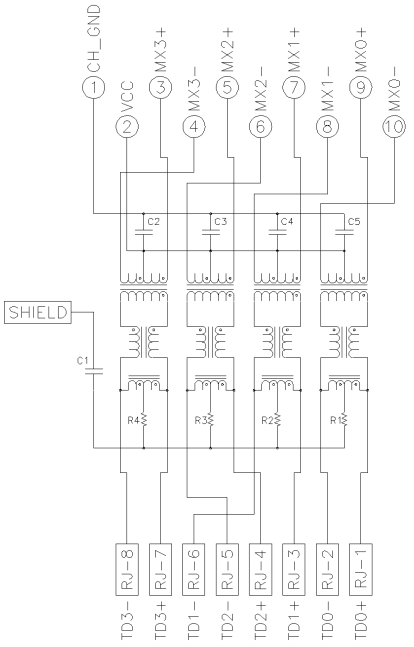
REVISIONS

#	LTR	DESCRIPTION	DATE	DWN	APVD
	–	SEE SHEET 1	–	–	–

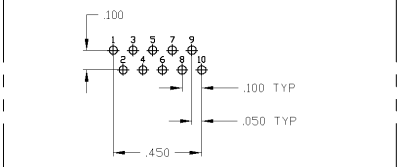
7G05 SERIES GIGABIT MAGNETIC CIRCUIT 3 5



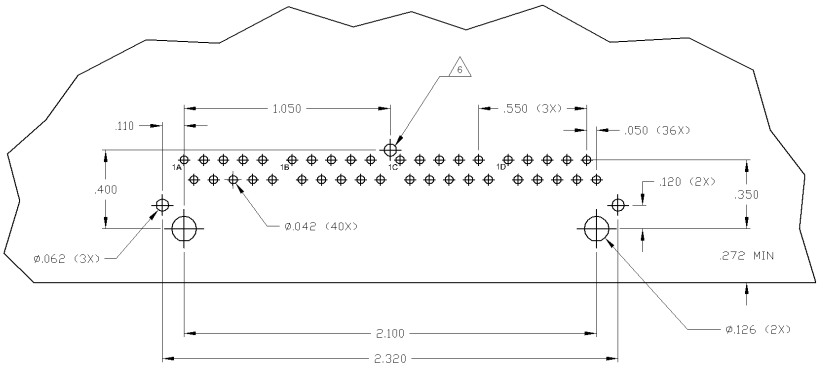
C1 = 1000 pF, 2kV CAPACITOR

R1–R4 = 75 OHMS, 1/16W RESISTORS

C2–C5 = 0.1µF, 50V, X7R CAPACITORS



PIN DESIGNATIONS



SUGGESTED PCB LAYOUT

(Component Side)

THIS DRAWING IS A CONTROLLED DOCUMENT.	DRN R.GRZYBOWSKI	Tyco Electronics	Tyco Electronics Corporation Harrisburg, Pa 17105-3608
DIMENSIONS: INCHES	CHK D.FAROLE	NAME	1X4 MAG45(TM) MODULAR JACK, 7G4 GIGABIT SCHEMATIC (10 PIN HORIZONTAL), 7G05 SERIES CIRCUIT, SHIELDED, NO LEDS
TOLERANCES UNLESS OTHERWISE SPECIFIED: 0 PLC ± – / 1 PLC ± .010 / 2 PLC ± .005 / 3 PLC ± – / ANGLES ± –	APVD D.FAROLE	PRODUCT SPEC 108-2100	
MATERIAL SEE SHEET 1	FINISH SEE SHEET 1	APPLICATION SPEC –	SIZE A1 / CAGE CODE 00779 / DRAWING NO C-6610129 / RESTRICTED TO –
		WEIGHT –	
		CUSTOMER DRAWING	SCALE 1:1 / SHEET 2 OF 2 / REV C

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