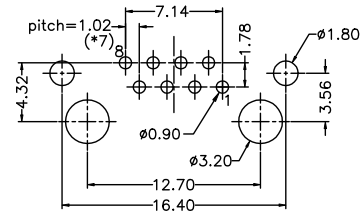
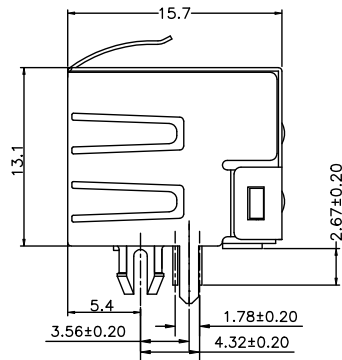
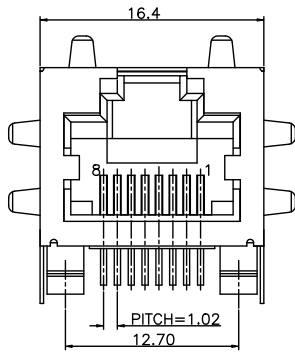


A1RFRD10

REVISION RECORD				
REV	ECO	DESCRIPTION	DRFT	CHKD



PCB (t=1.60mm) Layout
Component Side Shown

NOTES:

ELECTRICAL:

- VOLTAGE RATING : 125 VAC RMS.
- CURRENT RATING : 1.5 AMP.
- CONTACT RESISTANCE : 30 MILLIOHMS MAX.
- INSULATION RESISTANCE: 1000 MEGOHMS MIN @ 500 VDC.
- DIELECTRIC STRENGTH : 1000 VAC RMS 60Hz, 1MIN.
- OPERATING: 750 CYCLES MIN.

MECHANICAL:

- HOUSING MATERIAL : PBT+30%.G.F UL94V-0 COLOR-BLACK.
- INSERT MATERIAL : PBT+30%GF UL94V-0 COLOR-BLACK.
- BACK PLATE MATERIAL : PBT+30%GF COLOR-BLACK.
- SHIELD MATERIAL : COPPER ALLOY 0.25mm THICKNESS PLATING NI.
- CONTACT MATERIAL : PHOSPHOR BRONZE 0.35mm THICKNESS OR Ø0.46mm.LEAD FREE.
- PLATING : GOLD PLATING OVER NICKEL.
- PCB RETENTION PRE-SOLDER : 1 LB MIN.
- PCB RETENTION POST-SOLDER : 10 LBS MIN.

ENVIRONMENTAL:

- STORAGE : -40°C TO +85°C.
- OPERATION: 0°C TO 70°C.
- WAVE SOLDERING TEMPERATURE: 230~250°C(3~5 SECONDS)

MATES WITH MODULAR PLUG CONFORMING TO
FCC PART 68, SUBPART F.

CUL FILE NO. E163191

DOHS PART NUMBER: E 5J 8 8 - X X L Q X X -L

0-NO.LED
6-HS'G&SHIELD NO LED

0-W/O SHIELD 1-NO TAB
2-TOP TABS. 3-SIDE TABS.
4-TOP TABS & SIDE TABS.

PLATING:1-3u" 2-6u" 3-15u"
4-30u" 5-50u"
A-TIN SHIELD STAMPED PIN
B-NI SHIELD STAMPED PIN
F-NI SHIELD ROUND PIN (Ø0.46)
PBT+30%(A06-1490)

MM (INCH)		DFTO YANGLAN DATE 05/25/05		FULL RISE ELECTRONIC CO., LTD	
TOLERANCES EXCEPT AS NOTED		CHKD-DATE DATE 2006/9/25			
MM		MFO DATE		TITLE	
.0 ± 0.20		APPLY/SHINO DATE 2006/9/25			
.00 ± 0.15		MATERIAL :		RJ45 W/O LED JACK	
.000 ± 0.075		QTY :			
ANGLES ±		FINISH :		DRAWING NO. GE5J3A11-LC	
THIRD ANGLE PROJECTION		SCALE :		/PART NO. SEE NOTE	
				DO NOT SCALE DRAWING	
				SHEET 1 OF 1	
				SIZE REV	
				A3 0	

8 | 7 | 6 | 5 | 4 | 3 | 2 | 1

H
G
F
E
D
C
B
A