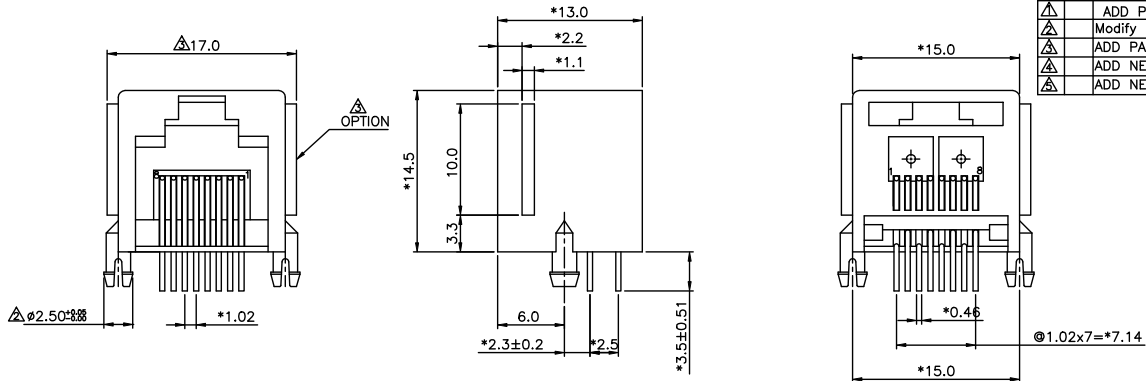


A1RFRD10



| REVISION RECORD | | | | |
|-----------------|-----|------------------|------------------|------|
| REV | ECO | DESCRIPTION | DRFT | CHKD |
| Δ | | ADD PART NO. | VICRY 4/12/01 | |
| Δ | | Modify Dimension | 900 7/10/01 | |
| Δ | | ADD PANEL STOP | 900 1/21/01 | |
| Δ | | ADD NEW P\N | 91 5/15/04* | |
| Δ | | ADD NEW P\N | 92 12/08/04* | |

NOTES:

ELECTRICAL:

- VOLTAGE RATING : 125 VAC RMS.
- CURRENT RATING : 1.5 AMP.
- CONTACT RESISTANCE : 30 MILLIOHMS MAX.
- INSULATION RESISTANCE: 1000 MEGOHMS MIN @ 500 VDC.
- DIELECTRIC STRENGTH : 1000 VAC RMS 60HZ, 1MIN.

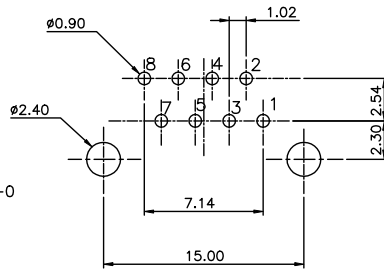
MECHANICAL:

- HOUSING MATERIAL : PBT+15%GF UL94V-0 OR ZYTEL FR50 NC010 UL94V-0
- CONTACT MATERIAL : PHOSPHOR BRONZE $\phi 0.46$ mm.
- PLATING : GOLD PLATING OVER NICKEL.
- OPERATING LIFE : 750 CYCLES MIN.
- PCB RETENTION PRE-SOLDER : 1 LB MIN.
- PCB RETENTION POST-SOLDER: 10 LBS MIN.

ENVIRONMENTAL:

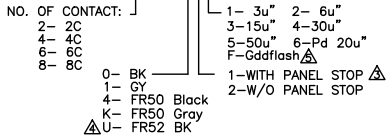
- STORAGE : -40°C TO +85°C.
- OPERATION: 0°C TO 70°C.

MATES WITH MODULAR PLUG CONFORMING TO FCC PART 68, SUBPART F.



PC Board Layout
Component Side Shown

Δ PART NUMBER: E558X-60LXXX



| | | | | |
|---|--|---|---|--|
| <p>MM (INCH)</p> <p>TOLERANCES EXCEPT AS NOTED</p> <p>MM .0 ± 0.2 ± .50 ± 0.15 ± .000 ± 0.075 ±</p> <p>ANGLES ± 0.5</p> <p>THIRD ANGLE PROJECTION</p> | | <p>DFTD Lien DATE 9/09/99*</p> <p>CHKD AMONG DATE 2004/8/18</p> <p>MFO DATE</p> <p>APPLV. czl DATE 2004/8/18</p> <p>MATERIAL :</p> <p>QT'Y :</p> <p>FINISH :</p> <p>SCALE : 3 : 1</p> | <p>FULL RISE ELECTRONIC CO., LTD</p> <p>TITLE</p> <p>MINI L/P PCB JACK 8P</p> <p>DRAWING NO. GE553AT2</p> <p>/PART NO. SEE NOTE</p> <p>DO NOT SCALE DRAWING</p> | <p>SIZE A3</p> <p>REV 13/08/04</p> <p>SHEET 5 OF</p> |
|---|--|---|---|--|

8 | 7 | 6 | 5 | 4 | 3 | 2 | 1