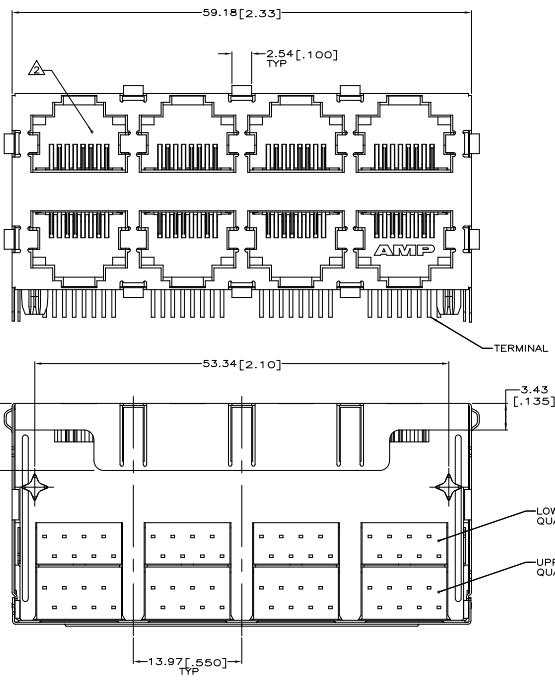
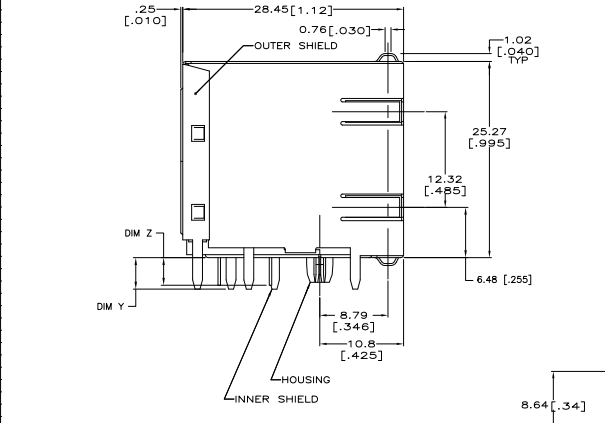
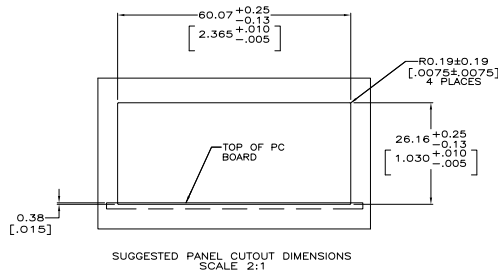
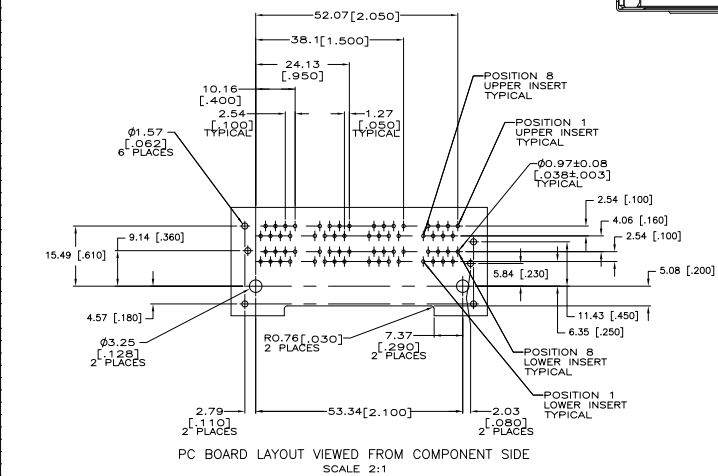
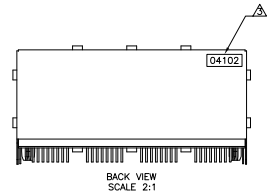


THIS DRAWING IS UNPUBLISHED
 © 2008 TYS ELECTRONICS CORPORATION ALL RIGHTS RESERVED

REV	DATE	DESCRIPTION	BY	CHK	APP
AA	22				
B1		REVISED ECR-08-031608			



- ▲ MATERIAL:
 HOUSING - PCT POLYESTER, BLACK, UL94V-0, OVERMOLDED TERMINAL ARRAY - PBT, BROWN, TERMINALS - 0.33 [0.13] THICK PHOS BRONZE PLATED WITH 1.27um [0.00050] MIN THICK HARD GOLD IN LOCALIZED AREA AND 3.81um [0.00150] MIN THICK MATTE TIN IN SOLDER AREA OVER 1.27um [0.00050] MIN THICK NICKEL UNDERPLATE
 SHIELD - 0.25 [0.10] THICK COPPER ALLOY PLATED WITH 2.03um [0.00080] MIN BRIGHT TIN OVER 1.27um [0.00050] MIN NICKEL
- ▲ JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS, PART 68 SUBPART F.
- ▲ DATE CODE LOCATED ON REAR OF PART APPROXIMATELY AS SHOWN:
 FIRST 2 DIGITS ARE LAST 2 DIGITS OF YEAR, SECOND 2 DIGITS ARE MANUFACTURING WORK WEEK, LAST DIGIT IS DAY OF WEEK, WITH SUNDAY = 1.
- ▲ MATERIAL:
 HOUSING - PCT POLYESTER, BLACK, UL94V-0, OVERMOLDED TERMINAL ARRAY - PBT, BROWN, TERMINALS - 0.33 [0.13] THICK PHOS BRONZE PLATED WITH 1.27um [0.00050] MIN THICK HARD GOLD IN LOCALIZED AREA AND 3.81um [0.00150] MIN THICK MATTE TIN IN SOLDER AREA OVER 1.27um [0.00050] MIN THICK NICKEL UNDERPLATE
 SHIELDS - 0.25 [0.10] THICK COPPER ALLOY PLATED WITH 1.27 um [0.00050] MINIMUM NICKEL AND 2.03 um [0.00080] MINIMUM HOT TIN DIP ON PCB GROUND TABS.



REVISION	DATE	DESCRIPTION	BY	CHK	APP
1	3.05.120				
2	3.26.140				
3	4.06.160				
4	4.06.160				

THIS DRAWING IS A CONTROLLED DOCUMENT.	DATE	DESCRIPTION	BY	CHK	APP
108-1163-2	108-1163-2	NICKEL			
114-2048	114-2048				
SEE NOTE 1	SEE NOTE 1				

REVISION	DATE	DESCRIPTION	BY	CHK	APP
1	3.05.120				
2	3.26.140				
3	4.06.160				
4	4.06.160				

APP 480 REV 01/2008

APP 480 REV 01/2008