



1. MATERIAL:
HOUSING - PCT POLYESTER, BLACK, UL94V-0.
OVERMOLDED TERMINAL ARRAY - PBT, BROWN.
TERMINALS - 0.33 [.013] THICK PHOS BRONZE PLATED WITH 1.27 μ m [.000050] MIN THICK HARD GOLD IN LOCALIZED AREA AND 3.81 μ m [.000150] MIN THICK MATTE TIN IN SOLDER AREA OVER 1.27 μ m [.000050] MIN THICK NICKEL UNDERPLATE.
SHIELDS - 0.25 [.010] THICK COPPER ALLOY PLATED WITH 1.27 μ m [.000050] MINIMUM NICKEL AND 2.03 μ m [.000080] MINIMUM HOT TIN DIP ON PCB GROUND TABS.
▲ JACK CAVITY CONFORMS TO FCC RULES AND REGULATIONS, PART 68 SUBPART F.
▲ DATE CODE LOCATED ON REAR OF PART APPROXIMATELY AS SHOWN: FIRST 2 DIGITS ARE LAST 2 DIGITS OF YEAR, SECOND 2 DIGITS ARE MANUFACTURING WORK WEEK, LAST DIGIT IS DAY OF WEEK, WITH SUNDAY = 1.

PC BOARD LAYOUT VIEWED FROM COMPONENT SIDE

SUGGESTED PANEL CUTOUT DIMENSIONS SCALE 2:1

4.06 [160]	5569381-2
3.56 [140]	5569381-1
DIM A	PART NUMBER

THIS DRAWING IS A CONTROLLED DOCUMENT.		DATE: 11/11/2015		REVISED ECR-08-031628		DATE: 11/11/2015	REVISED ECR-08-031628
DESIGNER: J. GILBERT	DRAWN: J. GILBERT	CHKD: J. GILBERT	APPVD: J. GILBERT	ELECTRONICS CORPORATION		Harrisburg, PA 17105-2608	
REVISIONS:	DATE:	BY:	DESCRIPTION:	REVISED:	DATE:	BY:	DESCRIPTION:
1	108-1163-2	J. GILBERT	STACKED MODULAR JACK ASSEMBLY, 2x1 PORT, B POSITION, SHIELDED, PANEL GROUND, CATEGORY 5	1	114-2048	J. GILBERT	REWORKED TO
SEE NOTE 1							
CUSTOMER DRAWING	SEE NOTE 1						