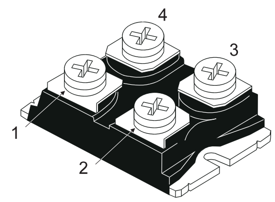
STGE50NC60VD

50 A - 600 V very fast IGBT

Features

- High current capability
- High frequency operation
- Low C_{RES}/C_{IES} ratio (no cross-conduction susceptibility)
- Very soft ultra fast recovery antiparallel diode



ISOTOP

Applications

- High frequency inverters
- SMPS and PFC in both hard switching and resonant topologies
- UPS
- Motor drivers

Description

Using the latest high voltage technology based on a patented strip layout, STMicroelectronics has designed an advanced family of IGBTs, the PowerMESH™ IGBTs, with outstanding performances. The suffix "V" identifies a family optimized for high frequency.

Figure 1. Internal schematic diagram

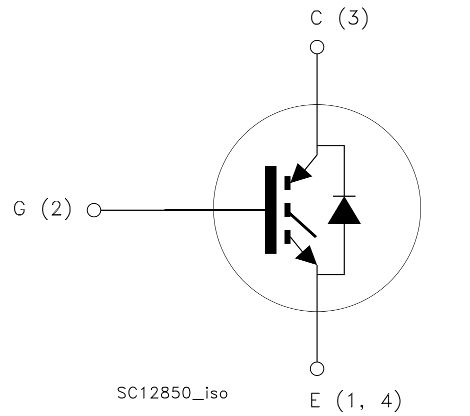


Table 1. Device summary

Order code	Marking	Package	Packaging
STGE50NC60VD	GE50NC60VD	ISOTOP	Tube

Contents

1 Electrical ratings

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit
V_{CES}	Collector-emitter voltage (V_{GE} = 0)	600	V
I_C (1)	Collector current (continuous) at T_C = 25 °C	90	A
I_C (1)	Collector current (continuous) at T_C = 100 °C	50	A
I_{CL} (2)	Turn-off latching current	200	A
I_{CP} (3)	Pulsed collector current	200	A
V_{GE}	Gate-emitter voltage	± 20	V
I_F	Diode RMS forward current at T_C=25°C	30	A
I_{FSM}	Surge non repetitive forward current t_p = 10 ms sinusoidal	120	A
P_{TOT}	Total dissipation at T_C = 25 °C	260	W
Tj	Operating junction temperature	-55 to 150	°C

1. Calculated according to the iterative formula

$$I_C(T_C) = \frac{T_{j(max)} - T_C}{R_{thj-c} \times V_{CE(sat)(max)}(T_{j(max)}, I_C(T_C))}$$

2. V_{clamp} = 80% of V_{CES}, T_j =150 °C, R_G=10 Ω, V_{GE}=15 V
3. Pulse width limited by max. junction temperature allowed

Table 3. Thermal data

Symbol	Parameter	Value	Unit
$R_{thj-case}$	Thermal resistance junction-case IGBT	0.48	°C/W
$R_{thj-case}$	Thermal resistance junction-case diode	1.6	°C/W
$R_{thj-amb}$	Thermal resistance junction-amb	30	°C/W

2 Electrical characteristics

(T_J = 25 °C unless otherwise specified)

Table 4. Static

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$V_{(BR)CES}$	Collector-emitter breakdown voltage (V_{GE} = 0)	I_C= 1 mA	600			V
$V_{CE(sat)}$	Collector-emitter saturation voltage	V_{GE}= 15 V, I_C= 40 A V_{GE}= 15 V, I_C=40 A, T_j=125 °C		1.9 1.7	2.5	V V
$V_{GE(th)}$	Gate threshold voltage	V_{CE}= V_{GE}, I_C= 250 µA	3.75		5.75	V
I_{CES}	Collector cut-off current (V_{GE} = 0)	V_{CE}=600 V V_{CE}= 600 V, T_j= 125 °C			150 1	µA mA
I_{GES}	Gate-emitter leakage current (V_{CE} = 0)	V_{GE}= ±20 V			±100	nA
g_{fs}(1)	Forward transconductance	V_{CE} = 15 V, I_C= 20 A		20		S

1. Pulsed: pulse duration= 300 µs, duty cycle 1.5%

Table 5. Dynamic

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
C_{ies} C_{oes} C_{res}	Input capacitance Output capacitance Reverse transfer capacitance	V_{CE} = 25 V, f = 1 MHz, V_{GE} = 0	-	4550 350 105	-	pF pF pF
Q_g Q_{ge} Q_{gc}	Total gate charge Gate-emitter charge Gate-collector charge	V_{CE} = 390 V, I_C = 40 A, V_{GE} = 15 V, see *Figure 17*	-	214 30 96	-	nC nC nC

Table 6. Switching on/off (inductive load)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
$t_{d(on)}$ t_r $(di/dt)_{on}$	Turn-on delay time Current rise time Turn-on current slope	V_{CC} = 390 V, I_C = 40 A R_G= 3.3 Ω, V_{GE}= 15 V, see *Figure 16*	-	43 17 2060	-	ns ns A/µs
$t_{d(on)}$ t_r $(di/dt)_{on}$	Turn-on delay time Current rise time Turn-on current slope	V_{CC} = 390 V, I_C = 40 A R_G= 3.3 Ω, V_{GE}= 15 V, T_j = 125 °C see *Figure 16*	-	42 19 1900	-	ns ns A/µs
$t_{r(Voff)}$ $t_{d(Voff)}$ t_f	Off voltage rise time Turn-off delay time Current fall time	V_{CC} = 390 V, I_C = 40 A R_G= 3.3 Ω, V_{GE}= 15 V, see *Figure 16*	-	25 140 45	-	ns ns ns
$t_{r(Voff)}$ $t_{d(Voff)}$ t_f	Off voltage rise time Turn-off delay time Current fall time	V_{CC} = 390 V, I_C = 40 A R_G= 3.3 Ω, V_{GE}= 15 V, T_j = 125 °C see *Figure 16*	-	60 170 77	-	ns ns ns

Table 7. Switching energy (inductive load)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
E_{on} (1) E_{off} (2) E_{ts}	Turn-on switching losses Turn-off switching losses Total switching losses	V_{CC} = 390 V, I_C = 40 A R_G= 3.3 Ω, V_{GE}= 15 V, see *Figure 18*	-	330 720 1050	450 970 1420	µJ µJ µJ
E_{on} (1) E_{off} (2) E_{ts}	Turn-on switching losses Turn-off switching losses Total switching losses	V_{CC} = 390 V, I_C = 40 A R_G= 3.3 Ω, V_{GE}= 15 V, T_j = 125 °C see *Figure 18*	-	640 1400 2040		µJ µJ µJ

1. Eon is the turn-on losses when a typical diode is used in the test circuit in *Figure 18* If the IGBT is offered in a package with a co-pak diode, the co-pack diode is used as external diode. IGBTs & Diode are at the same temperature (25 °C and 125 °C)
2. Turn-off losses include also the tail of the collector current

Table 8. Collector-emitter diode

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
V_F	Forward on-voltage	I_F = 20 A I_F = 20 A, Tj = 125°C	-	1.5 1	2.2	V V
t_{rr} Q_{rr} I_{rrm}	Reverse recovery time Reverse recovery charge Reverse recovery current	I_F = 20 A,V_R = 40 V, di/dt = 100 A/µs see *Figure 19*	-	44 66 3		ns nC A
t_{rr} Q_{rr} I_{rrm}	Reverse recovery time Reverse recovery charge Reverse recovery current	I_F = 20 A,V_R = 40 V, T_j =125 °C, di/dt = 100 A/µs see *Figure 19*	-	88 237 5.4		ns nC A

2.1 Electrical characteristics (curves)

Figure 2. Output characteristics

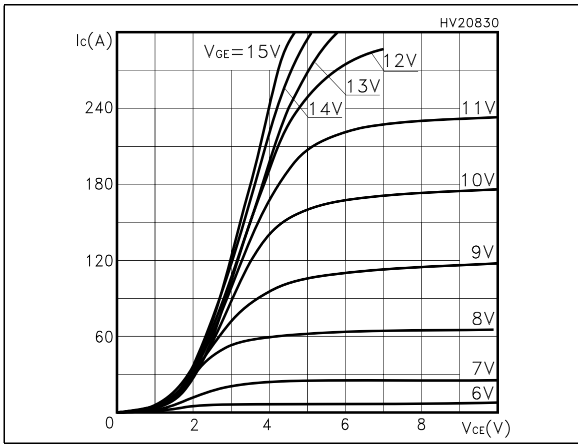


Figure 3. Transfer characteristics

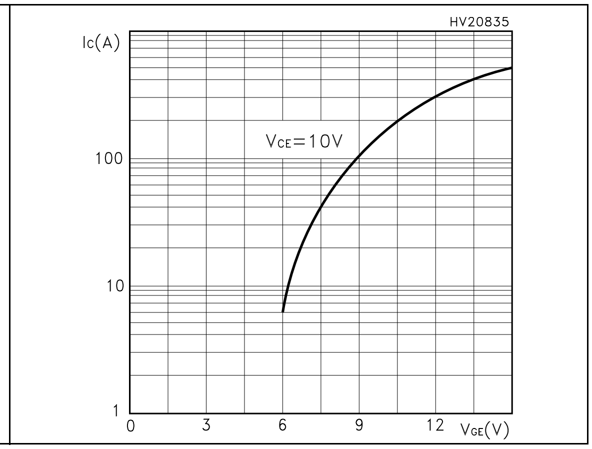


Figure 4. Transconductance

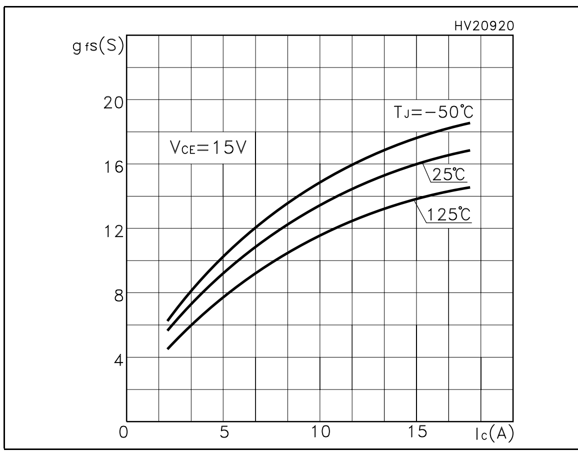


Figure 5. Collector-emitter on voltage vs temperature

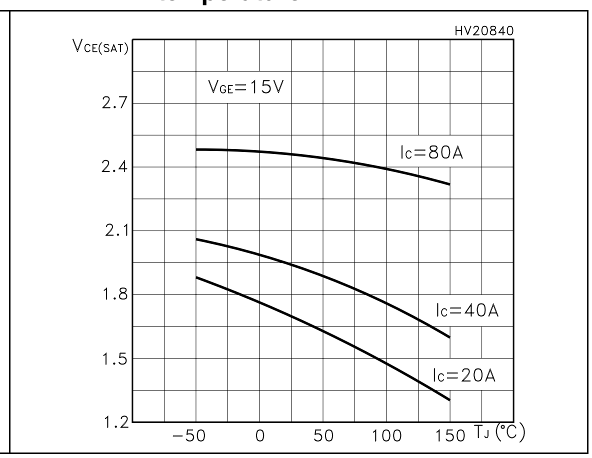


Figure 6. Collector-emitter on voltage vs collector current

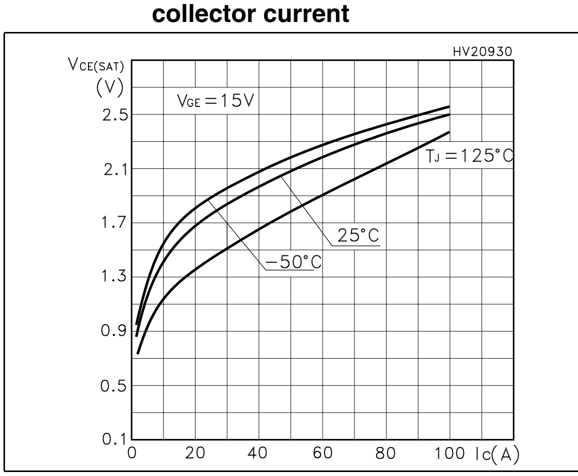


Figure 7. Normalized gate threshold vs temperature

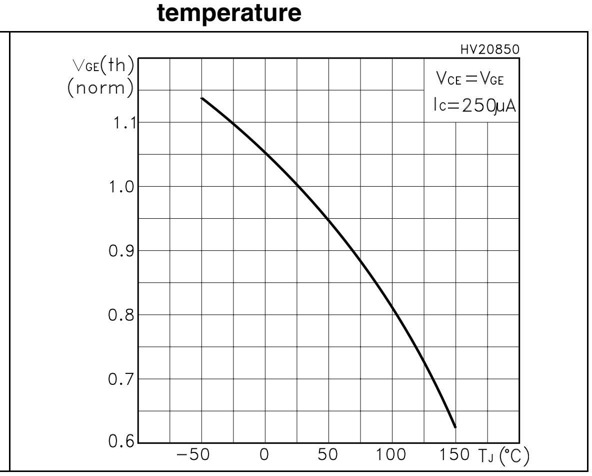


Figure 8. Normalized breakdown voltage vs temperature

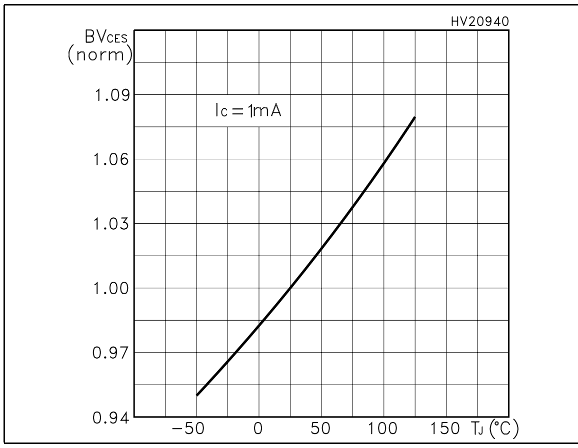


Figure 9. Gate charge vs gate-emitter voltage

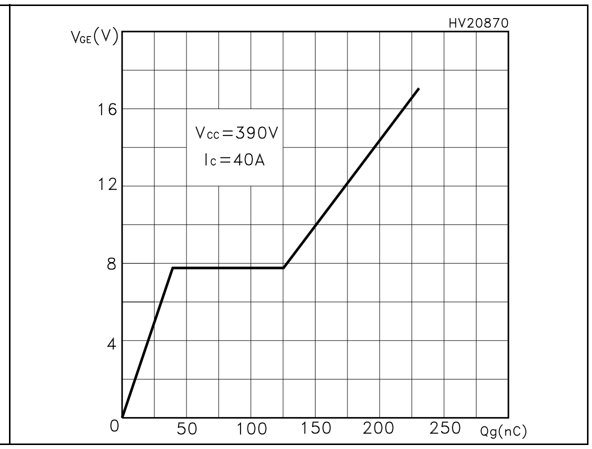


Figure 10. Capacitance variations

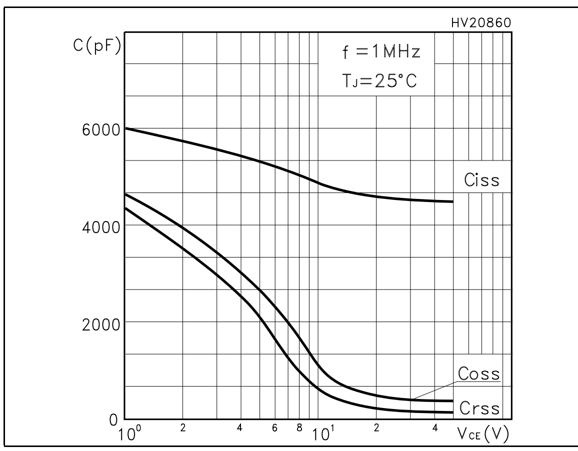


Figure 11. Total switching losses vs temperature

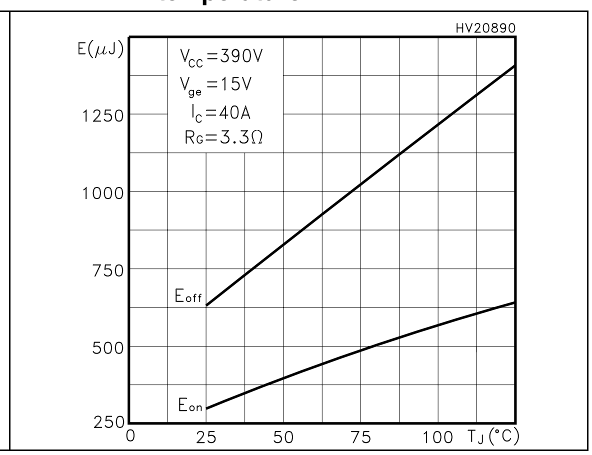


Figure 12. Total switching losses vs gate charge resistance

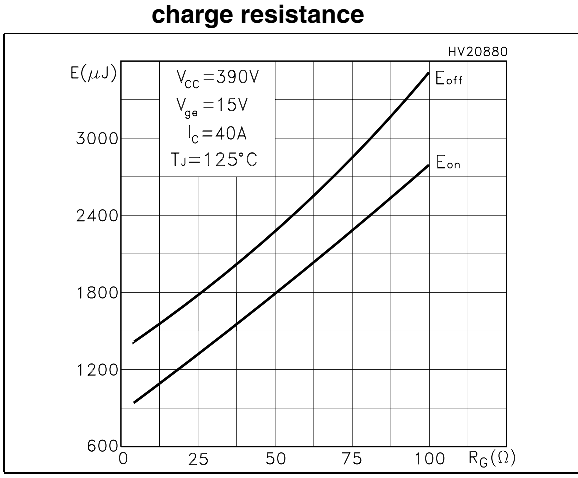


Figure 13. Total switching losses vs collector current

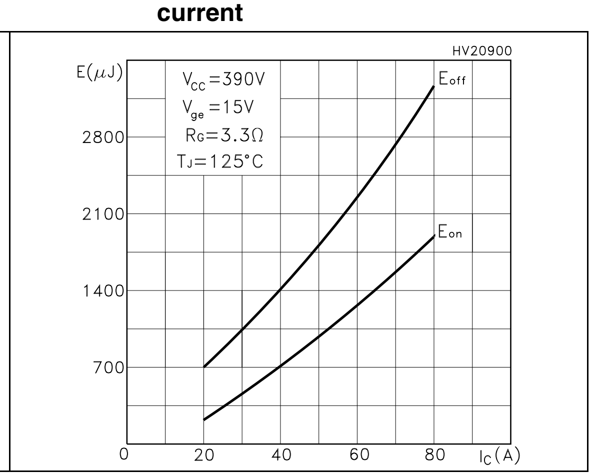


Figure 14. Turn-off SOA

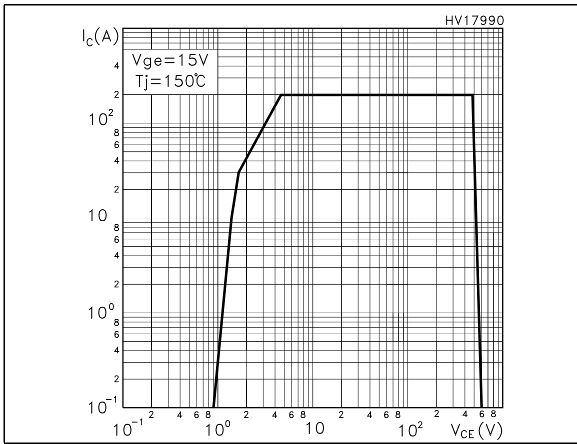
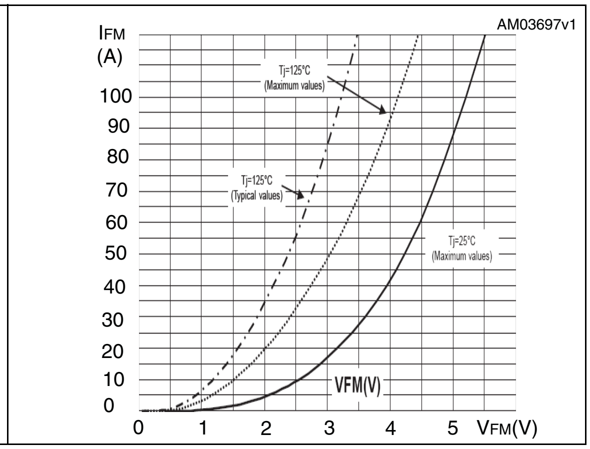


Figure 15. Emitter-collector diode characteristics



3 Test circuits

Figure 16. Test circuit for inductive load switching

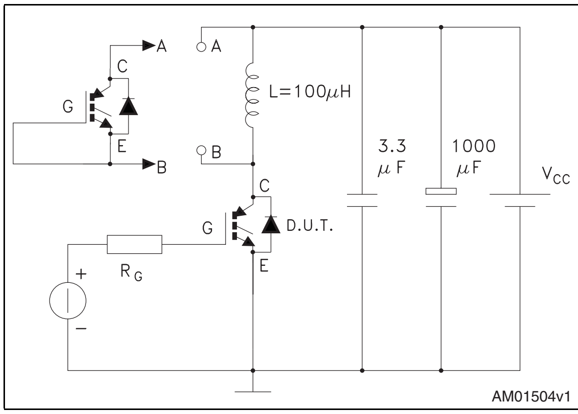


Figure 17. Gate charge test circuit

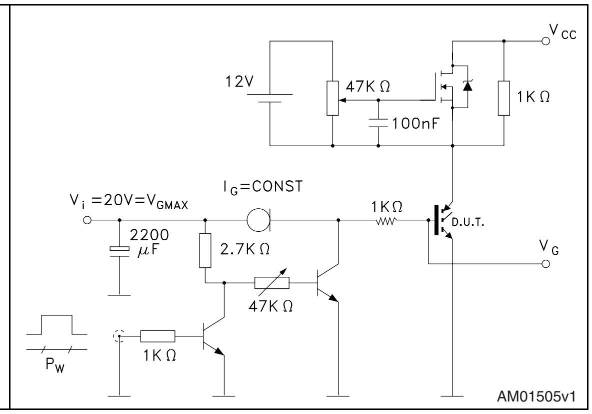


Figure 18. Switching waveform

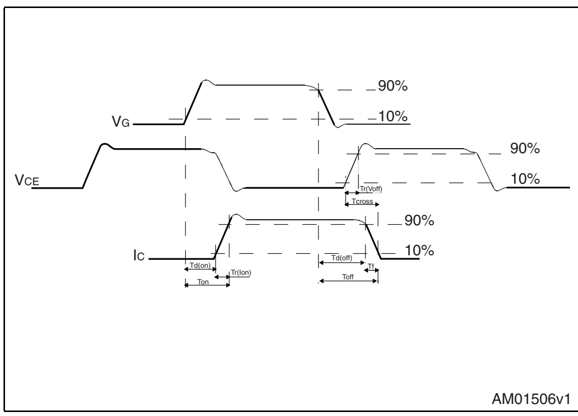
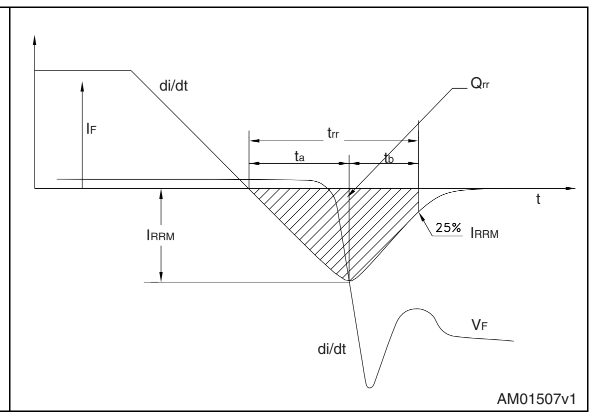


Figure 19. Diode recovery time waveform



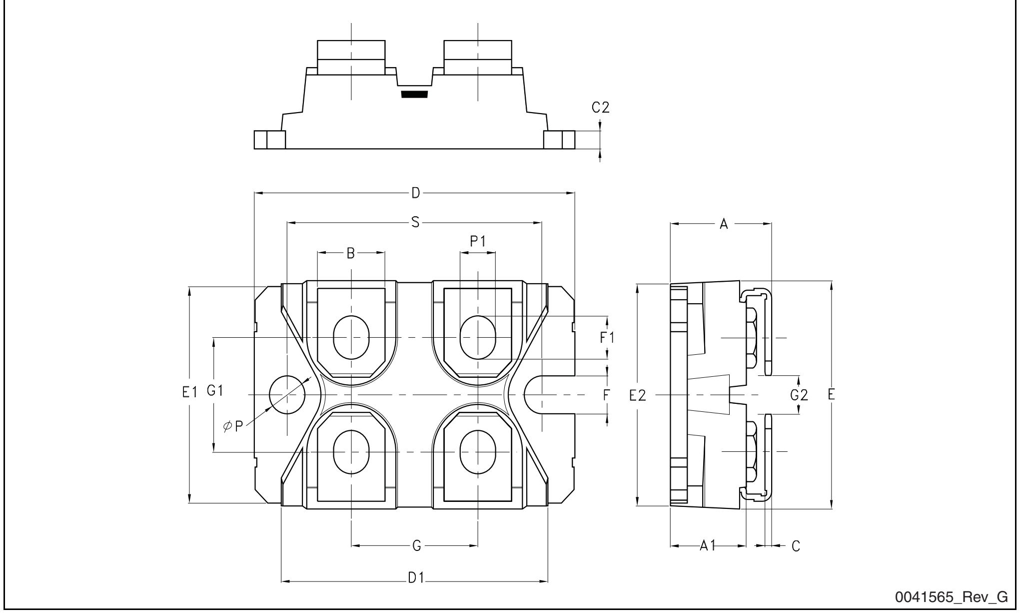
4 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: *www.st.com*. ECOPACK is an ST trademark.

Table 9. ISOTOP mechanical data

Dim.	mm		
	Min.	Typ.	Max.
A	11.80		12.20
A1	8.90		9.10
B	7.80		8.20
C	0.75		0.85
C2	1.95		2.05
D	37.80		38.20
D1	31.50		31.70
E	25.15		25.50
E1	23.85		24.15
E2		24.80	
G	14.90		15.10
G1	12.60		12.80
G2	3.50		4.30
F	4.10		4.30
F1	4.60		5
ϕP	4		4.30
P1	4		4.40
S	30.10		30.30

Figure 20. ISOTOP drawing



5 Revision history

Table 10. Document revision history

Date	Revision	Changes
11-Oct-2006	1	First release
24-Jul-2007	2	Internal schematic diagram has been updated *Figure 1*
23-Apr-2009	3	Updated: mechanical data

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