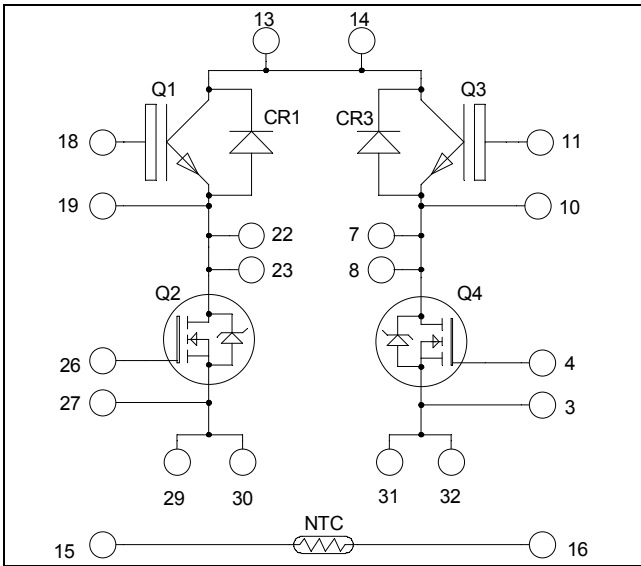


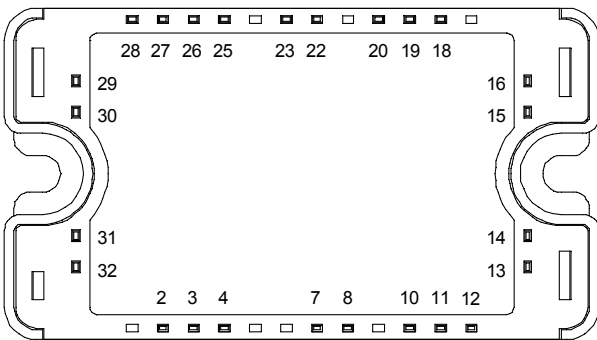
**Full - Bridge  
NPT & Trench + Field Stop<sup>®</sup> IGBT  
Power module**

**Trench & Field Stop<sup>®</sup> IGBT Q1, Q3:**  
 $V_{CES} = 600V$  ;  $I_C = 50A$  @  $T_c = 80^\circ C$

**CoolMOS<sup>™</sup> Q2, Q4:**  
 $V_{CES} = 600V$  ;  $I_C = 49A$  @  $T_c = 25^\circ C$



Top switches : Trench + Field Stop IGBT<sup>®</sup>  
 Bottom switches : CoolMOS<sup>™</sup>



All multiple inputs and outputs must be shorted together  
 13/14 ; 15/16 ; 26/27 ; 31/32

### Application

- Solar converter

### Features

- **Q2, Q4 CoolMOS<sup>™</sup>**
  - Ultra low  $R_{DSon}$
  - Low Miller capacitance
  - Ultra low gate charge
  - Avalanche energy rated
- **Q1, Q3 Trench & Field Stop IGBT<sup>®</sup>**
  - Low voltage drop
  - Switching frequency up to 20 kHz
  - RBSOA & SCSOA rated
  - Low tail current

- Kelvin emitter for easy drive
- Very low stray inductance
- High level of integration
- Internal thermistor for temperature monitoring

### Benefits

- Optimized conduction & switching losses
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Low profile
- Easy paralleling due to positive  $T_C$  of  $V_{CEsat}$
- RoHS Compliant

**CAUTION:** These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on [www.microsemi.com](http://www.microsemi.com)

All ratings @  $T_j = 25^\circ\text{C}$  unless otherwise specified

**1. Top switches**
**1.1 Top Trench + Field Stop IGBT<sup>®</sup> characteristics**
**Absolute maximum ratings**

<i>Symbol</i>	<i>Parameter</i>	<i>Max ratings</i>	<i>Unit</i>
$V_{CES}$	Collector - Emitter Breakdown Voltage	600	V
$I_C$	Continuous Collector Current	$T_C = 25^\circ\text{C}$	80
		$T_C = 80^\circ\text{C}$	50
$I_{CM}$	Pulsed Collector Current	$T_C = 25^\circ\text{C}$	100
$V_{GE}$	Gate - Emitter Voltage	$\pm 20$	V
$P_D$	Maximum Power Dissipation	$T_C = 25^\circ\text{C}$	176
RBSOA	Reverse Bias Safe Operating Area	$T_j = 150^\circ\text{C}$	100A @ 550V

**Electrical Characteristics**

<i>Symbol</i>	<i>Characteristic</i>	<i>Test Conditions</i>	<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
$I_{CES}$	Zero Gate Voltage Collector Current	$V_{GE} = 0\text{V}, V_{CE} = 600\text{V}$			250	$\mu\text{A}$
$V_{CE(sat)}$	Collector Emitter Saturation Voltage	$V_{GE} = 15\text{V}$ $I_C = 50\text{A}$	$T_j = 25^\circ\text{C}$	1.5	1.9	V
			$T_j = 150^\circ\text{C}$	1.7		
$V_{GE(th)}$	Gate Threshold Voltage	$V_{GE} = V_{CE}, I_C = 600\mu\text{A}$	5.0	5.8	6.5	V
$I_{GES}$	Gate - Emitter Leakage Current	$V_{GE} = 20\text{V}, V_{CE} = 0\text{V}$			600	nA

**Dynamic Characteristics**

<i>Symbol</i>	<i>Characteristic</i>	<i>Test Conditions</i>	<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
$C_{ies}$	Input Capacitance	$V_{GE} = 0\text{V}$ $V_{CE} = 25\text{V}$ $f = 1\text{MHz}$		3150		pF
$C_{oes}$	Output Capacitance			200		
$C_{res}$	Reverse Transfer Capacitance			95		
$T_{d(on)}$	Turn-on Delay Time	Inductive Switching ( $25^\circ\text{C}$ ) $V_{GE} = \pm 15\text{V}$ $V_{Bus} = 300\text{V}$ $I_C = 50\text{A}$ $R_G = 8.2\Omega$		110		ns
$T_r$	Rise Time			45		
$T_{d(off)}$	Turn-off Delay Time			200		
$T_f$	Fall Time			40		
$T_{d(on)}$	Turn-on Delay Time	Inductive Switching ( $150^\circ\text{C}$ ) $V_{GE} = \pm 15\text{V}$ $V_{Bus} = 300\text{V}$ $I_C = 50\text{A}$ $R_G = 8.2\Omega$		120		ns
$T_r$	Rise Time			50		
$T_{d(off)}$	Turn-off Delay Time			250		
$T_f$	Fall Time			60		
$E_{on}$	Turn-on Switching Energy	$V_{GE} = \pm 15\text{V}$ $V_{Bus} = 300\text{V}$ $I_C = 50\text{A}$ $R_G = 8.2\Omega$	$T_j = 25^\circ\text{C}$	0.3		mJ
			$T_j = 150^\circ\text{C}$	0.43		
$E_{off}$	Turn-off Switching Energy	$I_C = 50\text{A}$ $R_G = 8.2\Omega$	$T_j = 25^\circ\text{C}$	1.35		mJ
			$T_j = 150^\circ\text{C}$	1.75		
$R_{thJC}$	Junction to Case Thermal resistance				0.85	$^\circ\text{C/W}$

## 1.2 Top fast diode characteristics

<i>Symbol</i>	<i>Characteristic</i>	<i>Test Conditions</i>		<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
V <sub>RRM</sub>	Maximum Peak Repetitive Reverse Voltage			600			V
I <sub>RM</sub>	Maximum Reverse Leakage Current	V <sub>R</sub> =600V	T <sub>j</sub> = 25°C			25	μA
			T <sub>j</sub> = 125°C			500	
I <sub>F</sub>	DC Forward Current		T <sub>c</sub> = 80°C		30		A
V <sub>F</sub>	Diode Forward Voltage	I <sub>F</sub> = 30A			1.8	2.3	V
		I <sub>F</sub> = 60A			2.1		
		I <sub>F</sub> = 30A	T <sub>j</sub> = 125°C		1.5		
t <sub>rr</sub>	Reverse Recovery Time	I <sub>F</sub> = 30A	T <sub>j</sub> = 25°C		25		ns
			T <sub>j</sub> = 125°C		160		
Q <sub>rr</sub>	Reverse Recovery Charge	V <sub>R</sub> = 400V di/dt = 200A/μs	T <sub>j</sub> = 25°C		35		nC
			T <sub>j</sub> = 125°C		480		
R <sub>thJC</sub>	Junction to Case Thermal resistance					1.2	°C/W

## 2. Bottom switches

### 2.1 Bottom CoolMOS™ characteristics

#### Absolute maximum ratings

<i>Symbol</i>	<i>Parameter</i>	<i>Max ratings</i>	<i>Unit</i>
V <sub>DSS</sub>	Drain - Source Breakdown Voltage	600	V
I <sub>D</sub>	Continuous Drain Current	T <sub>c</sub> = 25°C	49
		T <sub>c</sub> = 80°C	38
I <sub>DM</sub>	Pulsed Drain current	130	A
V <sub>GS</sub>	Gate - Source Voltage	±20	V
R <sub>DS(on)</sub>	Drain - Source ON Resistance	45	mΩ
P <sub>D</sub>	Maximum Power Dissipation	T <sub>c</sub> = 25°C	290
I <sub>AR</sub>	Avalanche current (repetitive and non repetitive)	15	A
E <sub>AR</sub>	Repetitive Avalanche Energy	3	mJ
E <sub>AS</sub>	Single Pulse Avalanche Energy	1900	

#### Electrical Characteristics

<i>Symbol</i>	<i>Characteristic</i>	<i>Test Conditions</i>		<i>Min</i>	<i>Typ</i>	<i>Max</i>	<i>Unit</i>
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 600V	T <sub>j</sub> = 25°C			250	μA
		V <sub>GS</sub> = 0V, V <sub>DS</sub> = 600V	T <sub>j</sub> = 125°C			500	
R <sub>DS(on)</sub>	Drain – Source on Resistance	V <sub>GS</sub> = 10V, I <sub>D</sub> = 24.5A			40	45	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> = V <sub>DS</sub> , I <sub>D</sub> = 3mA		2.1	3	3.9	V
I <sub>GSS</sub>	Gate – Source Leakage Current	V <sub>GS</sub> = ±20 V, V <sub>DS</sub> = 0V				100	nA

**Dynamic Characteristics**

Symbol	Characteristic	Test Conditions	Min	Typ	Max	Unit
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0V ; V <sub>DS</sub> = 25V		7.2		nF
C <sub>rss</sub>	Reverse Transfer Capacitance	f = 1MHz		0.29		
Q <sub>g</sub>	Total gate Charge	V <sub>GS</sub> = 10V V <sub>Bus</sub> = 300V I <sub>D</sub> = 49A		150		nC
Q <sub>gs</sub>	Gate – Source Charge			34		
Q <sub>gd</sub>	Gate – Drain Charge			51		
T <sub>d(on)</sub>	Turn-on Delay Time	<b>Inductive Switching (125°C)</b> V <sub>GS</sub> = 10V V <sub>Bus</sub> = 400V I <sub>D</sub> = 49A R <sub>G</sub> = 4.7Ω		21		ns
T <sub>r</sub>	Rise Time			30		
T <sub>d(off)</sub>	Turn-off Delay Time			100		
T <sub>f</sub>	Fall Time			45		
E <sub>on</sub>	Turn-on Switching Energy	<b>Inductive switching @ 25°C</b> V <sub>GS</sub> = 10V ; V <sub>Bus</sub> = 400V I <sub>D</sub> = 49A ; R <sub>G</sub> = 4.7Ω		675		μJ
E <sub>off</sub>	Turn-off Switching Energy			520		
E <sub>on</sub>	Turn-on Switching Energy	<b>Inductive switching @ 125°C</b> V <sub>GS</sub> = 10V ; V <sub>Bus</sub> = 400V I <sub>D</sub> = 49A ; R <sub>G</sub> = 4.7Ω		1100		μJ
E <sub>off</sub>	Turn-off Switching Energy			635		
R <sub>thJC</sub>	Junction to Case Thermal resistance				0.5	°C/W

**3. Temperature sensor**

NTC (see application note APT0406 on www.microsemi.com for more information).

Symbol	Characteristic	Min	Typ	Max	Unit
R <sub>25</sub>	Resistance @ 25°C		50		kΩ
B <sub>25/85</sub>	T <sub>25</sub> = 298.15 K		3952		K

$$R_T = \frac{R_{25}}{\exp\left[B_{25/85}\left(\frac{1}{T_{25}} - \frac{1}{T}\right)\right]}$$

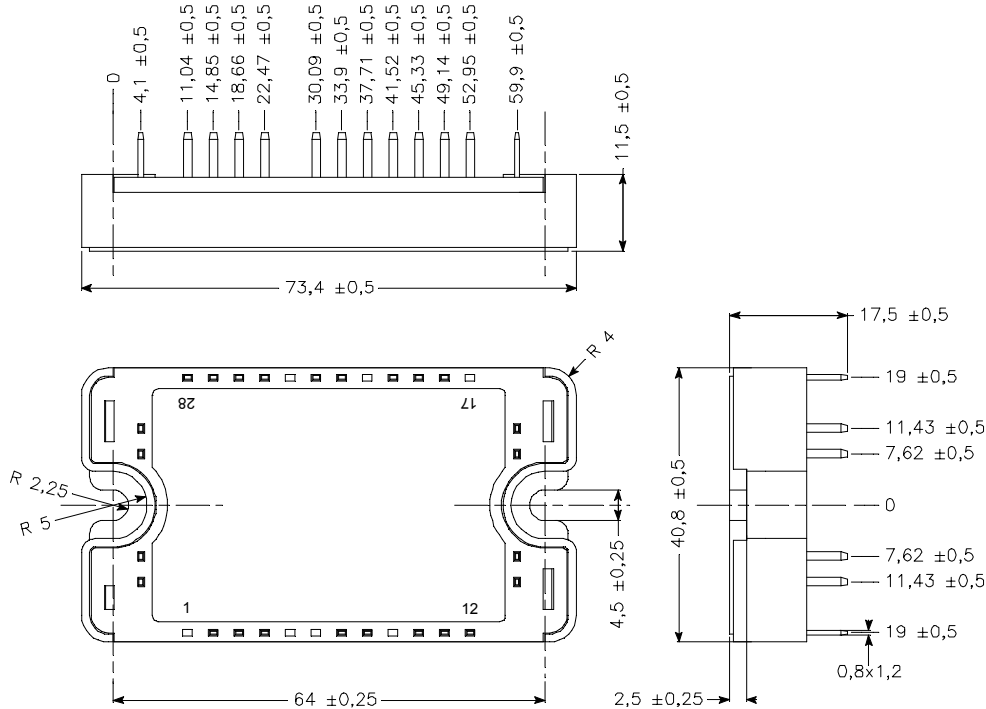
T: Thermistor temperature  
R<sub>T</sub>: Thermistor value at T

**4. Package characteristics**

Symbol	Characteristic	Min	Typ	Max	Unit	
V <sub>ISOL</sub>	RMS Isolation Voltage, any terminal to case t=1 min, I <sub>isol</sub> <1mA, 50/60Hz	2500			V	
T <sub>J</sub>	Operating junction temperature range	-40		150*	°C	
T <sub>STG</sub>	Storage Temperature Range	-40		125		
T <sub>C</sub>	Operating Case Temperature	-40		100		
Torque	Mounting torque	To heatsink	M4	2.5	4.7	N.m
Wt	Package Weight				110	g

T<sub>j</sub>=175°C for Trench & Field Stop IGBT

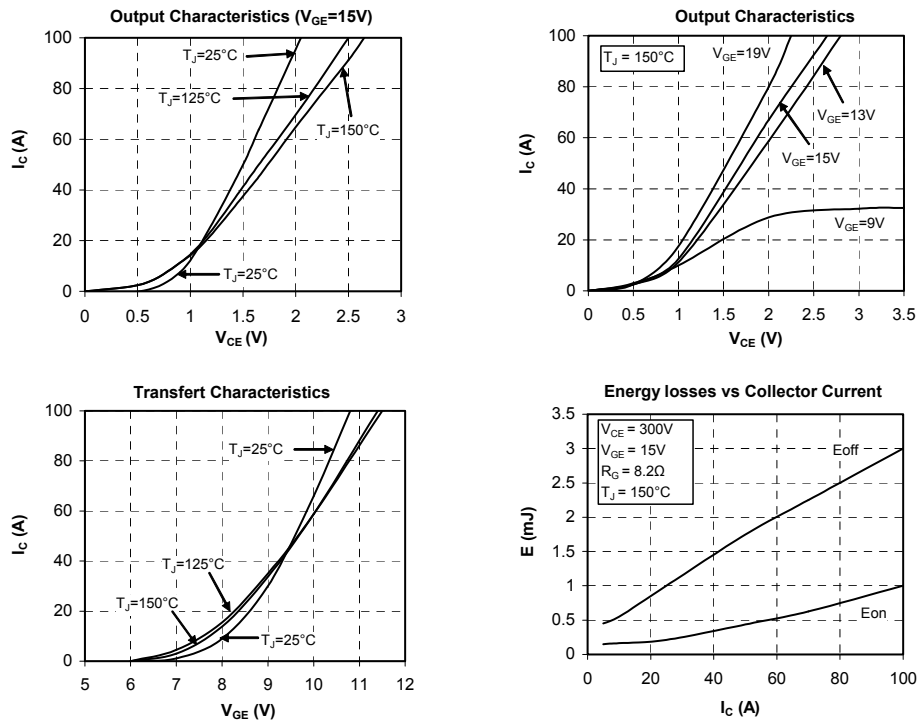
## 5. SP3 Package outline (dimensions in mm)

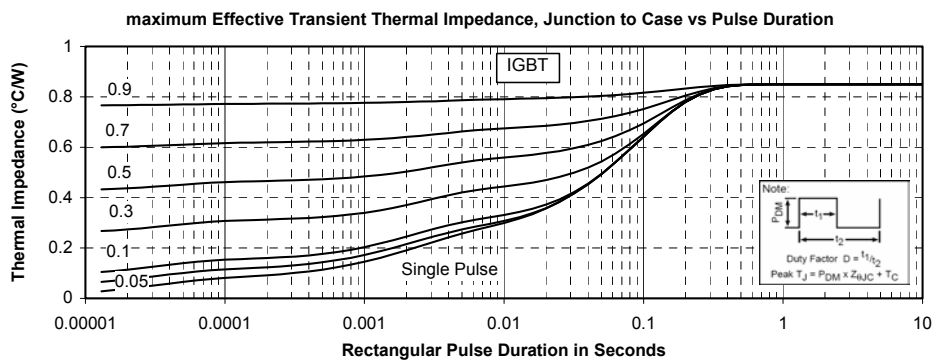
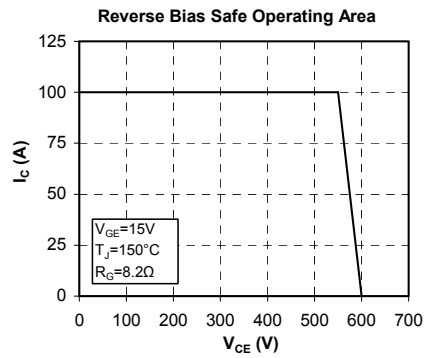
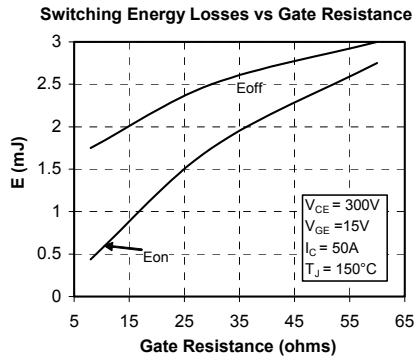


See application note 1901 - Mounting Instructions for SP3 Power Modules on [www.microsemi.com](http://www.microsemi.com)

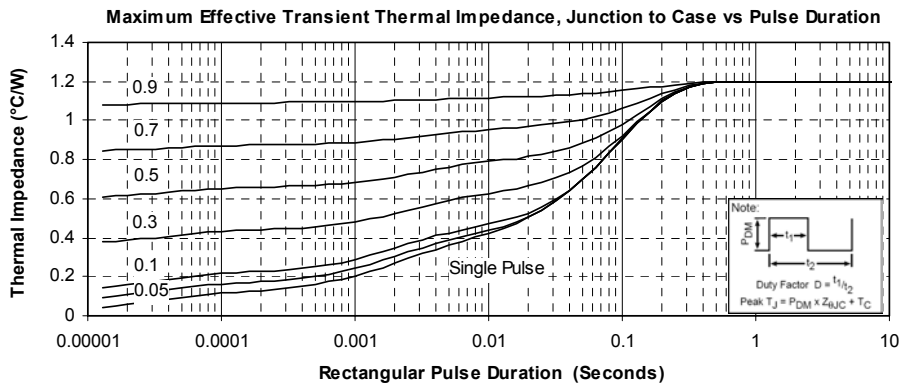
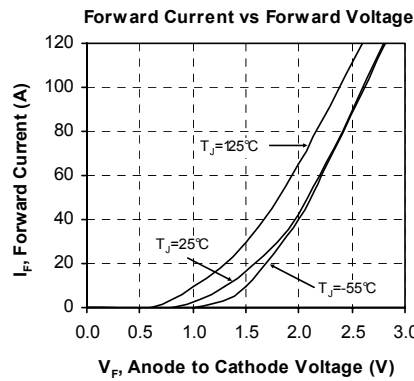
## 6. Top switches curves

### 6.1 Top Trench + Field Stop IGBT® typical performance curves



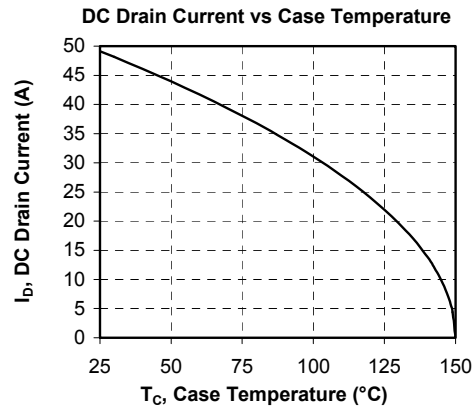
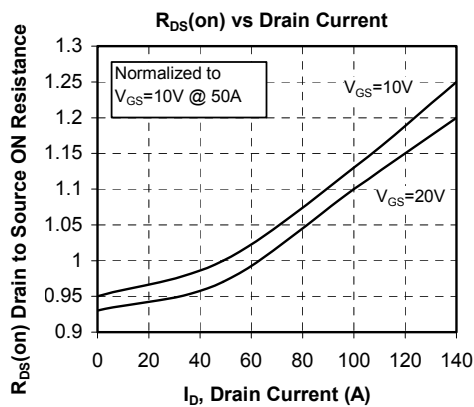
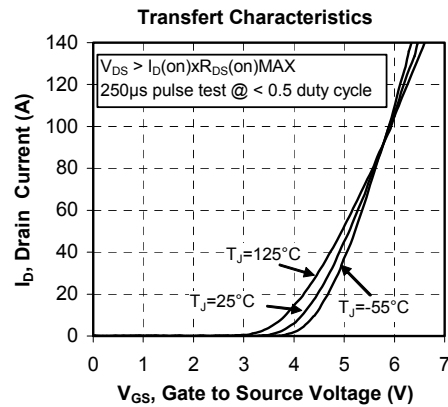
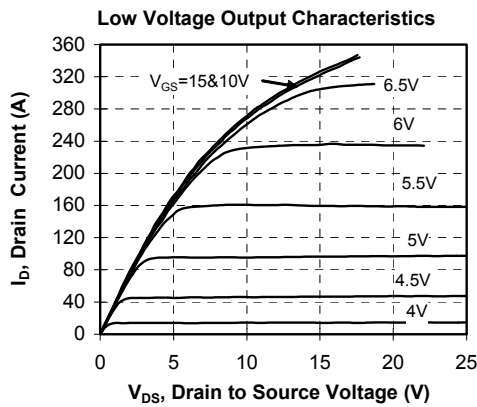
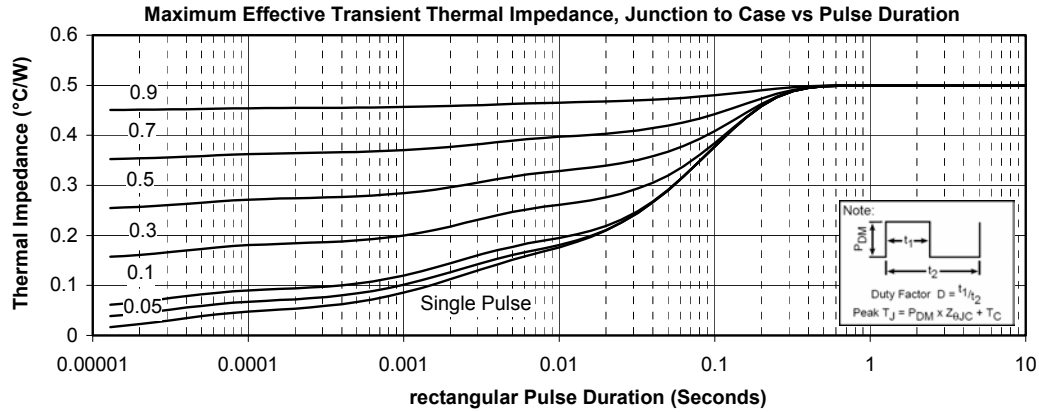


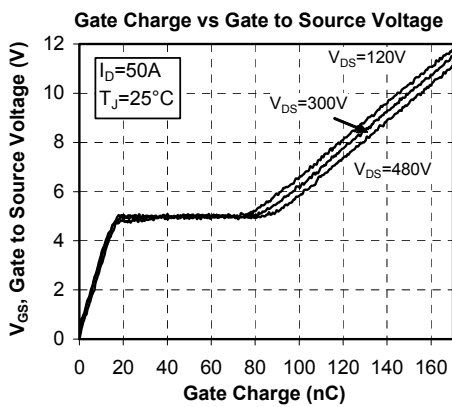
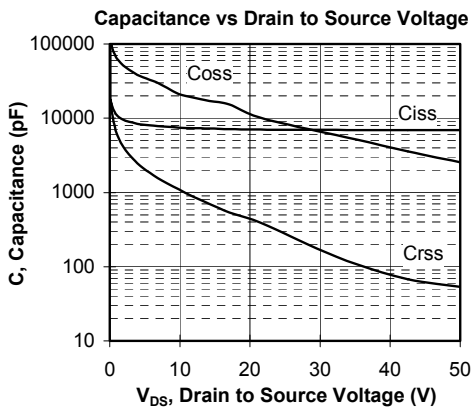
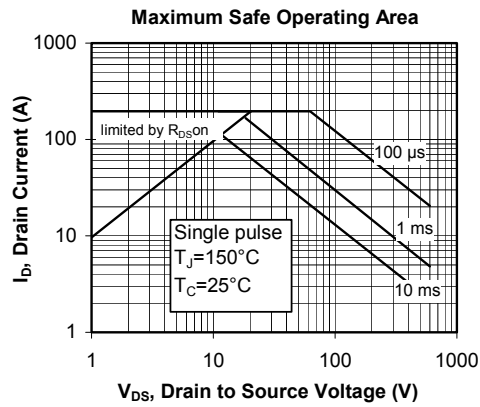
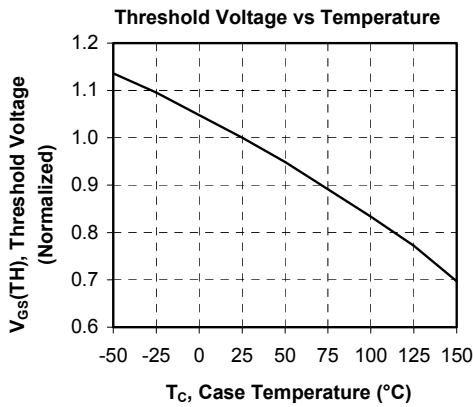
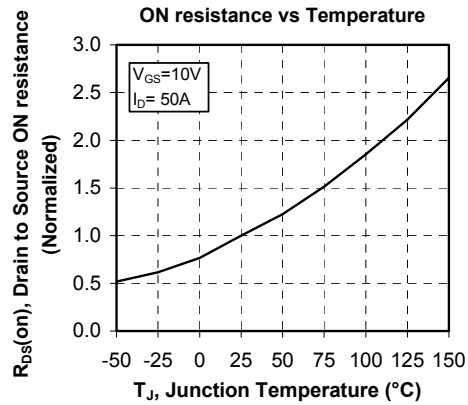
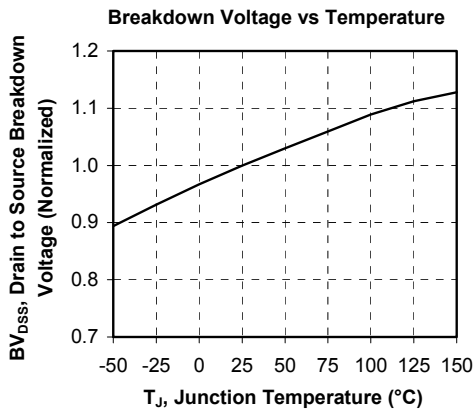
## 6.2 Top Fast diode typical performance curves



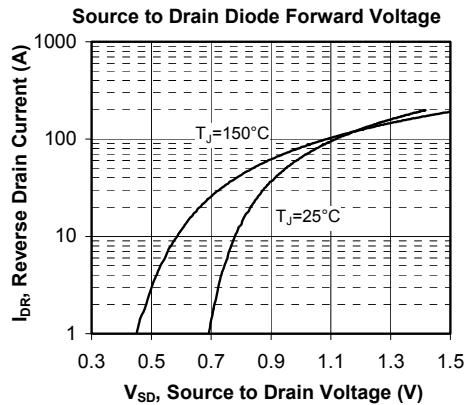
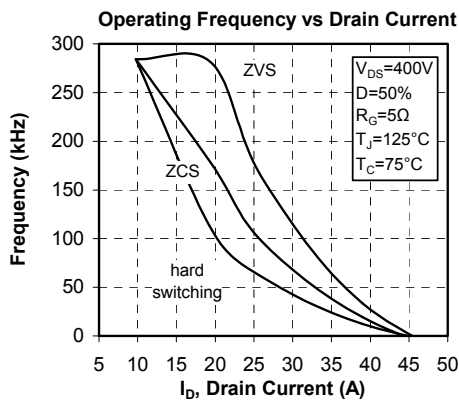
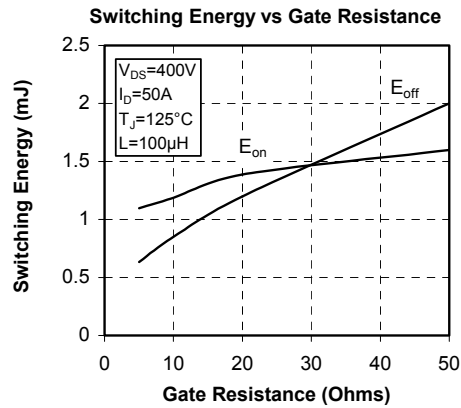
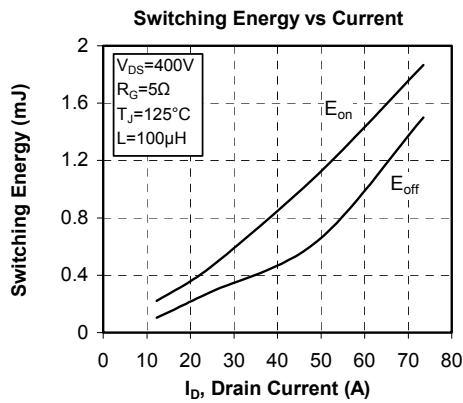
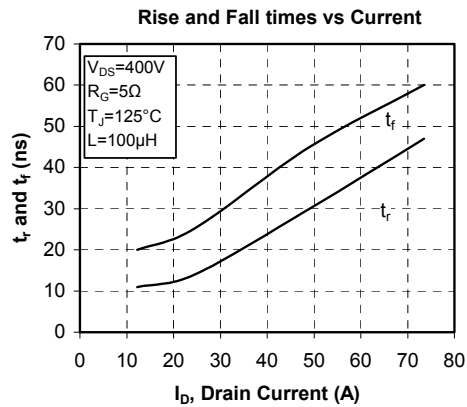
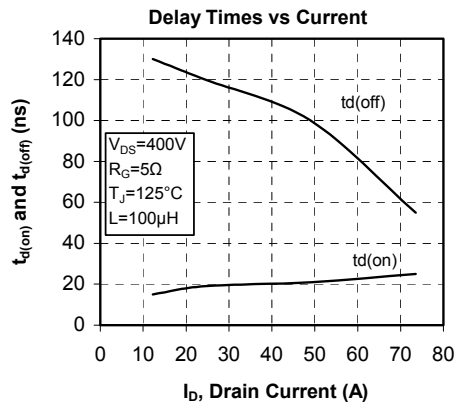
## 7. Bottom switches curves

### 7.1 Bottom CoolMOS™ typical performance curves









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Microsemi's products are covered by one or more of U.S. patents 4,895,810 5,045,903 5,089,434 5,182,234 5,019,522 5,262,336 6,503,786 5,256,583 4,748,103 5,283,202 5,231,474 5,434,095 5,528,058 and foreign patents. U.S. and Foreign patents pending. All Rights Reserved.