

## **STE50DE100**

Hybrid Emitter Switched Bipolar Transistor ESBT $^{\odot}$  1000 V - 50 A - 0.026  $\Omega$ 

### **General features**

V <sub>CS(ON)</sub>	I <sub>C</sub>	R <sub>CS(ON)</sub>
1.3 V	50 A	0.026 Ω

- High voltage / high current Cascode configuration
- Ultra low equivalent on resistance
- Very fast-switch up to 150 kHz
- Ultra low C<sub>iss</sub>
- Low dynamic V<sub>CS(ON)</sub>

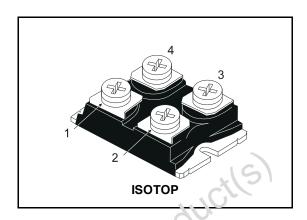
### **Applications**

- Industrial converters
- Welding

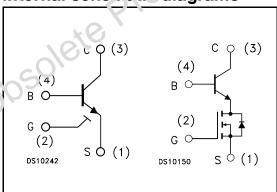
#### **Description**

The STE50DE100 is manufactured in a hybrid structure, using dedicated high voltage Bipolar and low voltage MOSFET technologies, aimed to providing the best performance in ESBT topology.

The STE50DE100 is designed for use in industrial converters and/or welding equipment.



### Internal schematic diagrams



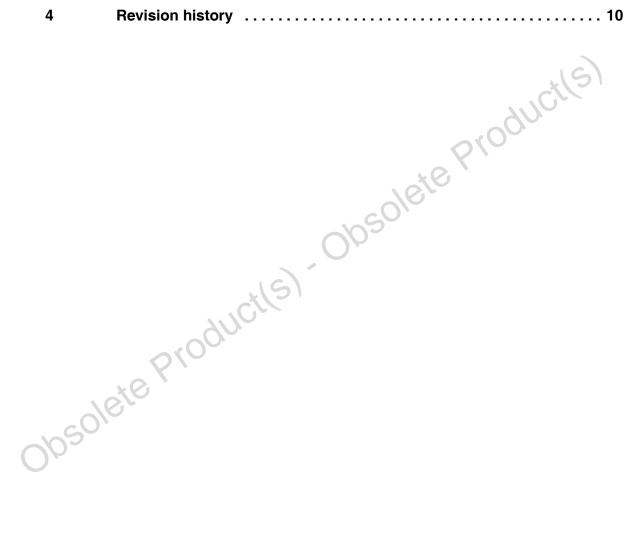
#### Order codes

Part Number	Marking	Package	Packing	
STE50DE100	STE50DE100	ISOTOP	Tube	

January 2007 Rev 2 1/11

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STE50DE100 Electrical ratings

# 1 Electrical ratings

Table 1. Absolute maximum rating

Symbol	Parameter	Value	Unit
V <sub>CS(SS)</sub>	Collector-source voltage (V <sub>BS</sub> = V <sub>GS</sub> = 0 V)	1000	V
V <sub>BS(OS)</sub>	Base-source voltage (I <sub>C</sub> = 0, V <sub>GS</sub> = 0 V)	40	V
V <sub>SB(OS)</sub>	Source-base voltage (I <sub>C</sub> = 0, V <sub>GS</sub> = 0 V)	12	V
V <sub>GS</sub>	Gate-source voltage	± 20	٧
I <sub>C</sub>	Collector current	50	Α
I <sub>CM</sub>	Collector peak current (t <sub>P</sub> < 5ms)	150	Α
I <sub>B</sub>	Base current	10	Α
I <sub>BM</sub>	Base peak current (t <sub>P</sub> < 5ms)	50	А
P <sub>tot</sub>	Total dissipation at T <sub>c</sub> = 25°C	160	W
V <sub>INS</sub>	Insulation withstand voltage (AC-RMS) from all four leads to external heatsink	2500	٧
T <sub>stg</sub>	Storage temperature	-40 to 150	°C
TJ	Max. operating junction temperature	150	°C

Table 2. Thermal data

	Symbol	Parameter	Value	Unit
	R <sub>thj-case</sub>	Thermal resistance junction-case max	0.78	°C/W
	R <sub>thc-h</sub>	Thermal resistance case-heatsink with conductive greas	e 0.05	°C/W
		applied max		
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Electrical characteristics STE50DE100

## 2 Electrical characteristics

 $(T_{case} = 25^{\circ}C \text{ unless otherwise specified})$ 

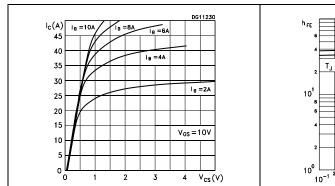
Table 3. Electrical characteristics

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
I <sub>CS(SS)</sub>	Collector-source current (V <sub>BS</sub> = V <sub>GS</sub> = 0)	V <sub>CE</sub> = 1000V			100	μА
I <sub>BS(OS)</sub>	Base-source current (I <sub>C</sub> = 0, V <sub>GS</sub> = 0)	V <sub>BS(OS)</sub> = 40V			10	μА
I <sub>SB(OS)</sub>	Source-base current (I <sub>C</sub> = 0, V <sub>GS</sub> = 0)	V <sub>SB(OS)</sub> = 10V			100	μА
I <sub>GS(OS)</sub>	Gate-source leakage	$V_{GS} = \pm 20V$			500	nA
V <sub>CS(ON)</sub>	Collector-source ON voltage	V <sub>GS</sub> = 10V I <sub>C</sub> = 50A I <sub>B</sub> = 10A V <sub>GS</sub> = 10V I <sub>C</sub> = 30A I <sub>B</sub> = 3A		1.3 1.1	1/3	V V
h <sub>FE</sub>	DC current gain	$V_{GS} = 10V I_C = 50A V_{CS} = 1V$ $V_{GS} = 10V I_C = 30A V_{CS} = 1V$	3 6		7 13	
V <sub>BS(ON)</sub>	Base Source ON voltage	$V_{GS} = 10V  I_C = 50A  I_B = 10A$ $V_{GS} = 10V  I_C = 30A  I_B = 3A$		2.2 1.4		V V
V <sub>GS(th)</sub>	Gate threshold voltage	$V_{BS} = V_{GS}$ $I_B = 250 \mu A$	3	3.7	4.5	٧
C <sub>ISS</sub>	Input capacitance	$V_{CS} = 25V$ $f = 1MHz$ $V_{GS} = V_{CB} = 0$		2500		pF
Q <sub>GS(tot)</sub>	Gate-source charge	$V_{CS} = 25V$ $V_{GS} = 10V$ $V_{CB} = 0$ $I_C = 50A$		60		nC
t <sub>s</sub>	INDUCTIVE LOAD Storage time Fall time	$I_C = 25A$ $I_B = 5A$ $V_{GS} = 10V$ $V_{Clamp} = 800V$ $R_G = 47\Omega$ $t_p = 4\mu s$ (see figure 13)		650 10		ns ns
t <sub>s</sub>	INDUCTIVE LOAD Storage time Fall time	$\begin{split} &I_C = 25 A  I_B = 2.5 A \ V_{GS} = 10 V \\ &V_{Clamp} = 800 V  R_G = 47 \Omega \\ &t_p = 4 \mu s \qquad \text{(see figure 13)} \end{split}$		430 6		ns ns
V <sub>CSW</sub>	Maximum collector- source voltage switched without snubber	$R_{G} = 47\Omega$ $h_{FE} = 5A$ $I_{C} = 35A$	1000			V
V <sub>CS(dyn)</sub>	Collector-source dynamic voltage (500ns)	$\begin{aligned} &V_{CC} = V_{Clamp} = 300V  V_{GS} = 10V \\ &R_G = 47\Omega  I_C = 5A  I_B = 5A \\ &I_{Bpeak} = I_C = 25A  t_{peak} = 500ns \end{aligned}$		5.5		V
V <sub>CS(dyn)</sub>	Collector-source dynamic voltage (1 µs)	$\begin{aligned} &V_{CC} = V_{Clamp} = 300V & V_{GS} = 10V \\ &R_G = 47\Omega & I_C = 5A & I_B = 5A \\ &I_{Bpeak} = I_C = 25A & t_{peak} = 500ns \end{aligned}$		4.8		٧

## 2.1 Electrical characteristics (curves)

Figure 1. Output characteristics

Figure 2. DC current gain



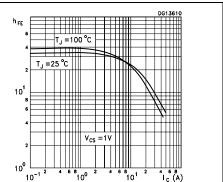
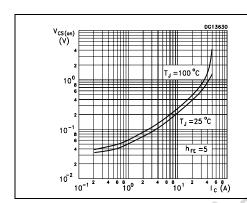


Figure 3. Collector-source On voltage Figure 4. Collector-source On voltage



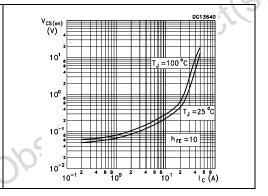
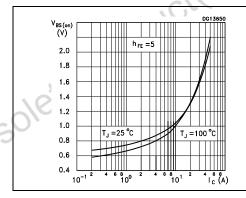
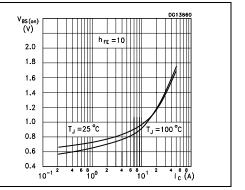


Figure 5. Base-source On voltage

Figure 6. Base-source On voltage





Electrical characteristics STE50DE100

Figure 7. Reverse biased safe operting Figure 8. Gate threshold voltage vs area temperature

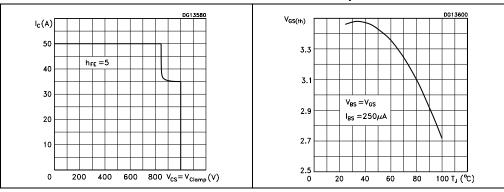


Figure 9. Dynamic collector-emitter saturation voltage

Figure 10. Inductive load switching time

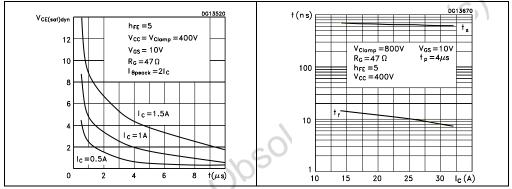
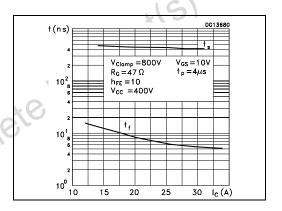


Figure 11. Inductive load switching time



## 2.2 Test circuits

Figure 12. Static V<sub>CS(ON)</sub> test circuit

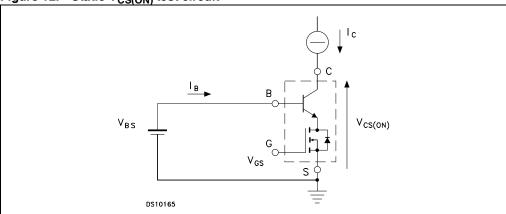


Figure 13. Inductive load switching and RBSOA test circuit

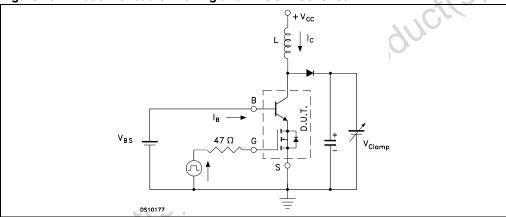
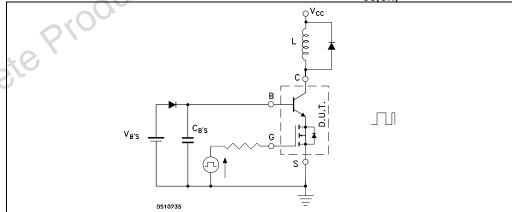


Figure 14. Inductive load turn-on switching and dynamic  $V_{CS(ON)}$  test circuit



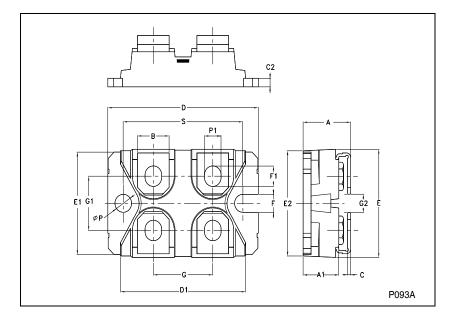
## 3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com



#### **ISOTOP MECHANICAL DATA**

DIM.	mm			inch		
DIIVI.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
Α	11.8		12.2	0.465		0.480
A1	8.9		9.1	0.350		0.358
В	7.8		8.2	0.307		0.322
С	0.75		0.85	0.029		0.033
C2	1.95		2.05	0.076		0.080
D	37.8		38.2	1.488		1.503
D1	31.5		31.7	1.240		1.248
Е	25.15		25.5	0.990		1.003
E1	23.85		24.15	0.938		0.950
E2		24.8			0.976	
G	14.9		15.1	0.586		0.594
G1	12.6		12.8	0.496		0.503
G2	3.5		4.3	0.137		1.169
F	4.1		4.3	0.161		0.169
F1	4.6		5	0.181		0.196
Р	4		4.3	0.157		0.169
P1	4		4.4	0.157		0.173
S	30.1		30.3	1.185		1.193



Obsolete

Revision history STE50DE100

## 4 Revision history

Table 4. Revision history

Date	Revision	Changes		
06-Oct-2004	1	Initial release.		
22-Jan-2007	2	The document has been reformatted		



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