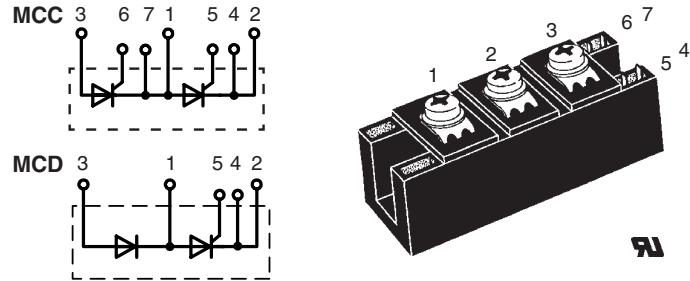


High Voltage Thyristor Module

I_{TRMS} = 2x300 A
I_{TAVM} = 2x165 A
V_{RRM} = 2000-2200 V

V _{RSM} V _{DSM}	V _{RRM} V _{DRM}	Type
2100	2000	MCC 161-20io1 MCD 161-20io1
2300	2200	MCC 161-22io1 MCD 161-22io1



Symbol	Conditions	Maximum Ratings	
I _{TRMS}	T _{VJ} = T _{VJM}	300	A
I _{TAVM}	T _C = 85°C; 180° sine	165	A
I _{TSM}	T _{VJ} = 45°C; t = 10 ms (50 Hz) V _R = 0 t = 8.3 ms (60 Hz)	6000	A
		6400	A
	T _{VJ} = T _{VJM} ; t = 10 ms (50 Hz) V _R = 0 t = 8.3 ms (60 Hz)	5250	A
		5600	A
I ² dt	T _{VJ} = 45°C; t = 10 ms (50 Hz) V _R = 0 t = 8.3 ms (60 Hz)	180000	A ² s
		170000	A ² s
	T _{VJ} = T _{VJM} ; t = 10 ms (50 Hz) V _R = 0 t = 8.3 ms (60 Hz)	137000	A ² s
		128000	A ² s
(di/dt) _{cr}	T _{VJ} = T _{VJM} ; repetitive, I _T = 500 A f = 50 Hz; t _p = 200 µs; V _D = 2/3 V _{DRM} ; _____ I _G = 0.5 A; non repetitive, I _T = I _{TAVM} di _G /dt = 0.5 A/µs	150	A/µs
(dv/dt) _{cr}	T _{VJ} = T _{VJM} ; V _{DR} = 2/3 V _{DRM} R _{GK} = ∞; method 1 (linear voltage rise)	1000	V/µs
P _{GM}	T _{VJ} = T _{VJM} ; t _p = 30 µs I _T = I _{TAVM} ; t _p = 500 µs	120	W
		60	W
P _{GAV}		8	W
V _{RGM}		10	V
T _{VJ}		-40...125	°C
T _{VJM}		125	°C
T _{stg}		-40...125	°C
V _{ISOL}	50/60 Hz, RMS; t = 1 min I _{ISOL} ≤ 1 mA; t = 1 s	3000	V~
		3600	V~
M _d	Mounting torque (M6) Terminal connection torque (M6)	2.25-2.75 4.5-5.5	Nm Nm
Weight	Typical including screws	125	g

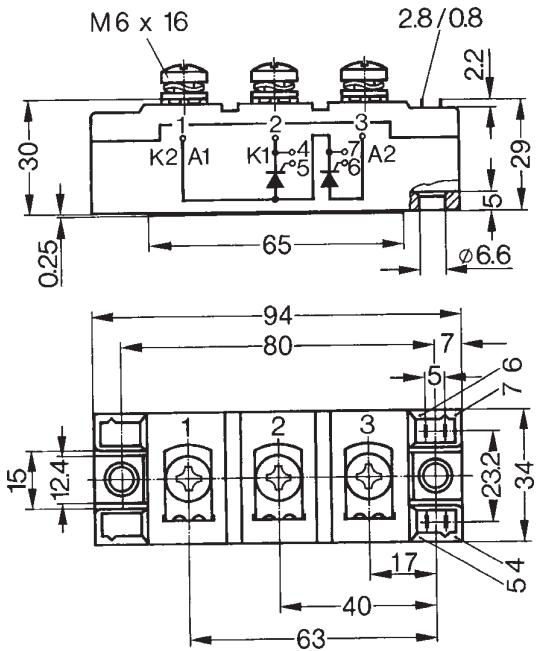
Data according to IEC 60747 and refer to a single thyristor/diode unless otherwise stated

IXYS reserves the right to change limits, test conditions and dimensions

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Symbol	Conditions	Characteristic Values	
I_{RRM}, I_{DRM}	$V_R = V_{RRM}; T_{VJ} = T_{VJM}$	40	mA
V_T	$I_T = 300A; T_{VJ} = 25^\circ C$	1.36	V
V_{TO}	For power-loss calculations only ($T_{VJ} = T_{VJM}$)	0.8	V
r_T		1.6	$m\Omega$
V_{GT}	$V_D = 6 V; T_{VJ} = 25^\circ C$ $T_{VJ} = -40^\circ C$	2	V
I_{GT}	$V_D = 6 V; T_{VJ} = 25^\circ C$ $T_{VJ} = -40^\circ C$	150	mA
V_{GD}	$V_D = \frac{2}{3}V_{DRM}; T_{VJ} = T_{VJM}$	0.25	V
I_{GD}	$V_D = \frac{2}{3}V_{DRM}; T_{VJ} = T_{VJM}$	10	mA
I_L	$T_{VJ} = 25^\circ C; V_D = 6 V; t_p = 30 \mu s$ $dI_G/dt = 0.45 A/\mu s; I_G = 0.45 A$	200	mA
I_H	$T_{VJ} = 25^\circ C; V_D = 6 V; R_{GK} = \infty$	150	mA
t_{gd}	$T_{VJ} = 25^\circ C; V_D = 1/2 V_{DRM}$ $dI_G/dt = 0.5 A/\mu s; I_G = 0.5 A$	2	μs
t_q	$T_{VJ} = T_{VJM}; V_R = 100 V; V_D = \frac{2}{3}V_{DRM}; t_p = 200 \mu s$ $dV/dt = 20 V/\mu s; I_T = 160 A; -dI/dt = 10 A/\mu s$	typ. 150	μs
Q_s	$T_{VJ} = T_{VJM}$	550	μC
I_{RM}	$-dI/dt = 50 A/\mu s; I_T = 300 A$	235	A
R_{thJC}	per thyristor; DC current	0.155	K/W
	per module	0.078	K/W
R_{thJK}	per thyristor; DC current	0.225	K/W
	per module	0.113	K/W
d_s	Creeping distance on surface	12.7	mm
d_a	Creepage distance in air	9.6	mm
a	Maximum allowable acceleration	50	m/s^2

Dimensions in mm (1 mm = 0.0394")



Optional accessories for modules

Keyed gate/cathode twin plugs with wire length = 350 mm, gate = yellow, cathode = red
 Type **ZY 180L** (L = Left for pin pair 4/5) UL 758, style 1385,
 Type **ZY 180R** (R = right for pin pair 6/7) CSA class 5851, guide 460-1-1

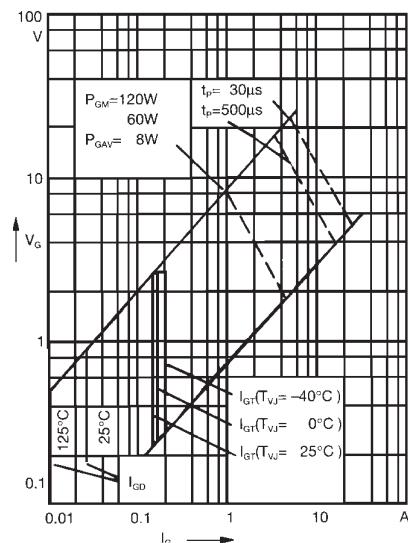


Fig. 1 Gate trigger characteristics

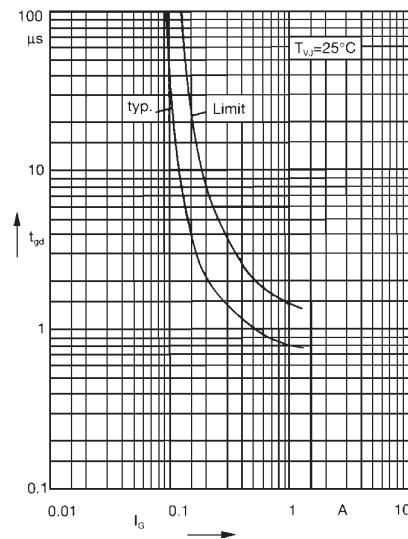


Fig. 2 Gate trigger delay time

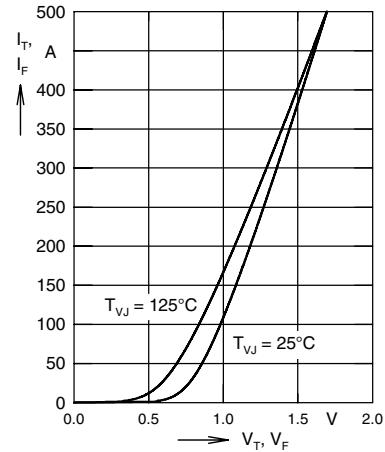


Fig. 3: Forward current vs. voltage drop per thyristor/diode

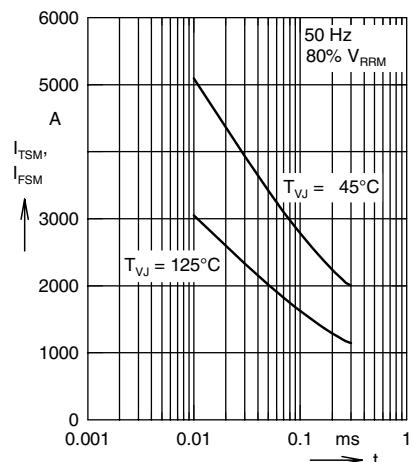
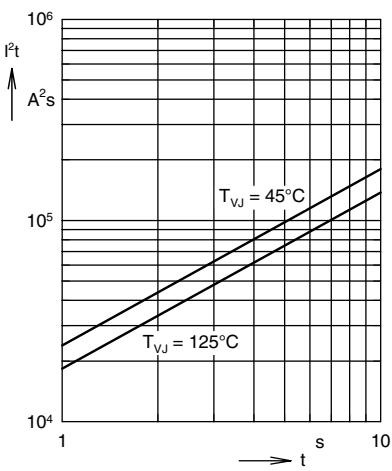
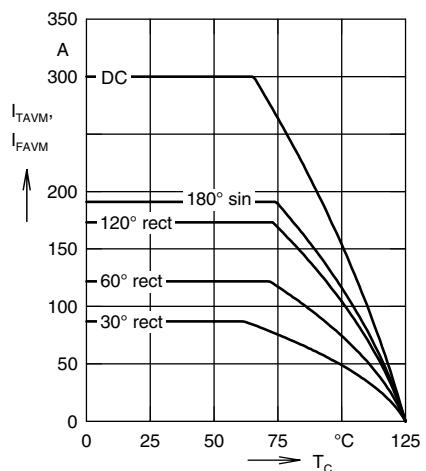
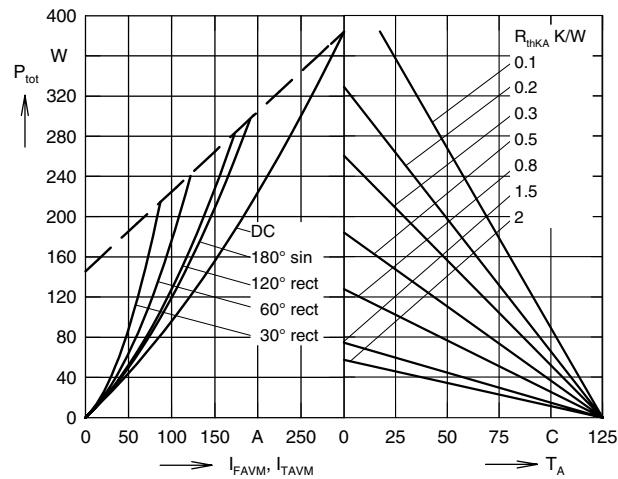
Fig. 4: Surge overload current
 $I_{TSM}, I_{FSM} = f(t)$ Fig. 5: I^2t versus time per diodeFig. 6: Max. forward current at case temperature $I_{TAVM/FAVM} = f(T_C, d)$ 

Fig. 7: Power dissipation vs. on-state current and ambient temperature (per thyristor/diode)

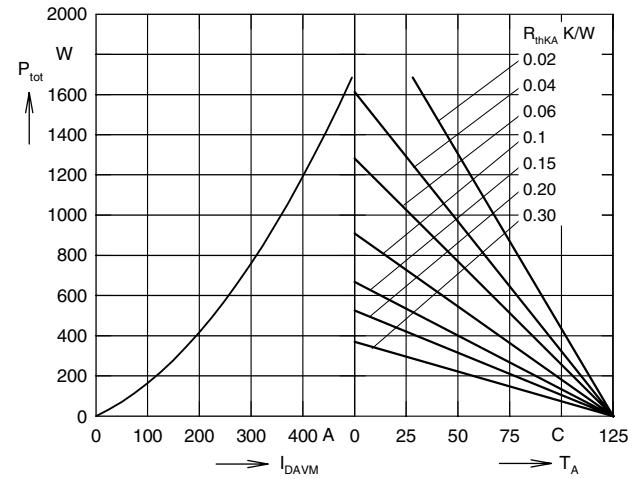
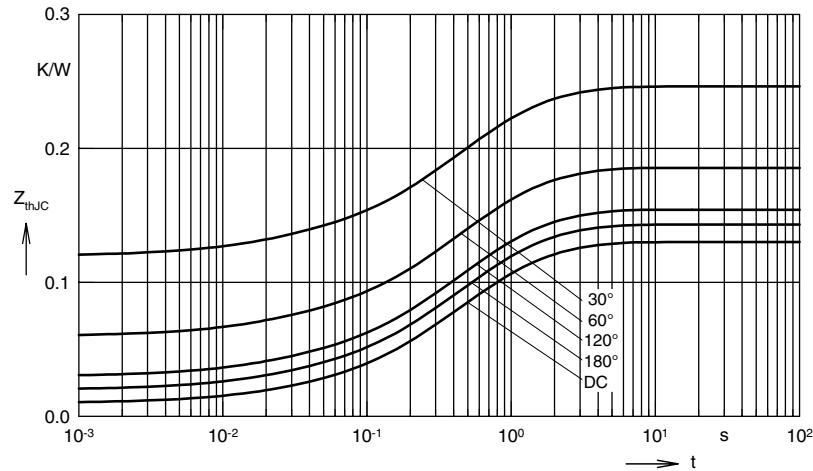


Fig. 8: Power dissipation vs. direct output current and ambient temperature (three phase rectifier bridge)

Fig. 9: Transient thermal impedance junction to case Z_{thJC} at various conduction anglesR_{thJC} for various condition angles:

d	R _{thJC} (K/W)
DC	0.155
180°	0.171
120°	0.184
60°	0.222
30°	0.294

Constants for Z_{thJC} calculation (DC):

i	R _{thi} (K/W)	t _i (s)
1	0.012	0.00014
2	0.008	0.019
3	0.03	0.18
4	0.073	0.52
5	0.032	1.6