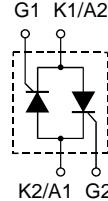


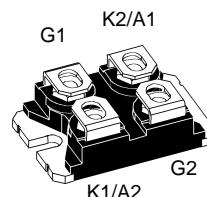
# AC Controller Modules

**I<sub>RMS</sub> = 90 A**  
**V<sub>RRM</sub> = 1200-1600 V**

V <sub>RSM</sub>	V <sub>RRM</sub>	Type
V <sub>DSM</sub>	V <sub>DRM</sub>	
V	V	
1200	1200	MMO 90-12io6
1600	1600	MMO 90-16io6



miniBLOC, SOT-227 B



Symbol	Test Conditions		Maximum Ratings	
I <sub>RMS</sub>	T <sub>C</sub> = 110°C, 50 - 400 Hz, module		90	A
I <sub>TRMS</sub>	T <sub>VJ</sub> = T <sub>VJM</sub>		65	A
I <sub>TAVM</sub>	T <sub>C</sub> = 110°C; (180° sine)		41	A
I <sub>TSM</sub>	T <sub>VJ</sub> = 45°C; V <sub>R</sub> = 0	t = 10 ms (50 Hz), sine t = 8.3 ms (60 Hz), sine	800 860	A A
	T <sub>VJ</sub> = T <sub>VJM</sub> V <sub>R</sub> = 0	t = 10 ms (50 Hz), sine t = 8.3 ms (60 Hz), sine	700 750	A A
I <sup>2</sup> t	T <sub>VJ</sub> = 45°C V <sub>R</sub> = 0	t = 10 ms (50 Hz), sine t = 8.3 ms (60 Hz), sine	3200 3110	A <sup>2</sup> s A <sup>2</sup> s
	T <sub>VJ</sub> = T <sub>VJM</sub> V <sub>R</sub> = 0	t = 10 ms (50 Hz), sine t = 8.3 ms (60 Hz), sine	2450 2360	A <sup>2</sup> s A <sup>2</sup> s
(di/dt) <sub>cr</sub>	T <sub>VJ</sub> = T <sub>VJM</sub> f = 50 Hz, t <sub>p</sub> = 200 µs V <sub>D</sub> = $\frac{2}{3} V_{DRM}$ I <sub>G</sub> = 0.3 A di <sub>G</sub> /dt = 0.3 A/µs	repetitive, I <sub>T</sub> = 150 A	100	A/µs
		non repetitive, I <sub>T</sub> = I <sub>TAVM</sub>	500	A/µs
(dv/dt) <sub>cr</sub>	T <sub>VJ</sub> = T <sub>VJM</sub> ; R <sub>GK</sub> = ∞; method 1 (linear voltage rise)	V <sub>DR</sub> = $\frac{2}{3} V_{DRM}$	1000	V/µs
P <sub>GM</sub>	T <sub>VJ</sub> = T <sub>VJM</sub> I <sub>T</sub> = I <sub>TAVM</sub>	t <sub>p</sub> = 30 µs t <sub>p</sub> = 300 µs	10 5 0.5	W W W
P <sub>GAVM</sub>				
V <sub>RGM</sub>			10	V
T <sub>VJ</sub>			-40...+150	°C
T <sub>VJM</sub>			150	°C
T <sub>stg</sub>			-40...+150	°C
V <sub>ISOL</sub>	50/60 Hz, RMS;	I <sub>ISOL</sub> ≤ 1 mA	2500	V~
M <sub>d</sub>	Mounting torque (M4) Terminal connection torque (M4)		1.1 - 1.5 / 9 - 13 Nm/lb.in. 1.1 - 1.5 / 9 - 13 Nm/lb.in.	
Weight	typ.		30	g

Data according to IEC 60747 and to a single thyristor/diode unless otherwise stated.  
IXYS reserves the right to change limits, test conditions and dimensions.

Symbol	Test Conditions		Characteristic Values		
$I_D$	$T_{VJ} = T_{VJM}$ ; $V_R = V_{RRM}$ ; $V_D = V_{DRM}$		$\leq 20$ mA		
$V_T$	$I_T = 80$ ; $T_{VJ} = 25^\circ\text{C}$		$\leq 1.43$ V		
$V_{TO}$	For power-loss calculations only			0.9	V
$r_T$				5.8	mΩ
$V_{GT}$	$V_D = 6$ V;	$T_{VJ} = 25^\circ\text{C}$	$\leq 1.5$	V	
		$T_{VJ} = -40^\circ\text{C}$	$\leq 1.6$	V	
$I_{GT}$	$V_D = 6$ V;	$T_{VJ} = 25^\circ\text{C}$	$\leq 100$	mA	
		$T_{VJ} = -40^\circ\text{C}$	$\leq 200$	mA	
$V_{GD}$	$T_{VJ} = T_{VJM}$ ;	$V_D = \frac{2}{3} V_{DRM}$	$\leq 0.2$	V	
$I_{GD}$			$\leq 5$	mA	
$I_L$	$T_{VJ} = 25^\circ\text{C}$ ; $t_p = 10$ μs		$\leq 250$	mA	
	$I_G = 0.3$ A; $di_G/dt = 0.3$ A/μs				
$I_H$	$T_{VJ} = 25^\circ\text{C}$ ; $V_D = 6$ V; $R_{GK} = \infty$		$\leq 100$	mA	
$t_{gd}$	$T_{VJ} = 25^\circ\text{C}$ ; $V_D = \frac{1}{2} V_{DRM}$		$\leq 2$	μs	
	$I_G = 0.3$ A; $di_G/dt = 0.3$ A/μs				
$t_q$	$T_{VJ} = T_{VJM}$ ; $I_T = 20$ A, $t_p = 200$ μs; $di/dt = -10$ A/μs		typ.	150	μs
	$V_R = 100$ V; $dv/dt = 15$ V/μs; $V_D = \frac{2}{3} V_{DRM}$				
$R_{thJC}$	per thyristor; DC current		0.6	K/W	
	per module		0.3	K/W	
$R_{thCH}$	per thyristor; DC current		0.1	K/W	
	per module		0.05	K/W	
$d_s$	Creeping distance on surface		8	mm	
$d_a$	Creepage distance in air		4	mm	
$a$	Max. allowable acceleration		50	$\text{m/s}^2$	

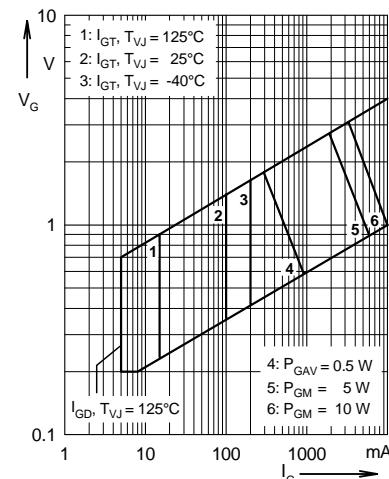


Fig. 1 Gate trigger characteristics

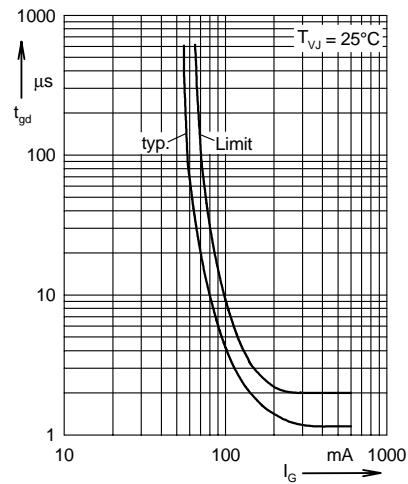
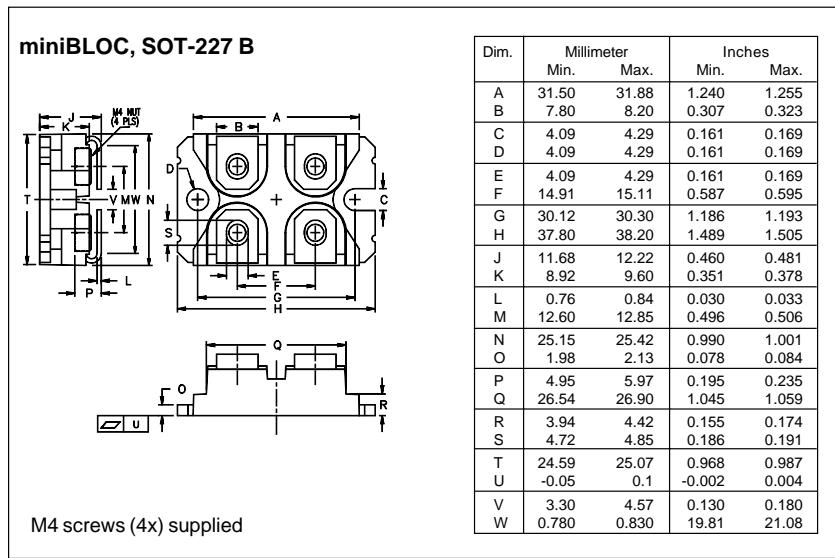


Fig. 2 Gate trigger delay time



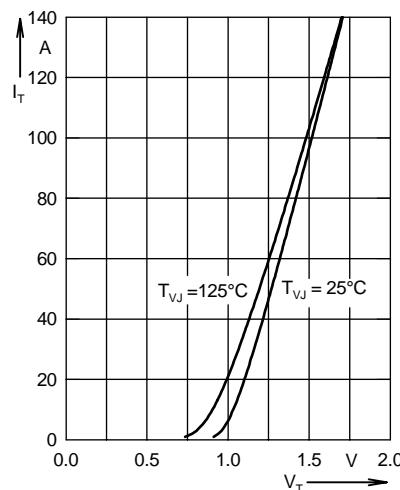


Fig. 3 Forward current versus voltage drop per leg

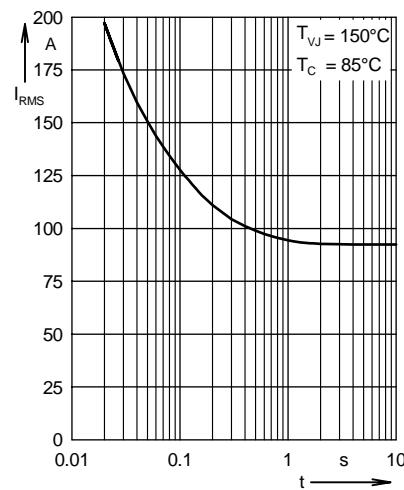


Fig. 4 Rated RMS current versus time (360° conduction)

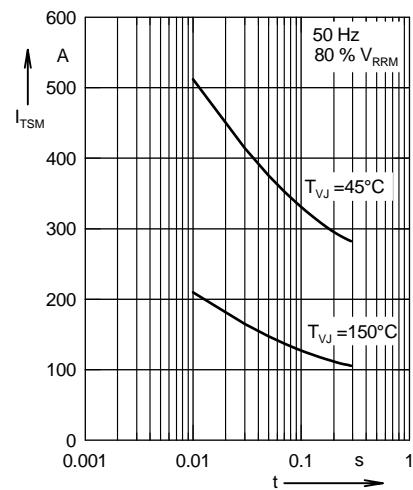


Fig. 5 Surge overload current

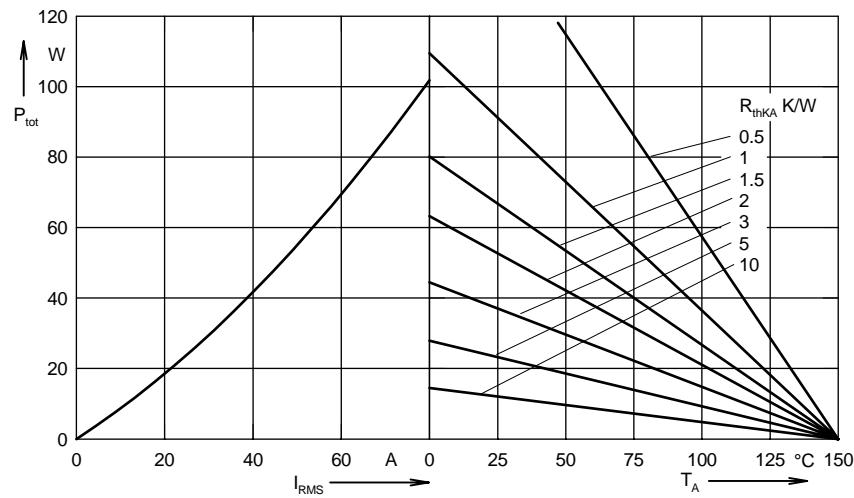


Fig. 6 Load current capability for single AC controller; 1 x MMO 90

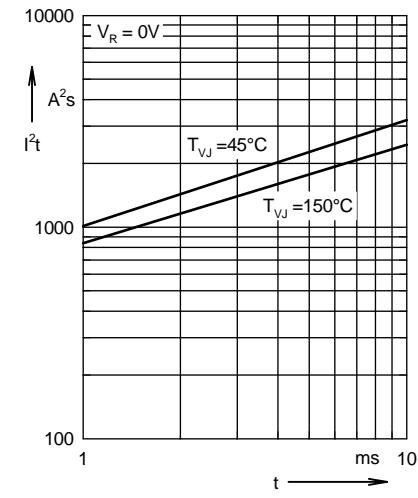


Fig. 7  $I^2t$  versus time (per thyristor)

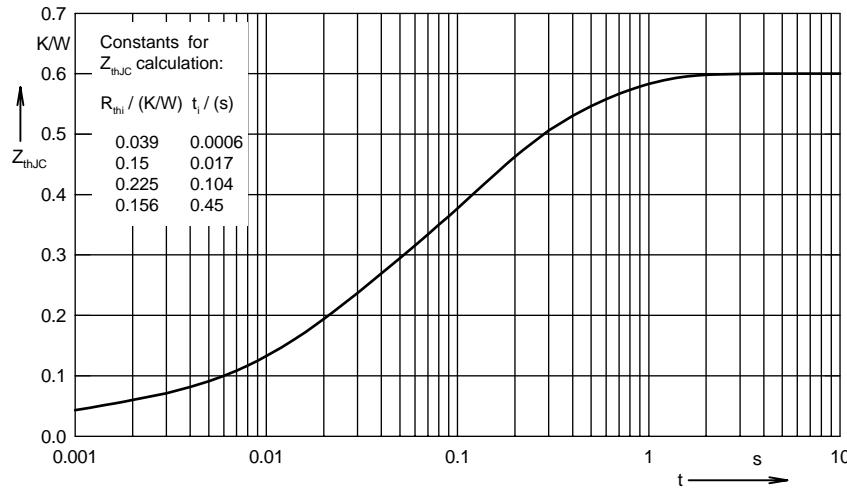


Fig. 8 Transient thermal impedance junction to case (per thyristor)

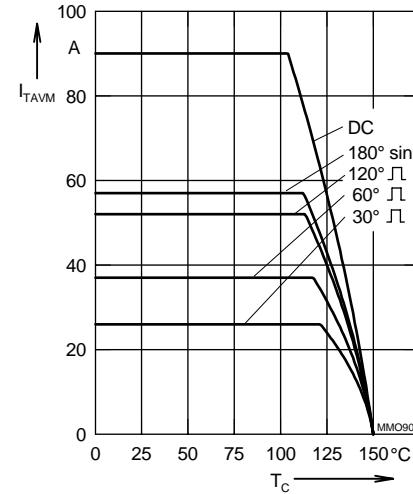


Fig. 9 Maximum forward current at case temperature