

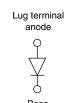
PRODU

 $I_{F(DC)}$ at T_{C}

Vishay High Power Products

HEXFRED® Ultrafast Soft Recovery Diode, 180 A





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Raco	

cathode

200 A at 100 °C

CT SUMMARY	1
I _{F(AV)}	180 A
V_{R}	400 V

FEATURES

- · Very low Q_{rr} and t_{rr}
- · Lead (Pb)-free
- · Designed and qualified for industrial level



BENEFITS

- · Reduced RFI and EMI
- · Reduced snubbing

DESCRIPTION

HEXFRED® diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems. An extensive characterization of the recovery behavior for different values of current, temperature and dl/dt simplifies the calculations of losses in the operating conditions. The softness of the recovery eliminates the need for a snubber in most applications. These devices are ideally suited for power converters, motors drives and other applications where switching losses are significant portion of the total losses.

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Cathode to anode voltage	V_{R}		400	V
Continuous forward current	I _F	T _C = 25 °C	395	
Continuous forward current		T _C = 100 °C	200	А
Single pulse forward current	I _{FSM}	Limited by junction temperature	1200	
Non-repetitive avalanche energy	E _{AS}	$L = 100 \mu H$, duty cycle limited by maximum T_J	1.4	mJ
Maximum power dissipation	P_D	T _C = 25 °C	657	W
Maximum power dissipation		T _C = 100 °C	263	VV
Operating junction and storage temperature range	T _J , T _{Stg}		- 55 to + 150	°C

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V _{BR}	I _R = 100 μA		400	-	-	
	V _{FM}	I _F = 180 A		=	1.08	1.46	V
Maximum forward voltage		I _F = 360 A	See fig. 1	=	1.22	1.8	
		I _F = 180 A, T _J = 125 °C		=	0.99	1.34	
Maximum reverse leakage current	I _{RM}	$T_J = 125 ^{\circ}\text{C}, V_R = 400 ^{\circ}\text{V}$ See fig. 2		-	-	4	mA
Junction capacitance	C _T	V _R = 200 V See fig. 3		=	370	500	pF
Series inductance	L _S	From top of terminal hole to mounting plane		-	6.0	-	nH

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HFA180NH40PbF

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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Reverse recovery time		T _J = 25 °C		-	90	140	20
See fig. 5	t _{rr}	T _J = 125 °C	I _F = 135 A dI _F /dt = 200 A/μs V _B = 200 V	-	280	440	ns
Peak recovery current	I _{RRM}	T _J = 25 °C		-	9	16	A nC
See fig. 6		T _J = 125 °C		-	18	32	
Reverse recovery charge	0	T _J = 25 °C		-	300	950	
See fig. 7	T _J = 125 °C	-11 -22	-	2650	6300	110	
Peak rate of recovery current See fig. 8 dl _{(rec)M} /dt	dl/dt	T _J = 25 °C		-	300	=	A /:
	T _J = 125 °C		-	290	=	A/μs	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL TEST CONDITIONS		VALUES	UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 150	°C	
Maximum thermal resistance, junction to case		R _{thJC}	DC operation See fig. 4	0.19	°C/W	
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.05		
Approximate weight				30	g	
Approximate weight				1.06	OZ.	
Mounting torque	minimum			3 (26.5)		
Mounting torque	maximum			4 (35.4)	N · m	
-	minimum			3.4 (30)	(lbf \cdot in)	
Terminal torque	maximum			5 (44.2)		
Case style			HALF-PAK module	•		





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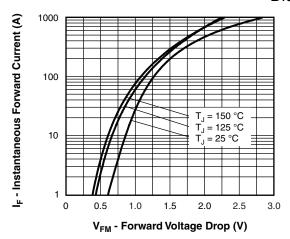


Fig. 1 - Maximum Forward Voltage Drop vs. Instantaneous Forward Current

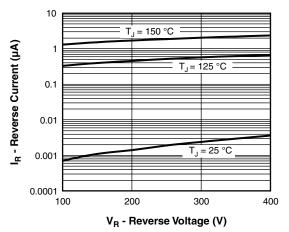


Fig. 2 - Typical Reverse Current vs. Reverse Voltage

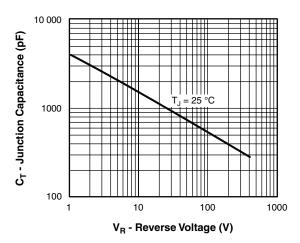


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

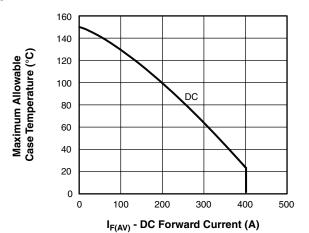


Fig. 4 - Maximum Allowable Case Temperature vs. DC Forward Current

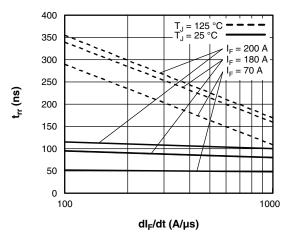


Fig. 5 - Typical Reverse Recovery Time vs. dI_F/dt

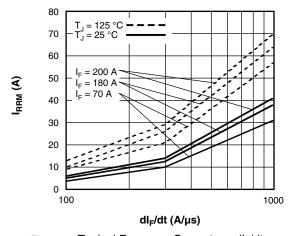


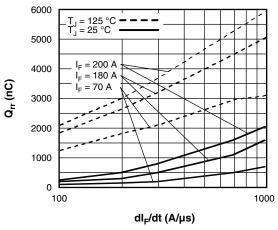
Fig. 6 - - Typical Recovery Current vs. dl_F/dt

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10 000
200 A
180 A
70 A

T_J = 125 °C
T_J = 25 °C

1000

dl_F/dt (A/µs)

Fig. 7 - - Typical Stored Charge vs. dI_F/dt

Fig. 8 - Typical $dl_{(rec)M}/dt$ vs. dl_F/dt

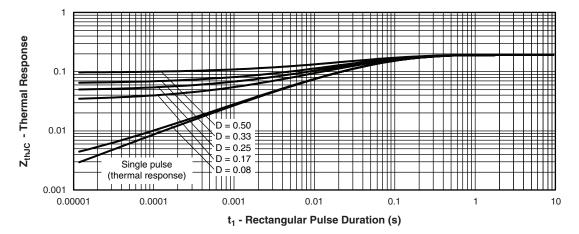


Fig. 9 - Maximum Thermal Impedance Z_{thJC} Characteristics



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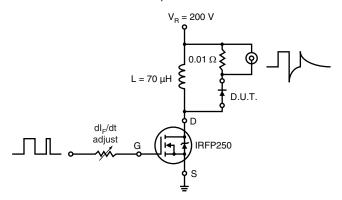
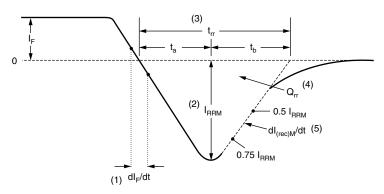


Fig. 10 - Reverse Recovery Parameter Test Circuit



- (1) dI_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $\rm t_{rr}$ reverse recovery time measured from zero crossing point of negative going $\rm I_F$ to point where a line passing through 0.75 $\rm I_{RRM}$ and 0.50 $\rm I_{RRM}$ extrapolated to zero current.
- (4) \mathbf{Q}_{rr} area under curve defined by \mathbf{t}_{rr} and \mathbf{I}_{RRM}

$$Q_{rr} = \frac{t_{rr} x I_{RRM}}{2}$$

(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 11 - Reverse Recovery Waveform and Definitions

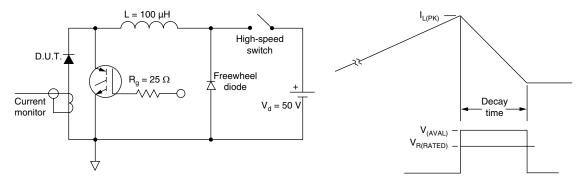


Fig. 12 - Avalanche Test Circuit and Waveforms

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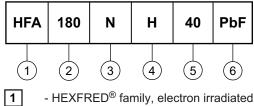
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ORDERING INFORMATION TABLE

Device code



- Average current rating

- N = Not isolated

- H = HALF-PAK

- Voltage rating (400 V)

- Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions	http://www.vishay.com/doc?95020			

Document Number: 94061 Revision: 01-Aug-08

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Document Number: 91000 www.vishay.com
Revision: 11-Mar-11 1