

# STTH200L04TV1

## Ultrafast high voltage rectifier

## **Mian product characteristics**

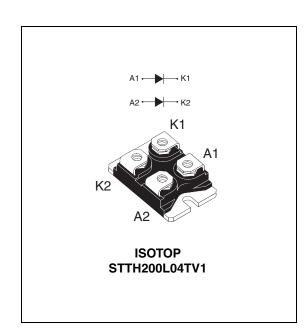
I <sub>F(AV)</sub>	up to 2 x 120 A
V <sub>RRM</sub>	400 V
T <sub>j</sub> (max)	150° C
V <sub>F</sub> (typ)	0.83 V
t <sub>rr</sub> (max)	50 ns

### Features and benefits

- Ultrafast switching
- Low reverse current
- Low thermal resistance
- Reduces switching and conduction losses
- Package insulation voltage: 2500 V<sub>RMS</sub>

## Description

The STTH200L04TV1 uses ST 400 V technology and is specially suited for use in switching power supplies, welding equipment, and industrial applications, as an output rectification diode.



#### Order codes

Part number	Marking
STTH200L04TV1	STTH200L04TV1

#### Table 1. Absolute ratings (limiting values, per diode)

Symbol	Para	Value	Unit		
V <sub>RRM</sub>	Repetitive peak reverse voltage			400	V
I <sub>F(RMS)</sub>	RMS forward current	RMS forward current			А
			Per diode	100	А
<sup>I</sup> F(AV)	Average forward current	$T_c = 73^\circ C \delta = 0.5$	Per diode	120	~
I <sub>FSM</sub>	Surge non repetitive forward current t <sub>p</sub> = 10 ms sinusoidal			900	А
T <sub>stg</sub>	Storage temperature range	-55 to + 150	°C		
Тj	Maximum operating junction temperate	150	°C		

## 1 Characteristics

#### Table 2.Thermal resistance

Symbol	Parameter		Value (max).	Unit
<b>B</b>	Junction to case	Per diode	0.50	
R <sub>th(j-c)</sub> Junction to case		Total	0.30	°C/W
R <sub>th(c)</sub>	Coupling		0.10	

When diodes 1 and 2 are used simultaneously:

 $\Delta$  Tj(diode 1) = P(diode 1) x R<sub>th(j-c)</sub>(Per diode) + P(diode 2) x R<sub>th(c)</sub>

#### Table 3. Static electrical characteristics (per diode)

Symbol	Parameter	Test conditions		Min.	Тур	Max.	Unit
I <sub>B</sub> <sup>(1)</sup>	Reverse leakage	$T_j = 25^\circ C$	V <sub>R</sub> = V <sub>RRM</sub>			100	μA
'R`´	current $T_j = 125^{\circ} C$	VR − VRRM		100	1000	μΑ	
V <sub>F</sub> <sup>(2)</sup>	Forward voltage drop	$T_j = 25^\circ C$	l <sub>E</sub> = 100 A			1.2	V
YF`	Torward voltage drop	$T_j = 150^\circ C$	1F - 100 A		0.83	1.0	v

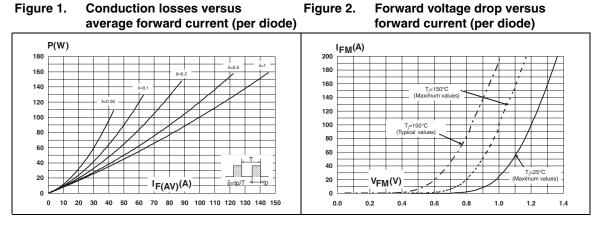
1. Pulse test:  $t_p = 5 \text{ ms}, \delta < 2\%$ 

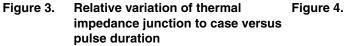
2. Pulse test:  $t_p = 380 \ \mu s, \ \delta < 2\%$ 

To evaluate the conduction losses use the following equation: P = 0.8 x  $I_{F(AV)}$  + 0.0033  $I_{F^2(RMS)}$ 

Symbol	Parameter	Test conditions			Тур	Max	Unit
+	Reverse recovery	T <sub>i</sub> = 25° C	$I_F = 1 A dI_F/dt = 50 A/\mu s$ $V_R = 30 V$		75	100	ns
ume ,	$I_F = 1 A dI_F/dt = 200 A/\mu s$ $V_R = 30 V$		45	60	115		
I <sub>RM</sub>	ourront		I <sub>F</sub> = 100 A V <sub>R</sub> = 200 V dI <sub>F</sub> /dt = 100 A/μs			18	A
S <sub>factor</sub>	Softness factor	$T_j = 125^\circ C$	I <sub>F</sub> = 100 A V <sub>R</sub> = 200 V dI <sub>F</sub> /dt = 100 A/μs		0.4		
t <sub>fr</sub>	Forward recovery time	$T_j = 25^\circ C$	$I_{F} = 100 \text{ A} \qquad dI_{F}/dt = 200 \text{ A}/\mu\text{s}$ $V_{FR} = 1.1 \text{ x } V_{Fmax}$			800	ns
V <sub>FP</sub>	Forward recovery voltage	$T_j = 25^\circ C$	$I_{F} = 100 \text{ A}  dI_{F}/dt = 200 \text{ A}/\mu\text{s}$ $V_{FR} = 1.1 \text{ x} \text{ V}_{Fmax}$		2.6		۷

 Table 4.
 Dynamic characteristics (per diode)





Peak reverse recovery current versus dl<sub>⊦</sub>/dt (typical values, per diode)

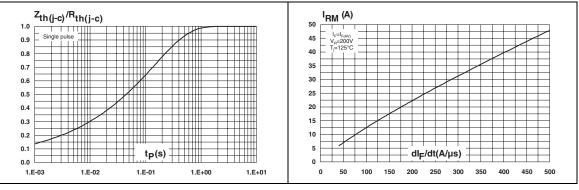
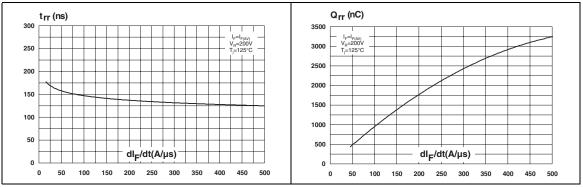


Figure 5. Reverse recovery time versus dl<sub>F</sub>/dt (typical values, per diode)

Figure 6. Reverse recovery charges versus dl<sub>F</sub>/dt (typical values, per diode)



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# Figure 7. Reverse recovery softness factor versus dl<sub>F</sub>/dt (typical values, per diode)

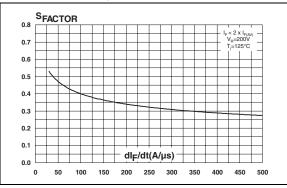
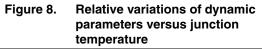


Figure 9. Transient peak forward voltage versus dl<sub>F</sub>/dt (typical values, per diode)



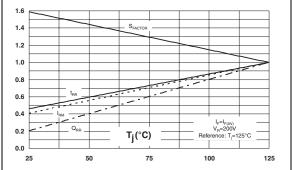


Figure 10. Forward recovery time versus dl<sub>F</sub>/dt (typical values, per diode)

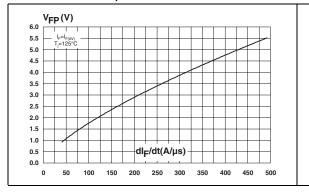
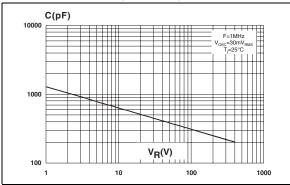
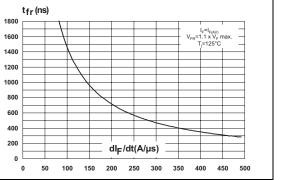


Figure 11. Junction capacitance versus reverse voltage applied (typical values, per diode)



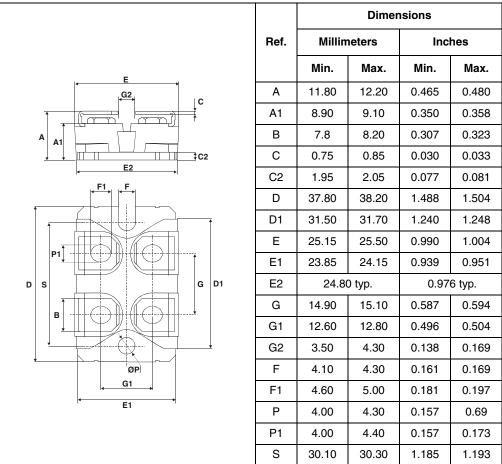
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## 2 Package information

- Epoxy meets UL94, V0
- Cooling method: by conduction (C)

Table 5. ISOTOP Dimensions



In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.



## **3** Ordering information

Ordering type	Marking	Package	Weight	Base qty	Delivery mode
STTH200L04TV1	STTH200L04TV1	ISOTOP	27 g (without screws)	10 (with screws)	Tube

## 4 Revision history

Date	Revision	Description of Changes
11-Aug-2006	1	First issue

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