

### NEC's 3.0 GHz DIVIDE BY 64/128/256 PRESCALER

**UPB1506GV** UPB1507GV

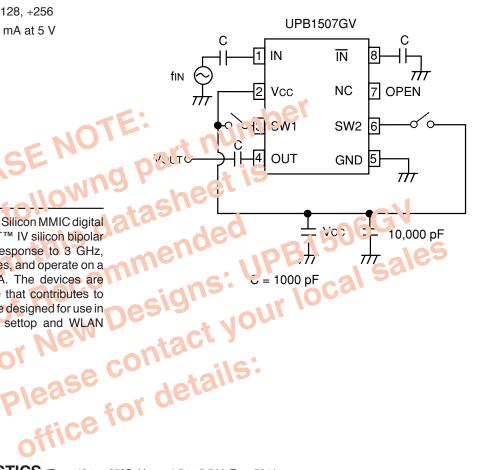
#### **FEATURES**

- **HIGH FREQUENCY OPERATION TO 3 GHz**
- SELECTABLE DIVIDE RATIO: ÷64, ÷128, ÷256
- LOW CURRENT CONSUMPTION: 19 mA at 5 V
- SMALL PACKAGE: 8 pin SSOP
- AVAILABLE IN TAPE AND REEL

#### DESCRIPTION

NEC's UPB1506GV and UPB1507GV are Silicon MMIC digital prescalers manufactured with the NESAT™ IV silicon bipolar process. They feature high frequency response to 3 GHz, selectable divide-by-64, 128, or 256 modes, and operate on a 5 volt supply while drawing only 19 mA. The devices are housed in a small 8 pin SSOP package that contributes to system miniaturization. These devices are designed for use in a PLL synthesizer for DBS and CATV settop and WLAN applications.

#### **TEST CIRCUIT**



#### ELECTRICAL CHARACTERISTICS (TA = -40 to +85°C, Vcc = 4.5 to 5.5 V, Zs = 50Ω)

EASE NO

PART NUMBER PACKAGE OUTLINE			UPB	1506GV,UPB1507 S08	7GV
SYMBOLS	PARAMETERS AND CONDITIONS	UNITS	MIN	TYP	MAX
Icc	Circuit Current	mA	12.5	19	26.5
fIN(U)	Upper Limit Operating Frequency, PIN = -15 to +6 dBm	GHz		3.0	
fIN(L)1	Lower Limit Operating Frequency, PIN = -10 to +6 dBm	GHz			0.5
fIN(L)2	Lower Limit Operating Frequency, PIN = -15 to +6 dBm	GHz			1.0
PIN1	Input Power, fin = 1.0 to 3.0 GHz	dBm	-15		+6
PIN2	Input Power, fin = 0.5 to 1.0 GHz	dBm	-10		+6
Vouт	Output Voltage, C∟ = 0.8 pF	V <sub>P-P</sub>	1.2	1.6	
VIN(H)	Division Ratio Control Input High	V		Vcc	
VIN(L)	Division Ratio Control Input Low	V		OPEN or GND	

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### ABSOLUTE MAXIMUM RATINGS<sup>1</sup> (TA = $25^{\circ}$ C)

SYMBOLS	PARAMETERS	UNITS	RATINGS
Vcc	Supply Voltage	V	-0.5 to 6.0
VIN	Input Voltage	V	-0.5 to Vcc + 0.5
Pin	Input Power	dBm	+10
PD	Power Dissipation <sup>2</sup>	mW	250
Тор	Operating Temperature	°C	-45 to +85
Тѕтс	Storage Temperature	°C	-55 to +150

#### Notes

- Operation in excess of any one of these parameters may result in permanent damage.
- 2. Mounted on a double-sided copper clad 50x50x1.6 mm epoxy glass PWB (TA =  $+85^{\circ}$ C).

# RECOMMENDED OPERATING CONDITIONS

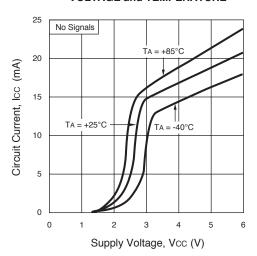
SYMBOL	PARAMETER	UNITS	MIN	TYP	MAX
Vcc	Supply Voltage	V	4.5	5.0	5.5
Тор	Operating Temperature	°C	-40	+25	+85

#### **PIN DESCRIPTIONS**

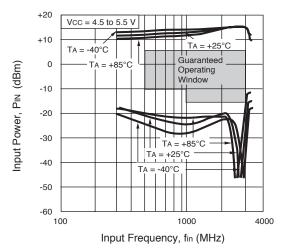
Pir	n No.	Pin Name	Applied Voltage	Pin Voltage			Des	cription	
UPB1506GV	UPB1507GV	Name	(V)	(V)					
2	1	IN	_	2.9	Signal input pin. This pin should be coupled to the source with a capacitor (eg 1000 pF).				
3	8	ĪN	_	2.9			in. This pin m -) to ground.	ust be equippe	ed with a bypass
4	5	GND	0	_			pattern on the ground impeda		be formed as wide as
1	3	SW1	H/L	_	Divided ratio input pin. The ratio can be controlled by the following input data to these pins.				
							S	SW2	
			<u> </u>				Н	L	
						Н	÷64	÷128	
6	6	SW2			SW1	L	÷128	÷256	
					These pins	s should be	equipped with	h a bypass ca	pacitor (e.g. 1000 pF) to
8	2	Vcc	4.5 to 5.5	_	Power supply pin. This pin must be equipped with bypass capacitor (eg 1000 pF) to ground.				
7	4	OUT	_	2.6 to 4.7	Divided frequency output pin. This pin is designed as an emitter follower output. This pin can be connected to CMOS input due to 1.2 Vp-p MIN output.				
5	7	NC	_	_	No connec	tion. This	pin must be o	pened.	

#### TYPICAL PERFORMANCE CURVES (TA = +25°C unless otherwise noted)

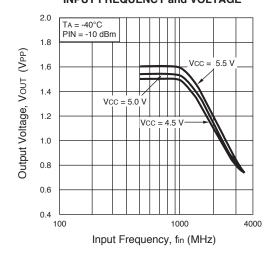
### CURRENT vs. VOLTAGE and TEMPERATURE



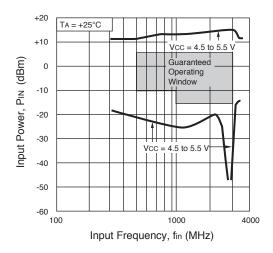
### INPUT POWER vs. INPUT FREQUENCY and TEMPERATURE



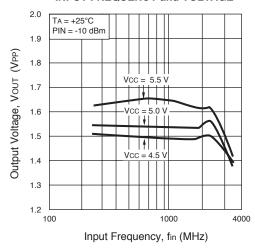
# OUTPUT VOLTAGE vs. INPUT FREQUENCY and VOLTAGE



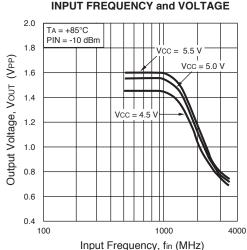
### INPUT POWER vs. INPUT FREQUENCY and VOLTAGE



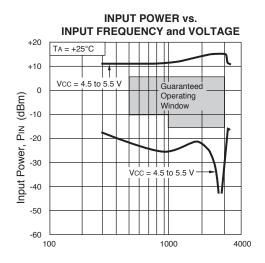
# OUTPUT VOLTAGE vs. INPUT FREQUENCY and VOLTAGE



### OUTPUT VOLTAGE vs.

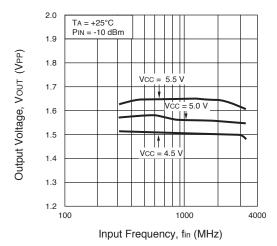


#### TYPICAL PERFORMANCE CURVES (TA = +25°C unless otherwise noted)

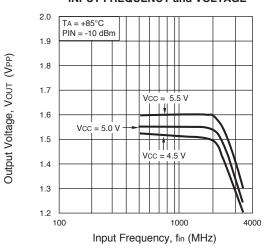


Input Frequency, fin (MHz)

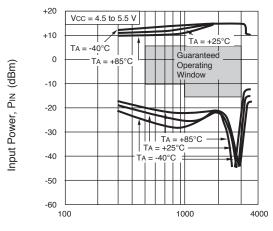
# OUTPUT VOLTAGE vs. INPUT FREQUENCY and VOLTAGE



# OUTPUT VOLTAGE vs. INPUT FREQUENCY and VOLTAGE

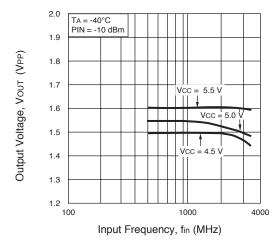


# INPUT POWER vs. INPUT FREQUENCY and TEMPERATURE

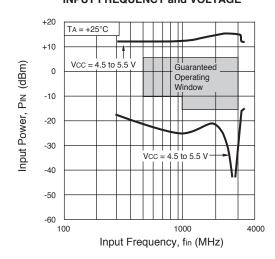


Input Frequency, fin (MHz)

# OUTPUT VOLTAGE vs. INPUT FREQUENCY and VOLTAGE

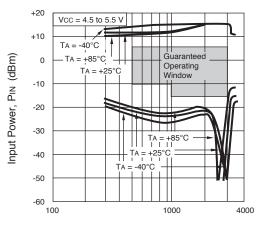


# INPUT POWER vs. INPUT FREQUENCY and VOLTAGE



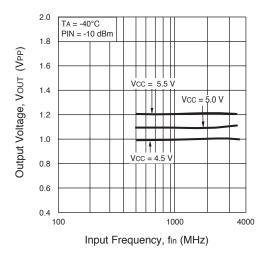
### TYPICAL PERFORMANCE CURVES (TA = +25°C unless otherwise noted)

# INPUT POWER vs. INPUT FREQUENCY and VOLTAGE

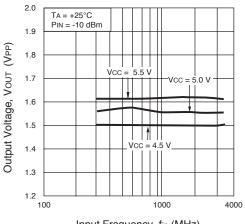


Input Frequency, fin (MHz)

# OUTPUT VOLTAGE vs. INPUT FREQUENCY and VOLTAGE

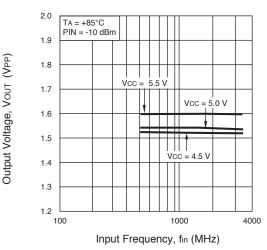


# OUTPUT VOLTAGE vs. INPUT FREQUENCY and VOLTAGE

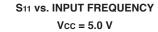


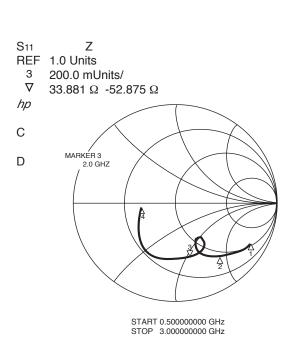
Input Frequency, fin (MHz)

# OUTPUT VOLTAGE vs. INPUT FREQUENCY and VOLTAGE



#### **UPB1506GV**





GHz MAG **ANG** 0.5 0.868 -26.6 0.6 0.828 -32.6 0.7 0.794 -37.4 8.0 0.761 -41.9 0.9 0.721 -46.5 0.706 -49.3 1.0 1.1 0.662 -54.0 0.629 -57.2 1.2 1.3 0.595 60.2 1.4 0.554 -62.9 1.5 0.516 -64.8 1.6 0.440 -61.9 1.7 0.428 51.0 0.543 -61.5 1.8 1.9 0.555 -68.4 2.0 0.560 -74.7 2.1 0.558 - 79.5 2.2 0.564 -84.9 2.3 0.570 -90.9 -98.3 2.4 0.574 2.5 0.574 -107.9 2.6 - 118.3 0.564 2.7 -131.4 0.530 2.8 0.476 - 144.6 - 159.1 0.411 2.9 3.0 0.331 - 175.8

**S**11

**FREQUENCY** 

: 0.5 GHz

: 1.0 GHz

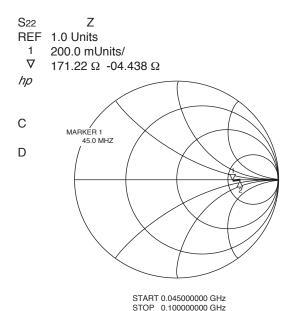
: 2.0 GHz

: 3.0 GHz

 $\Delta_2$   $\Delta_3$ 

#### **UPB1506GV**

#### S22 vs. OUTPUT FREQUENCY Divide by 64 mode, Vcc = 5.0 V



MHz MAG ANG 45.0 0.542 -1.4 50.0 0.602 -0.3 55.0 0.616 0.0 60.0 0.605 1.1 65.0 0.609 0.7 70.0 0.616 0.3 0.620 75.0 0.1 80.0 0.622 0.0 85.0 0.619 0.6 90.0 0.610 0.9 0.626 -0.7

0.623

-1.7

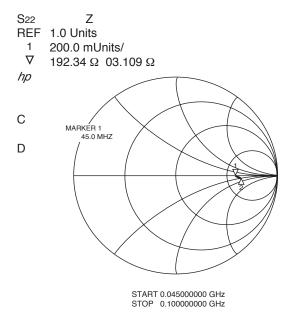
**S**22

95.0 100.0

↓ : 45 MHz △ : 100 MHz **FREQUENCY** 

#### **UPB1506GV**

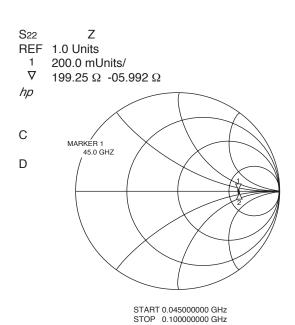
#### S<sub>22</sub> vs. OUTPUT FREQUENCY Divide by 128 mode, Vcc = 5.0 V



FREQUENCY	S	22
MHz	MAG	ANG
45.0	0.590	-0.4
50.0	0.604	-1.0
55.0	0.610	-1.1
60.0	0.607	-0.8
65.0	0.548	-5.9
70.0	0.630	-0.0
75.0	0.615	-1.0
80.0	0.618	-1.4
85.0	0.617	-1.2
90.0	0.616	-2.2
95.0	0.623	-2.4
100.0	0.624	-2.3

#### **UPB1506GV**

#### S<sub>22</sub> vs. OUTPUT FREQUENCY Divide by 256 mode, Vcc = 5.0 V

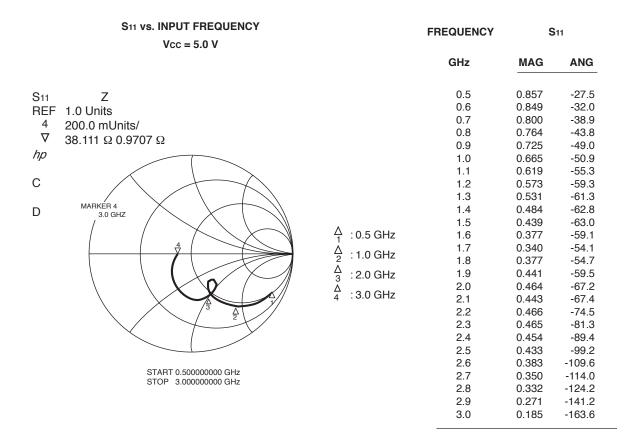


FREQUENCY	<b>S</b> 22	
MHz	MAG	ANG
45.0	0.601	-0.9
50.0	0.609	-1.6
55.0	0.611	-1.5
60.0	0.620	-1.4
65.0	0.607	-2.1
70.0	0.615	-1.9
75.0	0.613	-3.2
80.0	0.611	-2.8
85.0	0.607	-2.5
90.0	0.605	-2.4
95.0	0.610	-3.0
100.0	0.608	-2.8

 $\frac{\Delta}{1}$  : 45 MHz  $\frac{\Delta}{2}$  : 100 MHz

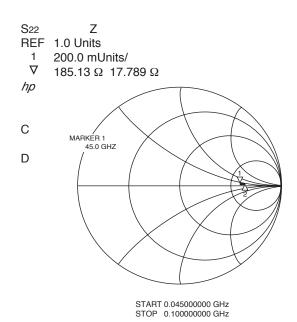
 $\frac{\Delta}{1}$  : 45 MHz  $\frac{\Delta}{2}$  : 100 MHz

#### **UPB1507GV**



#### **UPB1507GV**

#### S22 vs. OUTPUT FREQUENCY Divide by 64 mode, Vcc = 5.0 V



50.0	0.572	2.5
55.0	0.574	3.0
60.0	0.574	2.7
65.0	0.584	3.0
70.0	0.587	2.6
75.0	0.592	2.4
80.0	0.587	2.6
85.0	0.589	2.9
90.0	0.591	2.9
95.0	0.573	1.7
100.0	0.604	2.9

MHz MAG **ANG** 45.0 0.580 3.4

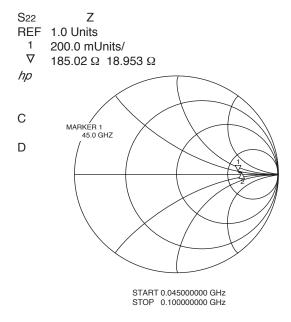
S22

**FREQUENCY** 

: 45 MHz : 100 MHz

#### **UPB1507GV**

S<sub>22</sub> vs. OUTPUT FREQUENCY Divide by 128 mode, Vcc = 5.0 V



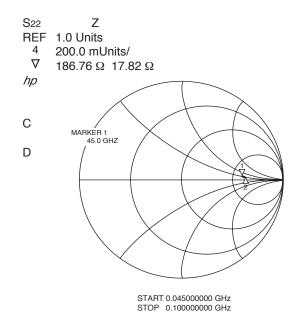
MHz	MAG	ANG
45.0	0.578	3.2
50.0	0.571	2.8
55.0	0.572	3.3
60.0	0.576	3.0
65.0	0.584	3.1
70.0	0.587	2.8
75.0	0.589	2.4
80.0	0.589	2.8
85.0	0.588	3.0
90.0	0.593	2.8
95.0	0.598	3.0
100.0	0.602	2.9

**S**22

**FREQUENCY** 

#### **UPB1507GV**

S<sub>22</sub> vs. OUTPUT FREQUENCY Divide by 256 mode, Vcc = 5.0 V



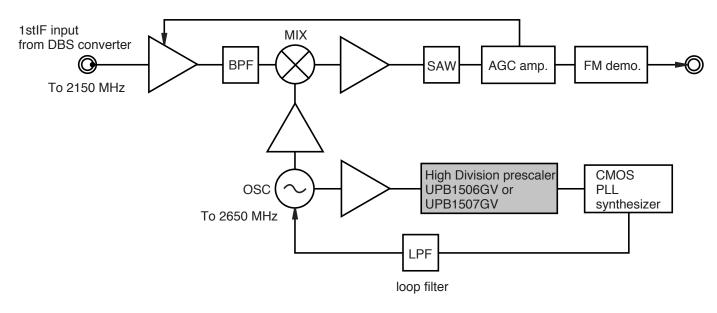
 $\frac{\Delta}{1}$ : 45 MHz  $\frac{\Delta}{2}$ : 100 MHz

 $\stackrel{\triangle}{1}$ : 45 MHz  $\stackrel{\triangle}{2}$ : 100 MHz

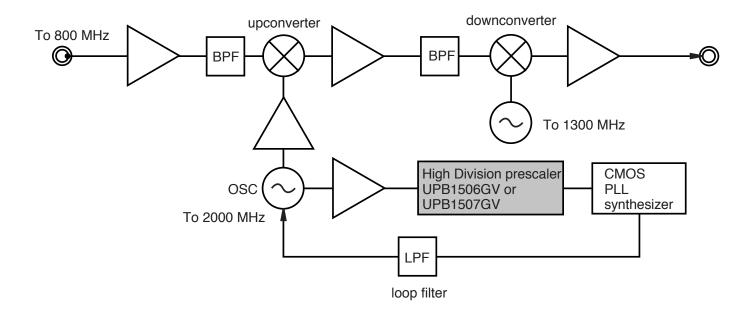
FREQUENCY	S	22
MHz	MAG	ANG
45.0	0.580	3.0
50.0	0.572	2.8
55.0	0.571	2.9
60.0	0.576	2.9
65.0	0.585	3.2
70.0	0.590	2.8
75.0	0.589	2.5
80.0	0.590	2.6
85.0	0.588	2.9
90.0	0.597	2.9
95.0	0.600	3.1
100.0	0.601	3.1

#### SYSTEM APPLICATION EXAMPLE

### RF unit block of Analog DBS tuners

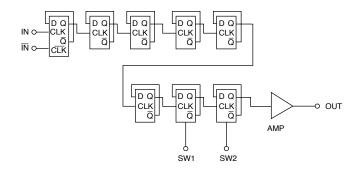


### RF unit block of Analog CATV converter

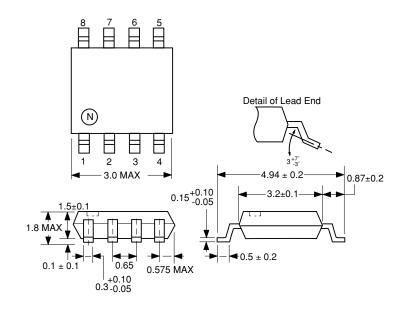


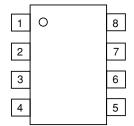
#### **INTERNAL BLOCK DIAGRAM**

### **OUTLINE DIMENSIONS** (Units in mm)



#### **PACKAGE OUTLINE S08**





#### PIN CONNECTIONS

Pin No.	UPB1506GV	UPB1507GV
1	SW1	IN
2	IN	Vcc
3	ĪN	SW1
4	GND	OUT
5	OPEN	GND
6	SW2	SW2
7	OUT	OPEN
8	Vcc	IN

#### ORDERING INFORMATION

PART NUMBER	QUANTITY	MARKING
UPB1506GV-E1	1000/Reel	1506
UPB1507GV-E1-A	1000/Reel	1507

#### Note:

Embossed tape 8 mm wide.
 Pin 1 is in the tape pull-out direction.

#### Life Support Applications

These NEC products are not intended for use in life support devices, appliances, or systems where the malfunction of these products can reasonably be expected to result in personal injury. The customers of CEL using or selling these products for use in such applications do so at their own risk and agree to fully indemnify CEL for all damages resulting from such improper use or sale.

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7/22/2004





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Facsimile: (408) 988-0279

Subject: Compliance with EU Directives

CEL certifies, to its knowledge, that semiconductor and laser products detailed below are compliant with the requirements of European Union (EU) Directive 2002/95/EC Restriction on Use of Hazardous Substances in electrical and electronic equipment (RoHS) and the requirements of EU Directive 2003/11/EC Restriction on Penta and Octa BDE.

CEL Pb-free products have the same base part number with a suffix added. The suffix –A indicates that the device is Pb-free. The –AZ suffix is used to designate devices containing Pb which are exempted from the requirement of RoHS directive (\*). In all cases the devices have Pb-free terminals. All devices with these suffixes meet the requirements of the RoHS directive.

This status is based on CEL's understanding of the EU Directives and knowledge of the materials that go into its products as of the date of disclosure of this information.

Restricted Substance per RoHS	Concentration Limit per RoHS (values are not yet fixed)	Concentration contained in CEL devices	
Lead (Pb)	< 1000 PPM	-A -AZ Not Detected (*)	
Mercury	< 1000 PPM	Not Detected	
Cadmium	< 100 PPM	Not Detected	
Hexavalent Chromium	< 1000 PPM	Not Detected	
PBB	< 1000 PPM	Not Detected	
PBDE	< 1000 PPM	Not Detected	

If you should have any additional questions regarding our devices and compliance to environmental standards, please do not hesitate to contact your local representative.

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