MODELS 694, 698, 699

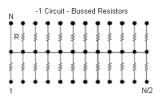


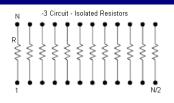
Isolated, bussed and other circuits Thin film resistor network 0.300" PDIP packages RoHS compliant available

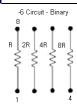
	Δ		- 1	г.

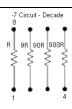
Precision Nichrome Resistors on Ceramic	Passivation coating provides protection in humid environments Excellent frequency response Excellent long term resistance stability	
Industry Standard Packaging	JEDEC 95, MS-001 (Plastic DIP 0.300 inch wide in 8, 14 and 16 lead pin counts)	
Ratio Tolerances	< ± 0.05%	
TCR Tracking Tolerances	< ± 5 ppm/°C	

SCHEMATICS









ELECTRICAL¹

Standard Resistance Range ²	1K ohms to 100K ohms (Isolated) 1K ohms to 45K ohms (Bussed)
TCR ³	± 25 ppm/℃
TCR Tracking ³	± 5 ppm/℃
Operating Temperature Range	-55°C to +125°C
Interlead Capacitance	< 2pF
Insulation Resistance	≥ 10,000 Megohms
Maximum Operating Voltage	100 Vdc or √ PR
Noise, Maximum (MIL-STD-202, Method 308)	-40 dB
Resistor Power Rating at 70 ℃	0.1 Watts

BI Technologies Corporation

4200 Bonita Place Fullerton, CA 92835 USA

Website: www.bitechnologies.com

May 29, 2008



¹ Specifications subject to change without notice.

² E96 codes available.

³ Standard limits for all resistance codes.

PACKAGE POWER AND DERATING CURVE

Model	Package Power @ 70°C(watts) 4	100
694	0.4	Percent of 60 - Rated Power 40 -
698	0.6	20 -
699	0.6	-0 70 125 150 Degrees C

-1 4 7 4 V	I - Y -	***	 4 7 1 1	L-R-8:	
		1 1 1 1 1 1		1 - 0 . 0 - 7	52111

Thermal Shock plus Power Conditioning	ΔR 0.25%
Short Time Overload	ΔR 0.1%
Terminal Strength	ΔR 0.1%
Moisture Resistance	ΔR 0.2%
Mechanical Shock	ΔR 0.25%
Vibration	ΔR 0.25%
Low Temperature Operation	ΔR 0.1%
High Temperature Exposure	ΔR 0.1%
Load Life, 1,000 Hours	ΔR 0.1%
Resistance to Solder Heat	ΔR 0.1%
Dielectric Withstanding Voltage	200V for 1 minute
Marking Permanency	MIL-STD-202, Method 215
Lead Solderability	MIL-STD-202, Method 208
Flammability	UL-94V-0 Rated
Storage Temperature Range	-65°C to +125°C

MECHANICAL

Lead Plating	80/20 Tin Lead (Standard) 100 matte Tin (RoHS)
Lead Material	Copper Alloy
Lead Configuration	Thru hole
Substrate Material	Alumina
Resistor Material	Passivated Nichrome
Body Material	Molded Epoxy

⁴ Maximum power per resistor @ 70 ℃ is 100 mW, not to exceed package power



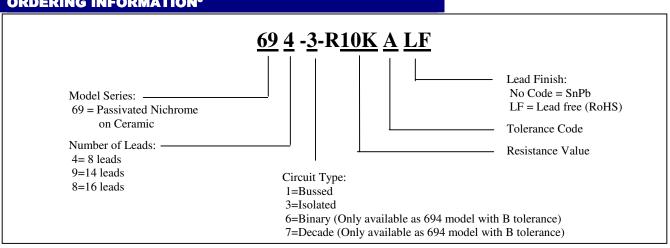
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ORDERING INFORMATION⁵



RESISTANCE VALUE⁵

Standard values follow E96 table. Character "K" denotes a multiplier of 1000.

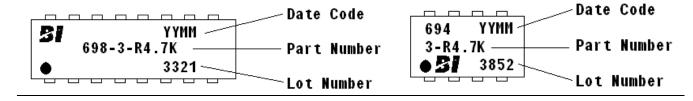
RESISTANCE TOLERANCE CODE

Accuracy Code at 25 ℃	Α	В	D	F
Absolute Resistance Tolerances (%)	±0.1	±0.1	±0.5	±1.0
Ratio Tolerances (R1 Ref) (%)	±0.05	±0.1	±0.1	±1.0

PACKAGING OPTIONS (UNIT COUNT/TUBE)

Model + Pin count		
694	100	
_699	50	
698	50	

YPICAL MARKING



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Consult customer service for custom designs and features.