

PART NUMBER

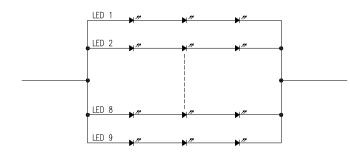
LCT-H800480M70W1T

REV.

#### BACKLIGHT SPECIFICATIONS

ITEM	SYMBOL	STA	NDARD VA	ALUE	UNIT	REMARK	
IIEM	SIMBOL	MIN	TYP.	MAX	UNII		
LED VOLTAGE	VL	-	9.6	12	V	NOTE 2	
LED CURRENT	IL	-	180	-	mA	NOTE 2	
LED LIFE TIME	-	20,000	1	-	Hr	NOTE 1	

NOTE (1): THE "LED LIFE TIME" IS DEFINED AS THE MODULE BRIGHTNESS DECREASE TO 50% ORIGINAL BRIGHTNESS THAT THE AMBIENT TEMPERATURE IS 25°C AND IL-20mA. NOTE (2): THE LED DRIVING CONDITION IS DEFINED FOR EACH LED MODULE. (3 LED



ITEM	CONTENTS	UNIT
DISPLAY TECHNOLOGY	7.0" (DIAGONAL) TFT NORMALLY WHITE TRANSMISSIVE LCD	
MODULE OUTER DIMENSION	165.1(H) x 104.2 (V) x 6.0(D)(MAX)	mm
PIXEL SIZE	120(H) x 380(V)	μm
ACTIVE DISPLAY AREA	152.40(H) x 91.44(V)	mm
NUMBER OF DOTS	800RGBx480	DOTS
VIEWING DIRECTION	12	DCLOCK
PIXEL ARRANGEMENT	RGB STRIPE	
PIXEL PITCH	0.0635 (H) x 0.1905 (V)	mm
BACKLIGHT	LED BACK-LIGHT/WHITE	
PIXEL DRIVING ELEMENT	a-Si TFT	-
MPU INTERFACE	TTL DIGITAL	-
DRIVE IC	HX8232-A02(SOCURE) X3 + HX8643-A00(GATE) X1	- I -
T-CON IC	NT39703-5	
NUMBER OF COLORS	262K COLORS	-
OPERATING TEMPERATURE	-20~70	<u>۳</u>
STORAGE TEMPERATURE	-30~80	۳
WEIGHT	TBD	g

## / TOUCH PANEL SPECIFICATIONS \

/						
ITFM	DESCRIPTION	STANDARD VALUE			CONDITIONS	
IIIEM	DESCRIPTION	MIN	TYP.	MAX	CONDITIONS	
TOTAL TRANSMITTANCE	-	80%	-	-	JIS K7105	
OPERATING FORCE	PEN INPUT	80g	-	100g	DIAMETER 3.0mm,SR 0.8mm	
OPERATING FORCE	FINGER INPUT	80g	-	100g	DIAMETER 12.0mm,SR 12.5mm	
LOOP RESISTANCE	X AXIS	200	-	900	_	
LOUP RESISTANCE	Y AXIS	400	-	900	_	
INSULATION RESISTANCE		20	-	-	At DC 25V	
KNOCKING LIFE		1000000	_	_	250g, 2HZ SHAPE OF PEN END R 0.8mm	
CHATTERING		-	-	10ms	_	



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REV. PART NUMBER LCT-H800480M70W1T

7.0" ACTIVE MATRIX FULL COLOR TFT PANEL 12:00 VIEW, LED BACKLIGHT, W/ TOUCH PANEL, -20°C TO +70°C OPERATING TEMP.

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INTE	RFACE PIN CO	ONNECTION
PIN	NAME	DESCRIPTION
1	GND	POWER GROUND
2	GND	POWER GROUND
3	NC	NO CONNECTION
4	VCC	POWER SUPPLY FOR DIGITAL CIRCUIT
5	VCC	POWER SUPPLY FOR DIGITAL CIRCUIT
6	VCC	POWER SUPPLY FOR DIGITAL CIRCUIT
7	VCC	POWER SUPPLY FOR DIGITAL CIRCUIT
8	NC	NO CONNECTION
9	DE	DATA ENABLE
10	GND	POWER GROUND
11	GND	POWER GROUND
12	GND	POWER GROUND
13	B5	BLUE DATA INPUT (MSB)
14	B4	BLUE DATA INPUT
15	B3	BLUE DATA INPUT
16	GND	POWER GROUND
17	B2	BLUE DATA INPUT
18	B1	BLUE DATA INPUT
19	B0	BLUE DATA INPUT (LSB)
20	GND	POWER GROUND
21	G5	GREEN DATA INPUT (MSB)
22	G4	GREEN DATA INPUT
23	G3	GREEN DATA INPUT
24	GND	POWER GROUND
25	G2	GREEN DATA INPUT
26	G1	GREEN DATA INPUT
27	G0	GREEN DATA INPUT (LSB)
28	GND	POWER GROUND
29	R5	RED DATA INPUT (MSB)
30	R4	RED DATA INPUT
31	R3	RED DATA INPUT
32	GND	POWER GROUND
33	R2	RED DATA INPUT
34	R1	RED DATA INPUT
35	R0	RED DATA INPUT (LSB)
36	GND	POWER GROUND
37	GND	POWER GROUND
38	DCLK	SAMPLE CLOCK
39	GND	POWER GROUND
40	GND	POWER GROUNG

BACK	KLIGHT UNIT		
PIN	NAME	DESCRIPTION	REMARK
1	LED+	HIGH VOLTAGE	WHITE LINE
2	LED-	LOW VOLTAGE	GRAY LINE
			0.000

/1	TOUCH PANEL PIN CONNECTION								
PIN	SYMBOL	FUNCTION							
1	XR	TOUCH PANEL OUTPUT RIGHT							
2	YD	TOUCH PANEL OUTPUT DOWN							
3	XL	TOUCH PANEL OUTPUT LEFT							
4	YU	TOUCH PANEL OUTPUT UPPER							

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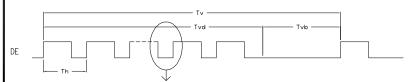
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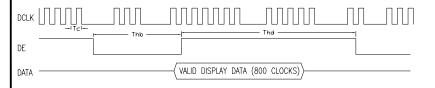
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INTERFACE TIMING	\	\						
SIGNAL	ITEM	SYMBOL	STA	NDARD V	ALUE	UNIT	NOTE	
SIGNAL	IILM	STWBOL	MIN	TYP.	MAX	UNII	NOIL	
	FREQUENCY	FC	-	33.3	50	MHz	_	
CLOCK	PERIOD	Tc	-	30	25	ns	1	
CLOCK	HIGH TIME	Tch	12	-	_	ns	-	
	LOW TIME	Tcl	13	-	-	ns	1	
DATA	SETUP TIME	Tlvs	5	-	-	ns	İ	
DAIA	HOLD TIME	Tlvh	10	_	_	ns	1	
	FRAME RATE	Fr	-	60	72	Hz	Tv=Tvd+Tvb	
VERTICAL ACTIVE DISPLAY	TOTAL	Tv	-	525	-	Th	_	
TERM	DISPLAY	Tvd	-	480	-	Th	-	
	BLANK	Tvb	-	45	-	Th	1	
LIODIZONIALI ACTIVE DICDIAV	TOTAL	Th	-	928	_	Tc	Th=Thd+Thb	
HORIZONTAL ACTIVE DISPLAY	DISPLAY	Thd	_	800	-	Tc	_	
TEI UII	BLANK	Thb	-	128	-	Tc	_	

NOTE (1): BECAUSE THIS MODULE IS OPERATED BY DE ONLY MODE, HSYNC AND VSYNC INPUT SIGNALS SHOULD BE SET TO LOW LOGIC LEVEL OR GROUND. OTHERWISE, THIS MODULE WOULD OPERATE ARMORMALLY



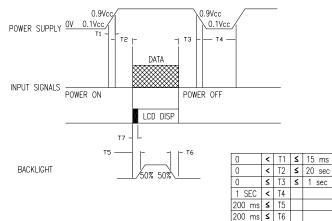


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#### POWER ON/OFF SEQUENCE

TO PREVENT A LATCH-UP OR DC OPERATION OF LCD MODULE, THE POWER ON/OFF SEQUENCE SHOULD FOLLOW THE CONDITIONS SHOWN IN THE FOLLOWING DIAGRAM.



#### NOTES:

- (1) THE SUPPLY OF THE EXTERNAL SYSTEM FOR THE MODULE INPUT SHOULD BE THE SAME AS THE DEFINITION OF VCC.
- (2) PLEASE APPLY THE LAMP VOLTAGE WITHIN THE LCD OPERATION RANGE. WHEN THE BACKLIGHT TURNS ON BEFORE THE LCD OPERATION OF THE LCD TURNS OFF, THE DISPLAY MAY, INSTANTLY, FUNCTION ABNORMALLY.
- (3) IN CASE OF VCC=OFF LEVEL, PLEASE KEEP THE LEVEL OF INPUT SIGNALS ON THE LOW OR KEEP A HIGH IMPEDANCE.
- (4) T4 SHOULD BE MEASURED AFTER THE MODULE HAS BEEN FULLY DISCHARGED BETWEEN POWER ON/OFF PERIODS.
- (5) INTERFACE SIGNAL SHALL NOT BE KEPT AT HIGH IMPEDANCE WHEN THE POWER IS ON.

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Our many years of experience data accumulation indicate that solder heat is a major cause of early and future failure. Please pay attention to your soldering process.

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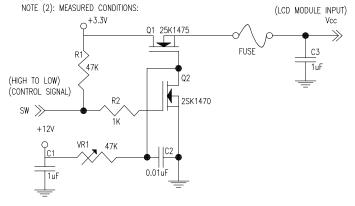
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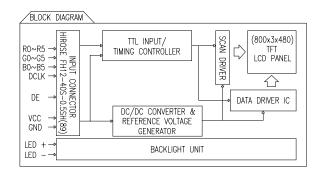
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DC CHARACTERISTICS	\						
PARAMETER	SYMBOL	STANDARD VALUE			UNIT	NOTE	
FARAMETER	SIMDUL	MIN	TYP.	MAX	UNII	NOIE	
POWER SUPPLY VOLTAGE	VCC	+3.0	+3.3	+3.6	٧	(1)	
RIPPLE VOLTAGE	VRP	-		TBD	m۷	-	
RUSH CURRENT	IRUSH	-	_	TBD	Α	(2)	
POWER SUPPLY CURRENT	ICC	-	TBD	-	m۷	(3)	
TTL INPUT LOW VOLTAGE	VIL	0	-	0.3Vcc	v	(4)	
TTL INPUT HIGH VOLTAGE	VIH	0.7Vcc	-	Vcc	١ ۲	(+)	
TTL INPUT LEAK CURRENT (LOW)	IIL	-	_	TBD	m۷	VI=0V(4)	
TTL INPUT LEAK CURRENT (HIGH)	ĬΗ	-	-	TBD	m۷	VI=3.3V(4)	

NOTE (1): THE MODULE IS RECOMMENDED TO OPERATE WITHIN SPECIFICATION RANGES LISTED ABOVE FOR NORMAL FUNCTION.



NOTE (3): THE SPECIFIED POWER SUPPLY CURRENT IS UNDER THE CONDITIONS AT Vcc=5.0V,  $T\alpha=25\pm2^{\circ}C$ , fv=60Hz, WHEREAS A GRAY LEVEL VERTICAL STRIPE PATTERN IS DISPLAYED. NOTE (4): CK,R0~R5, G0~G5, B0~B5, ENAB, HSYNC,VSYNC.



ABSULUTE MAXIMUM RATINGS	\						
ITEM	SYMBOL	STA	NDARD VA	ALUE	UNIT	NOTE	
IIEM	SIMBOL	MIN	TYP.	MAX	UNII	NOTE	
POWER SUPPLY VOLTAGE	VCC	0	-	+6.0	٧	(1)	
LOGIC INPUT VOLTAGE	VIN	-0.3	-	+6.3	٧	(1)	
STORAGE TEMPERATURE	TST	-20	-	+70	.C	(2)	
OPERATIN AMBIENT TEMPERATURE	TOP	-30	_	+80	*C	(4),(3)	

#### NOTE:

- (1) PERMANENT DAMAGE MIGHT OCCUR IF THE MODULE IS OPERATED AT CONDITIONS EXCEEDING THE MAXIMUM VALUES.
- (2) TEMPERATURE AND RELATIVE HUMIDITY RANGE IS SHOWN IN THE FIGURE BELOW.
  - (A) 95%RH MAX. (Ta≤40°C).

ADCOLUTE MAXIMUM DATINGS

- (B) WET-BULB TEMPERATURE SHOULD BE 39°C MAX. (Ta>40°C).
- (C) NO CONDENSATION.
- (3) THE TEMPERATURE OF PANEL SURFACE SHOULD BE 0°C MIN. AND 75°C MAX.

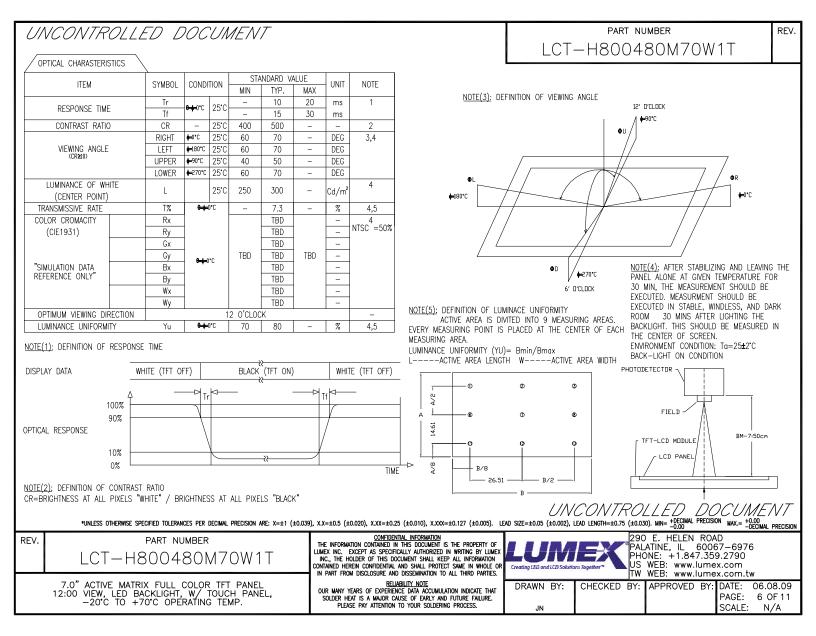
  (4) IT IS A NORMAL CHARACTERISTIC THAT THE LCD COLOR AND CONTRAST WILL CHANGE (SOMETIMES UNEVENNESS CAN BE SEEN) UNDER LOW TEMPERATURE AND HIGH TEMPERATURE ENVIRONMENT. WHEN THE APPLICATION TEMPERATURE RETURNS BACK TO ROOM TEMPERATURE, LCD COLOR AND CONTRAST WILL REVERSE BACK TO ITS ORIGINAL COLOR AND CONTRAST WILL REVERSE BACK TO ITS ORIGINAL COLOR AND CONTRAST WITHIN 24 HOURS AND ITS FUNCTIONALITY IS NOT AFFECTED.

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STANDARD SPECIFICATION FOR REABILITY

#### STANDARD SPECIFICATION OF REABILITY TEST

STAINE	STANDARD SPECIFICATION OF READILITY TEST							
NO	TEST ITEM	CONTENT OF TEST	TEST CONDITION	APPLICABLE STANDARD				
1	HIGH TEMPERATURE STORAGE	ENDURANCE TEST APPLYING THE HIGH STORAGE TEMPERATURE FOR A LONG TIME.	80+/-3°C 240HRS					
2	LOW TEMPERATURE STORAGE	ENDURANCE TEST APPLYING THE LOW STORAGE TEMPERATURE FOR A LONG TIME.	-30+/-3°C 240HRS					
3	HIGH TEMPERATURE OPERATION	ENDURANCE TEST APPLYING THE ELECTRIC STRESS (VOLTAGE & CURRENT) AND THE THERMAL STRESS TO THE ELEMENT FOR A LONG TIME.	70+/-3°C 240HRS					
4	LOW TEMPERATURE OPERATION	ENDURANCE TEST APPLYING THE ELECTRIC STRESS UNDER LOW TEMPERATURE FOR A LONG TIME.	-20+/-3°C 240HRS					
5	HIGH TEMPERATURE/ HUMIDITY OPERATION	ENDURANCE TEST APPLYING THE ELECTRIC STRESS (VOLTAGE & CURRENT) AND TEMPERATURE / HUMIDITY STRESS TO THE ELEMENT FOR A LONG TIME.	40°C, 90%RH 120HRS	MIL-202E-103B JIS-C5023				
6	TEMPERATURE CYCLE	ENDURANCE TEST APPLYING THE LOW AND HIGH TEMPERATURE CYCLE.  -20°C   5 MIN  5 MIN  1 CYCLE  -20°C   30 MIN  30 MIN	-20°C/ 70°C 10 CYCLES					
		MECHANICAL TEST						
7	DROP TEST	ENDURANCE TEST APPLYING THE DROP DURING	PACKED,100cm FREE FALL(6 SIDES, 1 CORNER, 3 EDGES)					

#### REMARKS:

1. FOR OPERATION TEST, ABOVE SPECIFICATION IS APPLICABLE WHEN TEST PATTERN IS CHANGING DURING ENTIRE OPERATION TEST.
2. INSPECTIONS AFTER RELIABILITY TESTS ARE PERFORMED WHEN THE DISPLAY TEMPERATURE RESUMES BACK TO ROOM TEMPERATURE.
3. IT IS A NORMAL CHARACTERISTIC THAT SOME DISPLAY ABNORMALITY CAN BE SEEN DURING REABILITY TEST. IF THE DISPLAY ABNORMALITY CAN RESUME BACK TO NORMAL CONDITION AT ROOM TEMPERATURE WITHIN 24 HOURS, THERE IS NO PERMANENT

DESTRUCTION OVER THE DISPLAY. THE DISPLAY STILL POSSESSES ITS FUNCTIONALITY AFTER RELIABILITY TESTS.

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#### QUALITY ASSURANCE

ACCEPTABLE QUALITY LEVEL (AQL)

EACH LOT SHOULD SATISFY THE QUALITY LEVEL DEFINED AS FOLLOWS:

A. INSPECTION METHOD: MIL-SDT-105E LEVEL II NORMAL ONE TIME SAMPLING.
B. AQL LEVEL.

CATEGORY	AQL	DEFINITION				
MAJOR	0.25%	FUNCTIONAL DEFECTIVE AS PRODUCT.				
MINOR	1.00%	SATISFY ALL FUNCTIONS AS PRODUCT BUT NOT SATISFY COSMETIC STANDARD.				

#### COSMETIC SCREENING CRITERIA

NO	DEFECT	JUDGMENT CRITERIA	CATEGORY
1	SPOTS/DUST /BUBBLE (ROUND TYPE)	SIZE, D(mm) ACCEPTABLE QUANTITY IN ACTIVE AREA DS0.15 DISREGARD 0.15≼D\$0.20 3 D>0.20 0	MINOR
2	DUST/ SCRATCHES/ BLACK STREAK (LINE TYPE)	ACCEPTABLE QUANTITY	MINOR
3	ALLOWABLE DENSITY	ABOVE DEFECTS SHOULD BE SEPARATED MORE THAN 5mm EACH OTHER.	MINOR
4		OBVIOUS UNVEN COLOR (RAINBOW) SHALL NOT BE NOTICEABLE.	MINOR
5	DISPLAY CONDITION	DIM DISPLAY ON THE PATTERNS, EXTRA PATTERN AND SHORT CIRCUIT ARE NOT ACCEPTABLE.	MAJOR
6	NO DISPLAY OR MISSING DISPLAY	THE PATTERNS OF DISPLAY SHALL LIGHT UP AS REQUIRED. NO DISPLAY OR MISSING DISPLAY ARE NOT ACCEPTABLE.	MAJOR

NOTE: D= (LONG LENGTH + SORTH LENGTH)/2

#### FAILURE JUDGMENT CRITERIA

AFTER REABILITY TEST ABOVE, TEST SAMPLE SHALL BE LET RETURN TO ROOM TEMPERATURE AND HUMIDITY AT LEAST 4 HOURS BEFORE FINAL TESTS ARE CARRIED OUT.

CRITERION ITEM	FAILURE JUDGMENT CRITERIA	
ELECTRICAL CHARACTERISTIC	ELECTRICAL SHORT AND OPEN.	
MECHANICAL CHARACTERISTIC	OUT OF MECHANICAL SPECIFICATION.	
OPTICAL CHARACTERISTIC	OUT OF APPEARANCE STANDARD.	

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#### PRECAUTIONS FOR USING LCD MODULE

PART NUMBER LCT-H800480M70W1T REV.

#### HANDLING PRECAUTIONS

- 1, THE DISPLAY PANEL IS MADE OF GLASS AND POLARIZER. DO NOT SUBJECT IT TO MECHANICAL SHOCK BY DROPPING OR IMPACT WHICH MAY CAUSE CHIPPING ESPECIALLY ON THE EDGES.
- 2. DO NOT TOUCH, PUSH OR RUB THE EXPOSED POLARIZERS WITH ANYTHING HARDER THAN AN HB PENCIL LEAD (GLASS, TWEEZERS, ETC.). THE POLARIZER COVERING THE DISPLAY SURFACE OF THE LCD MODULE IS SOFT AND EASILY SCRATCHED. HANDLE THIS POLARIZER CAERFULLY.
- 3. IF THE DISPLAY SURFACE BECOMES CONTAMINATED, BREATHE ON THE SURFACE AND GENTLY WIPE IT WITH A SOFT DRY CLOTH. IF IT IS HEAVILY CONTAMINATED, MOISTEN CLOTH WITH ISOPROPYL ALCOHOL OR ETHYL ALCOHOL. AVOID USING SOLVENTS LIKE ACETONE (KETENE), WATER, TOLUENE, ETHANOL TO CLEAN THE POLARIZER SURFACE.
- 4. PLEASE KEEP THE TEMPERATURE WITHIN SPECIFIED RANGE FOR USE AND STORAGE. POLARIZATION DEGRADATION, BUBBLE GENERATION OR POLARIZER PEEL-OFF MAY OCCUR WITH HIGH TEMPERATURE AND HIGH HUMIDITY
- 5. DO NOT APPLY EXCESSIVE FORCE TO THE DISPLAY SURFACE OR THE ADJOINING AREAS SINCE THIS MAY CAUSE THE COLOR TONE TO VARY
- 6. INSTALL THE LCD MODULE BY USING THE MOUNTING HOLES. WHEN MOUNTING THE LCD MODULE MAKE SURE IT IS FREE OF TWISTING, WARPING AND DISTORTION.
- EXERCISE CARE TO MINIMIZE CORROSION OF THE ELECTRODE. CORROSION OF THE ELECTRODES IS ACCELERATED BY WATER DROPLETS, MOISTURE CONDENSATION OR A CURRENT FLOW IN A HIGH-HUMIDITY ENVIRONMENT.
- 8. NC TERMINAL SHOULD BE OPEN. DO NOT CONNECT ANYTHING.
- IF THE LOGIC CIRCUIT POWER IS OFF, DO NOT APPLY THE INPUT SIGNALS.
- 10. AVOID CONTACTING OIL AND FATS.
- 11. CONDENSATION ON THE SURFACE AND CONTACT WITH TERMINALS DUE TO COLD WILL DAMAGE, STAIN OR DIRTY THE POLARIZERS. AFTER PRODUCTS ARE TESTED AT LOW TEMPERATURE THEY MUST BE WARMED UP IN A CONTAINER BEFORE COMING IN CONTACT WITH ROOM TEMPERATURE AIR.
- 12. WIPE OFF SALIVA OR WATER DROPS IMMIDEATLY, CONTACT WITH WATER OVER A LONG PERIOD OF TIME MAY CAUSE DEFORMATION OR COLOR FADING.

### ELECTRO-STATIC DISCHARGE CONTROL

- 1, SINCE THIS MODULE USES A CMOS LSI, THE SAME CAERFUL ATTENTION SHOULD BE PAID TO ELECTROSTATIC DISCHARGE AS FOR AN ORDINARY CMOS IC.

  2. BE SURE TO GROUND THE BODY WHEN HANDLING THE LCD MODULES. TOOLS REQUIRED FOR ASSEMBLING,
- SUCH AS SOLDERING IRONS, MUST BE PROPERLY GROUNDED.
- TO REDUCE THE AMOUNT OF STATIC ELECTRICITY GENERATED, DO NOT CONDUCT ASSEMBLING AND OTHER WORK UNDER DRY CONDITIONS. TO REDUCE THE GENERATION OF STATIC ELECTRICITY, BE CARFUL THAT THE AIR IN THE WORK IS NOT TOO DRIED. A RELATIVE HUMIDITY OF 50%-60% IS RECOMMENDED.

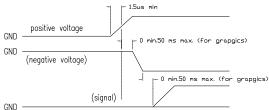
  4. THE LCD MODULE IS COATED WITH A FILM TO PROTECT THE DISPLAY SURFACE. EXERCISE CARE WHEN
- PEELING OFF THIS PROTECTIVE FILM SINCE STATIC ELECTRICITY MAY BE GENERATED
- 5. WHEN SOLDERING THE TERMINAL OF LCM, MAKE CERTAIN THE AC POWER SOURCE FOR THE SOLDERING IRON DOES NOT LEAK

#### PRECAUTION OF SOLDERING TO THE LCM

- 1, OBSERVE THE FOLLOWING WHEN SOLDERING LEAD WIRE, CONNECTOR CABLE AND ETC. TO THE LCD MODULE.
- SOLDERING IRON TEMPERATURE: 300~350°C.
- SOLDERING TIME: ≤3 SEC. SOLDER: EUTECTIC SOLDER.
- AROVE IS A RECOMMENDED APPROACH. DUE TO DIFFERENT SOLDER COMPOSITION AND PROCESSING METHOD. IT IS RECOMMENDED THAT CUSTOMER TO STUDY AND FINE TUNING THEIR SOLDERING PROCESS PARAMETERS
- 2. IF SOLDERING FLUX IS USED, BE SURE TO REMOVE ANY REMANING FLUX AFTER FINISHING TO SOLDERING OPERATION. (THIS DOSE NOT APPLY IN THE CASE OF A NON-HALOGEN TYPE OF FLUX.) IT IS RECOMMENDED THAT YOU PROTECT THE LCD SURFACE WITH A COVER DURING SOLDERING TO PREVENT ANY DAMAGE DUE TO FLLIX SPATTERS

#### PRECAUTION FOR OPERATION

- 1. VIEWING ANGLE VARIES WITH THE CHANGE OF LIQUID CRYSTAL DRIVING VOLTAGE (Vo.). ADJUST Vo. TO. SHOW THE BEST CONTRAST.
- 2. DRIVING THE LCD IN THE VOLTAGE ABOVE THE LIMIT SHORTERNS ITS LIFETIME.
- RESPONSE TIME IS GREATLY DELAYED AT TEMPERATURE BELOW THE OPERATING TEMPERATURE RANGE. HOWEVER, IT WILL RECOVER WHEN IT RETURNS TO THE SPECIFIED TEMPERATURE RANGE.
- 4. IF THE DISPLAY AREA IS PUSHED HARD DURING OPERATION, THE DISPLAY WILL BECOME ABNORMAL. HOWEVER, IT WILL RETURN TO NORMAL IF IT IS TURNED OFF AND THEN BACK ON.
- 5. WHEN TURNING THE POWER ON, INPUT EACH SIGNAL AFTER THE POSITIVE/NEGATIVE VOLTAGE BECOMES STABLE (BELOW FIGURE IS A GENERAL ILLUSRATION WHERE TYPICAL VALUE DEPENDS ON INDIVIDUAL PRODUCT DESIGN).



UNCONTROLLED DOCUMENT

\*UNLESS OTHERWISE SPECIFIED TOLERANCES PER DECIMAL PRECISION ARE: X=±1 (±0.039), X.X=±0.5 (±0.020), X.XX=±0.25 (±0.010), X.XXX=±0.127 (±0.005). LEAD SIZE=±0.05 (±0.002), LEAD LENGTH=±0.75 (±0.030). MIN=+DECIMAL PRECISION -0.00

PART NUMBER REV. LCT-H800480M70W1T

7.0" ACTIVE MATRIX FULL COLOR TFT PANEL 12:00 VIEW, LED BACKLIGHT, W/ TOUCH PANE -20°C TO +70°C OPERATING TEMP.

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RELIABILITY NOTE
OUR MANY YEARS OF EXPERIENCE DATA ACCUMULATION INDICATE THAT SOLDER HEAT IS A MAJOR CAUSE OF EARLY AND FUTURE FAILURE.
PLEASE PAY ATTENTION TO YOUR SOLDERING PROCESS.

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SCALE: N/A

PART NUMBER

REV.

LCT-H800480M70W1T

#### Rohs Compliant Product

LESS THAN 100PPM 1. CADMIUM AND CADMIUM COMPOUNDS 2. HEXAVALENT CHROMIUM COMPOUNDS LESS THAN 1000PPM 3. LEAD AND LEAD COMPOUNDS LESS THAN 1000PPM 4. MERCURY AND MERCURY COPMPOUNDS LESS THAN 1000PPM 5. POLYBROMINATED BIPHENYLS (PBBs) LESS THAN 1000PPM 6. POLYBROMINATED DIPHENYL ETHERS (PBDEs) LESS THAN 1000PPM

#### PACKAGING STANDARD

PRODUCT NO.	LCT-H800480M70W	RELEASE DATE	TBD
PRODUCT NAME.	7.0" TFT LCD MODULE	PREPARE BY:	
QUANTITY/ EACH BOX	TBD	BOX MATERIAL	PAPER CARTON
OUTER CARTON BOX SIZE	TBD	BOX TYPE	NEW
QUANTITY/ INER BOX QUANTITY/ OUTER BOX	N/A	WEIGHT	N/A KG

THERE ARE 12 PCS LCD PER EACH ANTI-STATIC PLASTIC PLATE. THERE ARE 7 LAYER PLASTIC PLATES PER EACH INNER CARTON BOX. THERE ARE 2 INNER CARTON BOX PER EACH OUTER CARTON BOX.

## STORAGE

- 1. WHEN STORING LCDS AS SPARES FOR SOME YEARS, THE FOLLOWING PRECAUCTIONS ARE NECESSARY.
  2. STORE THEM IN A SEALED POLYETHYLENE BAG. IF PROPERLY SEALED, THERE IS NO NEED FOR DESICCANT.
  3. STORE THEM IN A DARK PLACE. DO NOT EXPOSE TO SUNLIGHT OR FLUORESCENT LIGHT, KEEP THE
- TEMPERATURE BETWEEN 0°C AND 35°C.
- 4. ENVIRONMENTAL CONDITIONS:
- 5. DO NOT LEAVE THEM FOR MORE THAN 168HRS. AT 60°C.
- 6. SHOULD NOT BE LEFT FOR MORE THAN 48HRS. AT -20°C.

## <u>SAFETY</u>

1. ITS RECOMMENDED TO CRUSH DAMAGED OR UNNECESSARY LCD INTO PIECES AND WASH THEM OFF WITH SOLVENTS SUCH AS ACETONE AND ETHANOL, WHICH SHOULD LATER BE BURNED. 2. IF ANY LIQUID LEAKS OUT OF DAMAGED GLASS CELL AND COMES IN CONTACT WITH THE HANDS, WASH OFF THOROUGHLY WITH SOAP AND WATER.

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PART NUMBER REV. LCT-H800480M70W1T

7.0" ACTIVE MATRIX FULL COLOR TFT PANEL 12:00 VIEW, LED BACKLIGHT, W/ TOUCH PANEL, -20°C TO +70°C OPERATING TEMP.

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