



### C Series High Q Capacitors

Type:

C0603 [EIA CC0201]

Issue date:

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TDK MLCC US Catalog

Version A11

Downloaded from Elcodis.com electronic components distributor

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Please read before using this product

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## **C** Series High Q Capacitors

Type: C0603

### Available Through Distribution Only\*

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#### Features



- · Higher Q factor than standard capacitors
- · High stability with respect to time, temperature,
- frequency, and voltage · Excellent attenuation
- · High self-resonant frequency
- · Lower power dissipation/less energy absorption
- Capacitance range of 0.2pF to 15pF •
- Available in standard and tight tolerance •
- Please contact TDK for Q values

### Shape & **Dimensions**





#### **Applications**



- · High-frequency applications PA modules
- · Cellular communication, Bluetooth
- · Cable/satellite TV
- · GPS/satellite radio
- · Filter networks/matching networks · RF amplifiers/Low noise amplifiers
- · VCOs, TCXOs, etc.
- · DC blocking circuits

Dimensions	in	mm



#### Part Number Construction

Series Name 🗕						Internal Codes	
Dimensions L x	W (mm)					Packaging Style	
Case Code	Length	Width				Packaging Code	Style
C0603	0.60±0.03	0.30±0.03				Т	Tape and Reel
Femperature Ch	aracteristic —					Capacitance Tole	rance
Temperature	Capacitance	Temperature				Tolerance Code	Tolerance
Characteristics	Change	Range				В	± 0.10pF
C0G	0±30 ppm/ºC	-55 to +125ºC	_			С	± 0.25pF
Rated Voltage (E	)C)			] [		D	± 0.50pF
Voltage Code	Voltage(DC)					E	± 0.20pF
1F	25\/					G	± 2%
Ind Conceil						J	± 5%
vommai Capaci	lance (pr)						

second digits identify the first and second significant figures of the capacitance. The third digit identifies the multiplier. R designates a decimal point.

Capacitance Code Capacitance

0.5pF
1pF
1,000pF (1nF)
1,000,000pF (1µF)

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## MULTILAYER CERAMIC CHIP CAPACITORS



## C0603 [EIA CC0201]

Available Through Distribution Only\*

Capacitance Range Chart

Temperature Characteristics: C0G (0 ± 30 ppm/<sup>2</sup>C) Rated Voltage: 25V (1E)

Capacitanaa	Con						Tolerance			
(nE)	Code	Voltage		В		С	D	E	G	J
(pr)	Code		(±0	.10pF)	(±0.	25pF)	(±0.50pF)	(±0.20pF)	(± 2%)	(± 5%)
0.2	0R2	25V (1E)					· · · ·			. ,
0.3	0R3	- ( )								
0.4	0R4									
0.5	0R5									
0.6	0R6									
0.7	0R7									
0.8	0R8	4								
0.9	0R9	-								
1.0	010	-								
1.2	1R2	-								
1.5	1R5	-								
1.8	1R8	-								
2.0	020	-	_							
2.2	2R2	-	_							
2.7	2R7	-								
3.0	030	-	_							
3.3	200	-								
3.9	040	-	-							
4.0	/R7	-								
5.0	050	-								
5.0	5B6									
6.0	060	-								
6.8	6R8									
7.0	070									
8.0	080	1								
8.2	8R2	1								
9.0	090	1								
10.0	100	]								
12.0	120									
15.0	150									

Standard Thickness

0.30 ± 0.03 mm

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## CERAMIC CHIP CAPAC



### Capacitance Range Table

### C0603 [EIA CC0201]

Available Through Distribution Only\*

Class 1 (Temperature Compens	ating)				
Temperature Characteristics	: COG (0 ± 30 ppm/	<sup>2</sup> C)	<b>o</b> "		
TDK Part Number	Temperature	Rated	Capacitance	Capacitance	Thickness
(Ordering Code)	Characteristics	Voltage	(p⊦)	lolerance	(mm)
C0603C0G1E0R2BTQ	COG	25V	0.2	± 0.10pF	$0.30 \pm 0.03$
C0603C0G1E0R3BTQ	COG	25V	0.3	± 0.10pF	$0.30 \pm 0.03$
C0603C0G1E0R4BTQ	COG	25V	0.4	± 0.10pF	$0.30 \pm 0.03$
C0603C0G1E0R5BTQ	COG	25V	0.5	± 0.10pF	$0.30 \pm 0.03$
C0603C0G1E0R6BTQ	COG	25V	0.6	± 0.10pF	$0.30 \pm 0.03$
C0603C0G1E0R7BTQ	COG	25V	0.7	± 0.10pF	$0.30 \pm 0.03$
C0603C0G1E0R8BTQ	COG	25V	0.8	± 0.10pF	$0.30 \pm 0.03$
C0603C0G1E0R9BTQ	COG	25V	0.9	± 0.10pF	$0.30 \pm 0.03$
C0603C0G1E010BTQ	COG	25V	1	± 0.10pF	$0.30 \pm 0.03$
C0603C0G1E010CTQ	COG	25V	1	± 0.25pF	$0.30 \pm 0.03$
C0603C0G1E1R2BTQ	COG	25V	1.2	± 0.10pF	$0.30 \pm 0.03$
C0603C0G1E1R2CTQ	COG	25V	1.2	± 0.25pF	$0.30 \pm 0.03$
C0603C0G1E1R5BTQ	COG	25V	1.5	± 0.10pF	$0.30 \pm 0.03$
C0603C0G1E1R5CTQ	COG	25V	1.5	± 0.25pF	$0.30 \pm 0.03$
C0603C0G1E1R8BTQ	COG	25V	1.8	± 0.10pF	$0.30 \pm 0.03$
C0603C0G1E1R8CTQ	COG	25V	1.8	± 0.25pF	$0.30 \pm 0.03$
C0603C0G1E020BTQ	COG	25V	2	± 0.10pF	$0.30 \pm 0.03$
C0603C0G1E020CTQ	COG	25V	2	± 0.25pF	$0.30 \pm 0.03$
C0603C0G1E2R2BTX	COG	25V	2.2	± 0.10pF	$0.30 \pm 0.03$
C0603C0G1E2R2CTX	COG	25V	2.2	± 0.25pF	$0.30 \pm 0.03$
C0603C0G1E2R7BTX	COG	25V	2.7	± 0.10pF	$0.30 \pm 0.03$
C0603C0G1E2R7CTX	COG	25V	2.7	± 0.25pF	$0.30 \pm 0.03$
C0603C0G1E030BTX	COG	25V	3	± 0.10pF	$0.30 \pm 0.03$
C0603C0G1E030CTX	COG	25V	3	± 0.25pF	$0.30 \pm 0.03$
C0603C0G1E3R3BTX	COG	25V	3	± 0.10pF	$0.30 \pm 0.03$
C0603C0G1E3R3CTX	COG	25V	3.3	± 0.25pF	$0.30 \pm 0.03$
C0603C0G1E3R9BTX	COG	25V	3.9	± 0.10pF	$0.30 \pm 0.03$
C0603C0G1E3R9CTX	COG	25V	3.9	± 0.25pF	$0.30 \pm 0.03$
C0603C0G1E040BTX	COG	25V	4	± 0.10pF	$0.30 \pm 0.03$
C0603C0G1E040CTX	COG	25V	4	± 0.25pF	$0.30 \pm 0.03$
C0603C0G1E4R7BTX	COG	25V	4.7	± 0.10pF	$0.30 \pm 0.03$
C0603C0G1E4R7CTX	COG	25V	4.7	± 0.25pF	$0.30 \pm 0.03$
C0603C0G1E050BTX	COG	25V	5	± 0.10pF	$0.30 \pm 0.03$
C0603C0G1E050CTX	COG	25V	5	± 0.25pF	$0.30 \pm 0.03$
C0603C0G1E5R6BTX	COG	25V	5.6	± 0.10pF	$0.30 \pm 0.03$
C0603C0G1E5R6CTX	COG	25V	5.6	± 0.25pF	$0.30 \pm 0.03$
C0603C0G1E060BTX	COG	25V	6	± 0.10pF	$0.30 \pm 0.03$
C0603C0G1E060CTX	COG	25V	6	± 0.25pF	$0.30 \pm 0.03$
C0603C0G1E6R8BTX	COG	25V	6.8	± 0.10pF	$0.30 \pm 0.03$
C0603C0G1E6R8CTX	COG	25V	6.8	± 0.25pF	$0.30 \pm 0.03$
C0603C0G1E070BTX	COG	25V	7	± 0.10pF	$0.30 \pm 0.03$
C0603C0G1E070CTX	COG	25V	7	± 0.25pF	$0.30 \pm 0.03$
C0603C0G1E080BTX	COG	25V	8	± 0.10pF	$0.30 \pm 0.03$
C0603C0G1E080CTX	COG	25V	8	± 0.25pF	$0.30 \pm 0.03$
C0603C0G1E8R2BTX	COG	25V	8.2	± 0.10pF	$0.30 \pm 0.03$
C0603C0G1E8R2CTX	COG	25V	8.2	± 0.25pF	$0.30 \pm 0.03$
C0603C0G1E090BTX	COG	25V	9	± 0.10pF	$0.30 \pm 0.03$
C0603C0G1E090CTX	COG	25V	9	± 0.25pF	$0.30 \pm 0.03$
C0603C0G1E100ETX	COG	25V	10	± 0.20pF	$0.30 \pm 0.03$
C0603C0G1E100DTX	COG	25V	10	± 0.50pF	$0.30 \pm 0.03$
C0603C0G1E120GTX	COG	25V	12	± 2%	$0.30 \pm 0.03$
C0603C0G1E120JTX	COG	25V	12	± 5%	$0.30 \pm 0.03$
C0603C0G1E150GTX	COG	25V	15	± 2%	$0.30 \pm 0.03$
C0603C0G1E150JTX	COG	25V	15	± 5%	$0.30 \pm 0.03$

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## MULTILAYER CERAMIC CHIP CAPACITORS

General Specifications

## C0603 Series – High Q Capacitors

No.	ltem	Performance		Test or Inspection Method			
1	External Appearance	No defects which mapperformance.	ay affect	Inspect with magnifying glass (10 $\times$ ).			
2	Insulation Resistance	10,000M $\Omega$ min.		Apply ra	ted voltage for 60s.		
3	Voltage Proof	Withstand test voltage insulation breakdow	ge without n or other damage.	Class Class 1 Above D	Apply voltage $3 \times$ rated voltageOC voltage shall be a current shall not e	- - - applied for 1 to 5s. Charge / exceed 50mA.	
4	Capacitance	Within the specified	tolerance.	Class 1	Measuring Frequency	Measuring voltage	
5	Q (Class 1)	Rated Capacitance   C ≥ 30pF   C < 30pF   C < 50pF	Q   1,000 min.   400 + 20×C min.   Rated capacitance (pF)	See No.4 in this table for measuring condition.			
6	Temperature Characteristics of Capacitance (Class 1)	T.C.TemperatCOG $0 \pm 30 \text{ pp}$ Capacitance driftWithin $\pm 0.2\%$ or $\pm$ larger.	Tempera values a Measuri -25ºC.	ature coefficient sha It 25ºC and 85ºC ter ng temperature belo	ll be calculated based on nperature. w 20ºC shall be -10ºC and		
7	Robustness of Terminations	No sign of termination breakage of ceramic signs.	on coming off, c, or other abnormal	Reflow s Append 10±1s.	solder the capacitor ix 1) and apply a pus	on P.C. board (shown in shing force of 2N for Pushing force P.C. board	
8	Bending	No mechanical dam	age.	Reflow s Append	solder the capacitor ix 2) and bend it for $50 = 10^{-20}$	on P.C. board (shown in 1mm. f F R230 45 1 45 1 1 1 1 1 1 1 1	

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## MULTILAYER CERAMIC CHIP CAPACITORS



## C0603 Series – High Q Capacitors

No.	ltem	Performance			Test or Inspection Method	
9	Solderability	New solder to contermination.	ver ov	ver 75% of	Completely soak both terminations in solder at $235\pm5^{\circ}$ C for $2\pm0.5$ s.	
		25% may have pi	inhole	es or rough spots	Solder: H63A (JIS Z 3282)	
		but not concentra	ated in	n one spot.	Flux: Isopropyl alcohol (JIS K 8839)	
		Ceramic surface be exposed due t termination mater	of A s to mel rial.	ections shall not Iting or shifting of	Rosin (JIS K 5902) 25% solid solution.	
		K	As	ection		
10	Resistance to so	older heat			Completely soak both terminations in solder at	
	External	No cracks are all	owed	and terminations	$260 \pm 5^{\circ}$ C for 5±1s.	
	appearance	shall be covered	at lea	st 60% with new	Preheating condition	
					$= 16mp.: 150 \pm 10^{\circ}C$	
	Capacitance	Characteristics Cha		nge from the e before test		
		Class 1 C0G	Capa	acitance drift within	Bosin (JIS K 5902) 25% solid solution.	
			+±2.	5% or $\pm$ 0.25pF, hever larger.	Solder: H63A (JIS 7 3282)	
			1		Leave the capaciter in ambient conditions for 6 to 24h	
	Q (Class 1)	Rated Capacitance		Q 1.000 min	before measurement.	
		$C \le 30 \text{pF}$		1,000  min. 400 + 20 × C min		
		<u> </u>	C : Rate	ed capacitance (pF)		
	Insulation	Meet the initial sr			-	
	Resistance	weet the initial sp	500.			
	Voltage	No insulation bre	akdov	vn or other	-	
	Proof	damage.				
11	Vibration				Reflow solder the capacitor on P.C. board (shown in	
	External	No mechanical da	amaq	e.	Appendix 1) before testing.	
	appearance		0		Vibrate the capacitor with amplitude of 1.5mm P-P	
	Capacitance	Characteristics	Cha valu	nge from the e before test	to 10Hz after 1min.	
		Class 1 C0G	Capacitance drift with $\pm 2.5\%$ or $\pm 0.25$ pF, whichever larger.		Repeat this for 2h each in 3 perpendicular directions.	
	Q (Class 1)	Rated Capacitan	nce	Q		
		C ≥ 30pF		1,000 min.		
		C < 30pF		400+20×C min.		
	C : Rated capacitance (pF)					

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## MULTILAYER CERAMIC CHIP CAPACITORS



### General Specifications

## C0603 Series – High Q Capacitors

No.	ltem	Performance	;		Test or	r Inspection Method		
12	Temperature cycle External appearance	e No mechanica	dama	age.	Reflow solder the capacitors on a P.C. board (shown in Appendix 1) before testing. Expose the capacitor in the conditions in step 1			
	Capacitance	Characteristic	s Ch va	nange from the lue before test	Leave the force r	step 4, and repeat 5 times of the capacitor in ambient conc neasurement.	ditions for 6 to 24h	
			±   wł	2.5% or $\pm$ 0.25pF, hichever larger.	Step	Temperature (°C)	Time (min.)	
					· 1	Min. operating temp. $\pm 3$	30 ± 3	
	Q (Class 1)	Rated Capaci	ance	Q	2	Reference Temp.	2-5	
		$C \ge 30 pF$		1,000 min.	3	Max. operating temp. $\pm$ 2	30 ± 2	
		C < 30pF		400 + 20×C min.	4	Reference Temp.	2 - 5	
			C : Ra	ated capacitance (pF)		· ·	1	
13	Resistance Voltage Proof Moisture Resistan	No insulation b damage.	oreakd	own or other	Reflows	solder the capacitor on P.C.	board (shown in	
	External appearance	No mechanica	dama	age.	Appendix 1) before testing. Leave at temperature $40\pm2^{\circ}$ C, 90 to 95%RH for 500			
	Capacitance	Characteristic	Change from the value before test			Leave the capacitor in ambient condition for 6 to 24h		
		Class 1 COG	Ca ± wh	apacitance drift within 5% or $\pm$ 0.5pF, nichever larger.	before r	neasurement.		
	Q (Class 1)	Rated Capacit	ance	0	-			
	· · ·	C > 30pF		350 min.				
		$10 \text{ pF} \le C < 30$	νF	$275 + 5/2 \times C \min$				
		C < 10 n E		$200 + 10 \times C \min$				
		<u> </u>	C : Ra	ated capacitance (pF)				
	Insulation Resistance	1,000M $\Omega$ min			-			

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## MULTILAYER CERAMIC CHIP CAPACITORS

General Specifications

## C0603 Series – High Q Capacitors

No.	ltem	Perform	ance			Test or Inspection Method		
14	Moisture Resistance				Reflow solder the capacitors on P.C. board (shown in			
	External	No mech	anical d	ama	ge.			
	appearance					Apply the rated voltage at temperature $40\pm2^{\circ}$ C and 90 to 95% BH for 500 +24.0h		
	Capacitance	Characte	eristics	Ch val	ange from the ue before test	Charge/discharge current shall not exceed 50mA.		
		Class 1	C0G	Cal ±7	pacitance drift within 7.5% or $\pm 0.75$ pF,	Leave the capacitor in ambient conditions for 6 to 24h before measurement.		
				VVIII		Use this measurement for initial value.		
	Q (Class 1)	Rated C	apacitar	ce	Q			
		$C \ge 30 pF$	-		200 min.			
		C < 30pF	30pF 100 + 10/3×C mi		100 + 10/3×C min.			
			C	: Ra	ted capacitance (pF)			
	Insulation Resistance	500MΩ r	nin.			·		
15	Life					Reflow solder the capacitor on P.C. board (shown in		
	External appearance	No mech	anical d	ama	ge.	Apply 2x rated voltage at 125±2°C for 1,000 +48, 0h.		
	Capacitance	Characteristics Change from the			ange from the	Charge/discharge current shall not exceed 50mA.		
		Class 1	C0G	Cal ±3	pacitance drift within $3\%$ or $\pm 0.3pF$ ,	Leave the capacitors in ambient condition for 6 to 24h before measurement.		
				whi	chever larger.	Use this measurement for initial value.		
	Q (Class 1)	Rated Ca	apacitan	се	Q			
		C ≥ 30pF			350 min.			
		10pF ≤ C	; < 30pF		275 + 5/2×C min.			
		C < 10pF	C < 10pF		200 + 10×C min.			
			C	: Ra	ted capacitance (pF)			
	Insulation Resistance	1,000M Ω	min.					

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## MULTILAYER CERAMIC CHIP CAPACITORS



General Specifications

## C0603 Series – High Q Capacitors





Material : Glass Epoxy ( As per JIS C6484 GE4 )

P.C. Board thickness :	Appendix - 2	0.8mm
	Appendix - 1	1.6mm



Copper ( thickness 0.035mm ) Solder resist

Case	Code	Dimensions (mm)					
JIS	EIA	а	b	С			
C0603	CC0201	0.3	0.8	0.3			

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## MULTILAYER CERAMIC CHIP CAPACITORS



### Recommended Soldering Land Pattern



Reflow Soldering	Unit: mm		
Туре	C0603		
Symbol	[CC0201]		
А	0.25 ~ 0.35		
В	0.2 ~ 0.3		
С	0.25 ~ 0.35		

#### • Recommended Solder Amount



## C0603 Series – High Q Capacitors

#### Recommended Soldering Profile



#### **Recommended soldering duration**

Temp./	Reflow Soldering		
Dura. Solder	Peak temp	Duration	
	(0)	(Sec.)	
Sn-Pb Solder	230 max.	20 max.	
Lead-Free Solder	260 max.	10 max.	

Recommended solder compositions Sn-37Pb (Sn-Pb solder) Sn-3.0Ag-0.5Cu (Lead Free Solder)

#### **Preheating Condition**

Soldering	Temp. (ºC)
Reflow soldering	ΔT ≤ 150
Manual soldering	∆T ≤ 150

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## MULTILAYER CERAMIC CHIP CAPACITORS



## C0603 Series – High Q Capacitors

#### Carrier Tape Configuration



#### • Peel Back Force (Top Tape)



#### • Chip Quantity Per Reel and Structure of Reel

• Carrier tape shall be flexible enough to be wound around a minimum radius of 30mm with components in tape.

 $\bullet$  The missing of components shall be less than 0.1%

Components shall not stick to the cover tape.

• The cover tape shall not protrude beyond the edges of the carrier tape and shall not cover the sprocket holes.



Paper Carrier Tape & Reel

(Bottom cover tape is not always applied)

Case	Code	Chip	Toning	Chip quantity (pcs.)	
JIS	EIA	Thickness (mm)	Material	φ178mm (7") reel	
C0603	CC0201	0.30	Paper	15,000	

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## MULTILAYER CERAMIC CHIP CAPACITORS



### C0603 Series – High Q Capacitors

Shape & Dimensions



Case Code		Dimensions (mm)				
JIS	EIA	L	W	Т	В	G
C0603	CC0201	0.60	0.30	0.30	0.15	0.20 min.

#### Inside Structure & Material System



No.	NAME	MATERIAL
		Class 1
(1)	Ceramic Dielectric	CaZrO <sub>3</sub>
(2)	Internal Electrode	Nickel (Ni)
(3)		Copper (Cu)
(4)	Termination	Nickel (Ni)
(5)		Tin (Sn)

#### Environmental Information

TDK Corporation established internal product environmental assurance standards that include the six hazardous substances banned by the EU RoHS Directive<sup>1</sup> enforced on July 1, 2006 along with additional substances independently banned by TDK and has successfully completed making general purpose electronic components conform to the RoHS Directive<sup>2</sup>.

- Abbreviation for Restriction on Hazardous Substances, which refers to the regulation EU Directive 2002/95/EC on hazardous substances by the European Union (EU) effective from July 1, 2006. The Directive bans the use of six specific hazardous substances in electric and electronic devices and products handled within the EU. The six substances are lead, mercury, cadmium, hexavalent chromium, PBB (polybrominated biphenyls), and PBDE (polybrominated diphenyl ethers).
- This means that, in conformity with the EU Directive 2002/95/EC, lead, cadmium, mercury, hexavalent chromium, and specific bromine-based flame retardants, PBB and PBDE, have not been used, except for exempted applications.
- For REACH (SVHC : 15 substances according to ECHA / October 2008) : All TDK MLCC do not contain these 15 substances.
- For European Directive 2000/53/CE and 2005/673/CE : Cadmium, Hexavalent Chromium, Mercury, Lead are not contained in all TDK MLCC.
- For European Directive 2003/11/CE : Pentabromodiphenyl-ether, Octabromodiphenyl-ether are not contained in all TDK MLCC.

\* This series is available through the distribution channel only. Please see www.tdk.com/distributor.php for a list of authorized distributors.