

Standard CP*2512, CP*2525 shown in image	<ul> <li><u>Construction</u>:</li> <li>99.5% BeO or High Purity Alumina</li> <li>Nickel alloy thin-film resistive element</li> <li>Epoxy-resin overcoat</li> <li>Pre-tinned (Sn100, matte) terminations over Ni barrier is standard</li> </ul>	<ul> <li>Features:</li> <li>TCR's to ± 25ppm/°C</li> <li>Tolerances less than ± 1% available</li> <li>Standard and custom sizes &amp; terminations available (Sn60Pb40 option)</li> <li>High volume production, suitable for commercial and special applications</li> </ul>
		<ul> <li>Competitive pricing</li> </ul>

# **Description:**

These power resistors are designed to tolerate high current and establish a low thermal resistance interface with the circuit board. A lower thermal resistance more efficiently sinks heat to the board, enabling a larger effective area for heat dissipation. As a result, much lower surface temperatures are achievable in comparison to standard chip resistors for the same chip size and applied power.

# **Dimensions:**



**CPA2512 Derating Curve Examples:** <sup>6</sup>

# **Electrical Specifications:**

Size: Inch (Metric)	2512 (6332)	100
Rated Power <sup>1,2</sup> (Alumina)	Up to 16W <sup>1,2</sup>	
Rated Power <sup>1,2</sup> (BeO)	Up to $16W^{1,2}$ (Up to $60W$	80 10W @ 4.5C/W
	to be qualified soon)	
Rated Voltage	$\sqrt{(PxR)}$	
Resistance Tolerance	± 1%	
Standard Resistance Values (E12)	3.3 to 120 Ω	30
	Call for other values	20 10W @ 0.1C/W or 1
TCR (ppm/°C) <sup>3</sup>	$\pm 25 (E) > 22 \Omega$	10 200 @ 300/W etc.
	$\pm 50 (Q)$ 3.3 to 22 $\Omega$	
Operating Temperature Range <sup>4</sup>	-55 to 155°C	
Insulation Resistance (100V, 1min) <sup>5</sup>	$> 1G\Omega$	Ambient/Heat-Sink Temperature "C

Notes:

1. Dependent on effective thermal conductivity/resistance of board construction/land design and size of board - greater power capability for board/land with lower thermal resistance. For relatively high thermal resistance mountings, the power resistors are capable of generating sufficient heat to reflow solder bonds without device damage.

- 2. Refer to Thermal Performance Plot below.
- 3. Per MIL-PRF-55342 (-55/25/125°C).
- 4. Per MIL-PRF-55342.
- 5. Per IEC 60115-1.
- 6. Derating curves are derived from the thermal performance plots.



#### Notes:

- Plots produced via characterization of thermal coefficients determined from experimental measurements (via thermal imaging camera) at thermal equilibrium with parts mounted to various boards (with homogeneous thermal conductivity to minimize uncertainty) per recommended solder pad dimensions and with boards pressed against a Cu carrier/heat-sink (not ideal) with a thermal compound interface in a static environment (no air flow).
- Heat flow primarily through thickness of board with virtually zero lateral heat transfer in board.
- Thermal resistance of test boards were calculated based on material manufacturer specified thermal conductivity (20°C) via the following: Thermal Resistance (°C/W) = L / (k A), where Thermal Conductivity, k (W/m•K) = (L / (A  $\Delta$ T))  $\Delta$ Q/ $\Delta$ t, L = Thickness of board in meters and A = area of chip resistor in meters (2512 size = 6.3x3.2mm)
- The relationships between peak surface temperature rise, power, and board thermal resistance are linear, but the x-axis is plotted in log-scale to offer greater resolution at lower board thermal resistances.

# **Recommended Solder Pad Dimensions:**



	Stondard Dimensions (mm)
Cince In the (Materia)	Standard Dimensions (mm)
Size: Inch (Metric)	2512 (6332)
А	1.6
В	7.7
С	3.5

# **Environmental Performance Specifications:**

Test	Reference	Conditions of Test	Requirement	
Life <sup>4</sup>	MIL-PRF-55342,	70°C, 2000h, rated power <sup>3</sup> , 1.5h on, 0.5h off	$\pm 0.5\% + 0.01\Omega$	
	MIL-STD-202			
	Method 108A			
Thermal Shock	MIL-PRF-55342,	Condition F-3, -65°C/0.25h to 155°C/0.25h, 100	$\pm 0.1\% \pm 0.01\Omega$	
	MIL-STD-202	cycles		
	Method 107G			
High Temperature Exposure	MIL-PRF-55342	155°C, 100h	$\pm \ 0.1\% + 0.01\Omega$	
Short Time Overload <sup>4</sup>	MIL-PRF-55342	6.25x rated power <sup>3</sup> , 5 sec.	$\pm 0.1\% \pm 0.01\Omega$	
Moisture Resistance	MIL-PRF-55342,	25/65/25/65/25/–10°C, 90% to 98%RH, 10 cycles,	$\pm 0.1\% \pm 0.01\Omega$	
	MIL-STD-202	24h/cycle, with and without bias, bias = 1.5h on,		
	Method 106G	$0.5h \text{ off } @ 1/10^{th} \text{ rated power}^{3}$		
Resistance to Soldering Heat <sup>1</sup>	MIL-PRF-55342,	260°C for 15 sec., over 220°C for 60 sec., 3 cycles	$\pm 0.1\% \pm 0.01\Omega$	
	MIL-STD-202			
	Method 210F			
Solderability <sup>2</sup>	MIL-PRF-55342,	Precondition E: 150°C dry bake for 16h,	Min 95% coverage	
	MIL-STD-202	Method 1 "Dip and Look Test", 245°C, 5 sec., Pb-	of critical area	
	Method 208H	free (SnAgCu) Solder		
Board Flex	IEC 60115-1 /	Bend amount of 3mm, measurements during and	$\pm 0.1\% + 0.01\Omega$ ,	
	JIS C 5202	after bend	No mech. damage	
Terminal Strength	MIL-PRF-55342	Force of 3kg for 30 sec.	No mech. damage	

Notes:

1. Test conditions modified to represent the high temperature Pb-free reflow conditions and an extra cycle is added.

2. JESD22-B102D adds test conditions for Pb-free and is aligned with J-STD-002B referenced in MIL-STD-202 Method 208H.

JESD22-B102D procedure comes from EIA-638, "Surface Mount Solderability Test".

- Parts mounted to boards in accordance with NEMA grade FR-4 of IPC-4101 (62mils thick) with no Cu carrier/heat-sink at a rated power of 2W (Board Therm. Res. ~ 72C/W).
- 4. Due to the complexity of managing the heat load of hundreds of pieces during qualification, long-term reliability testing for the 16W power rating had been conducted in terms of the equivalent current density via much thinner/narrower resistor patterns to limit the heat load. Full power testing is being conducted on a smaller scale to be completed soon.



# Marking:

A27R0	<ul> <li>Marking shall include:</li> <li>Material Designator (A = Alumina, B = BeO)</li> <li>The 4-digit Resistance Value (MIL-STD-1285D)</li> <li>Ex. A27R0 = 27.0Ω Resistance with Alumina Material</li> </ul>
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### Part Numbering: (Ex. CPA2512E27R0FS-T10)

СР		A	2512		Е	27R0	F	S	-T10
Product	M	laterial	Size, Inch		TCR	Resistance	Tolerance	Custom	Packaging
Designator	esignator Designator					Value		Designator	Tape & Reel
СР	A =	Alumina	Refer to	E =	± 25 ppm/°C	Ex. 27R0	$F = \pm 1\%$	Standard $=$ S	-T10 = 1000
	B =	BeO	table above	Q =	$\pm$ 50 ppm/°C	= 27.0 Ω		Custom = TBD	-T50 = 5000

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