

Sulfur Tolerant Chip Resistors

TRR18 (3216size (1206inch))

Features

- 1) Unique protect materials prevent from silver sulfide occurrence under sulfur environment.
- 2) Highly recommended for automotive, industrial and Power supply applications under sulfur environment.
- 3) Realize the good cost performance not like the Au terminal components from other suppliers.
- 4) ROHM resistors have approved ISO9001 / ISO/TS 16949 certification.

Design and specifications are subject to change without notice. Carefully check the specification sheet supplied with the product before using or ordering it.

Ratings

Item	Conditions	Specifications 0.25W (1 / 4W) at 70°C		
Rated power	Power must be derated according to the power derating curve in Figure 1 when ambient temperature exceeds 70°C.			
Rated voltage	The voltage rating is calculated by the following equation. If the value obtained exceeds the limiting element voltage, the voltage rating is equal to the maximum operating voltage. $E = \sqrt{P \times R} \qquad E : \text{Rated voltage (V) } P : \text{Rated power (W)} \\ R : \text{Nominal resistance } (\Omega)$	Limiting element voltage 200V		
Nominal resistance	See <u>Table 1</u> .			
Operating temperature		−55°C to +155°C		

Jumper type

Resistance	Max. $50m\Omega$	
Rated current	2A	
Operating temperature	-55°C to +155°C	

Table 1

Resistance tolerance	Resistance range (Ω)		Resistance temperature coefficien (ppm/°C)	
J (±5%)	1.0 to 9.1	(E24)	±400	
J (±5%)	10 to 10M	(E24)	±200	
F (±1%)	10 to 2.2M	(E24)	±100	

•Before using components in circuits where they will be exposed to transients such as pulse loads (short–duration, high– level loads), be certain to evaluate the component in the mounted state. In addition, the reliability and performance of this component cannot be guaranteed if it is used with a steady state voltage that is greater than its rated voltage.

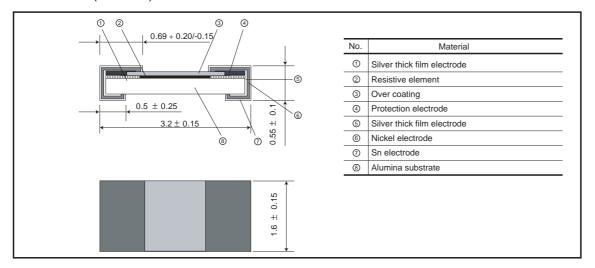
TRR18 Data Sheet

Characteristics

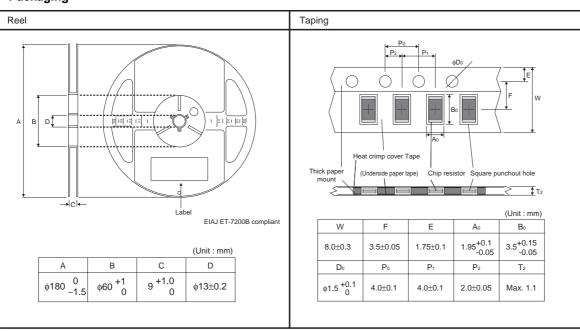
Item	Guaranteed value		Test conditions (JIS C 5201-1)	
пеш	Resistor type Jumper type			
Resistance	J: ±5% F: ±1%	Max. 50mΩ	JIS C 5201-1 4.5	
Variation of resistance with temperature	See Table.1		JIS C 5201-1 4.8 Measurement : +25 / +125°C	
Overload	± (2.0%+0.1Ω)	JIS C 5201-1 4.13 Max. 50mΩ Rated voltage (current Maximum overload vol		
Solderability	A new uniform coating of minimum of 95% of the surface being immersed and no soldering damage.		JIS C 5201-1 4.17 Rosin-Ethanol (25%WT) Soldering condition: 235±5°C Duration of immersion: 2.0±0.5s.	
Resistance to soldering heat	$\begin{array}{c c} \pm (1.0\% + 0.05\Omega) & \text{Max. } 50\text{m}\Omega \\ \text{No remarkable abnormality on the appearance.} \end{array}$		JIS C 5201-1 4.18 Soldering condition : 260±5°C Duration of immersion : 10±1s.	
Rapid change of temperature	\pm (1.0%+0.05Ω) Max. 50mΩ		JIS C 5201-1 4.19 Test temp. : –55°C to +125°C 5cyc	
Damp heat, steady state	± (3.0%+0.1Ω)	Max. 100mΩ	JIS C 5201-1 4.24 40°C, 93%RH Test time : 1,000h to 1,048h	
Endurance at 70°C	± (3.0%+0.1Ω)	Max. 100mΩ	JIS C 5201-1 4.25.1 Rated voltage (current), 70°C 1.5h : ON – 0.5h : OFF Test time : 1,000h to 1,048h	
Endurance	± (3.0%+0.1Ω)	Max. 100mΩ	JIS C 5201-1 4.25.3 155°C Test time : 1,000h to 1,048h	
Resistance to solvent	\pm (1.0%+0.05 Ω) Max. 50m Ω		JIS C 5201-1 4.29 23±5°C, Immersion cleaning, 5±0.5mir Solvent : 2-propanol	
Bend strength of the end face plating	$\begin{array}{c c} \pm \mbox{(1.0\%+0.05$\Omega)} & \mbox{Max. 50m}\Omega \\ & \mbox{Without mechanical damage such as breaks.} \end{array}$		JIS C 5201-1 4.33	

TRR18 Data Sheet

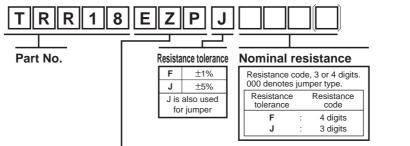
●Dimensions (Unit: mm)



Packaging



●Part No. Explanation



Packaging Specifications Code

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Part No.	Code	J(±5%)	F(±1%)	Packaging specifications	Packaging specifications Reel	Basic ordering unit (pcs)
TRR18	EZP	0	0	Paper tape (4mm Pitch)	φ180mm (7inch)	5,000

Reel (\phi180) : JEITA ET-7200B : Standard product

Notes

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Examples of application circuits, circuit constants and any other information contained herein illustrate the standard usage and operations of the Products. The peripheral conditions must be taken into account when designing circuits for mass production.

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