Anti-surge thick film chip resistor

ESR25 (1210 size: 1 / 2W)

Features

- 1) Power rating of 1 /2W (MCR25 1/4W)
- 2) Superior anti surge to MCR series
- 3) Highly reliable chip resistor
 - Ruthenium oxide dielectric offers superior resistance to the elements.
- 4) ROHM resistors have approved ISO–9001, ISO/TS 16949 certification. Design and specifications are subject to change without notice. Carefully check the specification sheet before using or ordering it.
- 5) This product is in compliance with the RoHS directive.

Applications

Automotive, LCD Monitor, projector, power supply, charger, inverter and so on.

●Ratings

Item	Conditions	Specifications		
Rated power	Power must be derated according to the power derating curve in Figure 1 when ambient temperature exceeds 70°C. **Botomorphisms** **Botomorphisms	0.5W (1/2W) at 70°C		
Rated voltage	The voltage rating is calculated by the following equation. If the value obtained exceeds the limiting element voltage, the voltage rating is equal to the maximum operating voltage. E: Rated voltage (V)			
	$E=\sqrt{P\times R}$ P: Rated power (W)			
	R: Nominal resistance (Ω)	Limiting element voltage	200V	
Nominal resistance	See <u>Table 1</u> .			
Operating temperature		−55°C to +155°C		

Table 1

Resistance tolerance	Resistance range (Ω)	Resistance temperature coefficient (ppm/°C)	
D (±0.5%)	10 ≤ R ≤ 1M (E24)	±100	
F (±1%)	1 ≤ R ≤ 10M (E24)	±100	
J (±5%)	1 ≤ R ≤ 10M (E24)	±200	

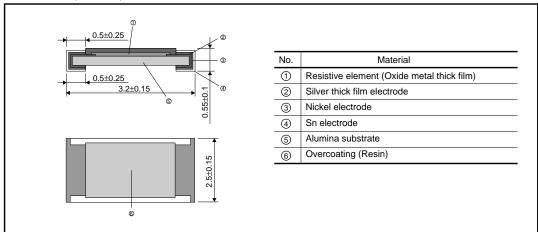
•Before using components in circuits where they will be exposed to transients such as pulse loads (short–duration, high–level loads), be certain to evaluate the component in the mounted state. In addition, the reliability and performance of this component cannot be guaranteed if it is used with a steady state voltage that is greater than its rated voltage.

Characteristics

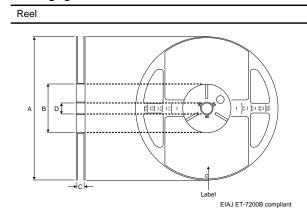
Item	Guaranteed value	Test conditions (JIS C 5201-1)	
Kom	Resistor type		
Resistance	J : ±5% F : ±1% D : ±0.5%	JIS C 5201-1 4.5	
Variation of resistance with temperature	See Table.1	JIS C 5201-1 4.8 Measurement : -55 / +25 / +125°C	
Overload	± (2.0%+0.1Ω)	JIS C 5201-1 4.13 Rated voltage (current) ×2.5, 2s. Maximum overload voltage : 400V	
Solderability	A new uniform coating of minimum of 95% of the surface being immersed and no soldering damage.	JIS C 5201-1 4.17 Rosin-Ethanol (25%WT) Soldering condition: 235±5°C Duration of immersion: 2.0±0.5s. JIS C 5201-1 4.18 Soldering condition: 260±5°C Duration of immersion: 10±1s.	
Resistance to soldering heat	$\pm \ (\text{1.0\%+0.05}\Omega)$ No remarkable abnormality on the appearance.		
Rapid change of temperature	± (1.0%+0.05Ω)	JIS C 5201-1 4.19 Test temp. : –55°C to +125°C 5cyc	
Damp heat, steady state	± (3.0%+0.1Ω)	JIS C 5201-1 4.24 40°C, 93%RH Test time : 1,000h to 1,048h	
Endurance at 70°C	± (3.0%+0.1Ω)	JIS C 5201-1 4.25.1 Rated voltage (current), 70°C 1.5h : ON – 0.5h : OFF Test time : 1,000h to 1,048h	
Endurance	± (3.0%+0.1Ω)	JIS C 5201-1 4.25.3 155°C Test time : 1,000h to 1,048h	
Resistance to solvent	± (1.0%+0.05Ω)	JIS C 5201-1 4.29 23±5°C, Immersion cleaning, 5±0.5min Solvent : 2-propanol	
Bend strength of the end face plating	$\pm (1.0\% + 0.05 \Omega)$ Without mechanical damage such as breaks.	JIS C 5201-1 4.33	
Static electric characteristics	± (5.0%+0.05Ω)	EIAJ ED-4701 1300 Test method 304 Voltage : $3kv$ R : $1.5k\Omega$ C : $100pF$ Apply cycle : 1 time	



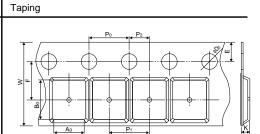
● **Dimensions** (Unit: mm)



●Packaging

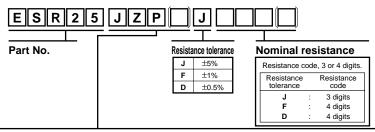


			(Unit: mm)	
Α	В	С	D	
φ180 0 -1.5	φ60 ⁺¹ 0	9 +1.0	φ13±0.2	



				(Unit : mm)	
W	F	Е	A ₀	B ₀	
8.0±0.3	3.5±0.05	1.75±0.1	3.0±0.1	3.5±0.1	
D ₀	P ₀	P ₁	P ₂	K	
φ1.5 ^{+0.1} ₀	4.0±0.1	4.0±0.1	2.0±0.05	Max. 1.1	

● Part No. Explanation



Packaging Specifications Code

- 1	Part No. Code	Resistance tolerance		rance	Desire es estimations	Deel	Dania andania a unit(a an)	
ı		Code	J(±5%)	F(±1%)	D(±0.5%)	Packaging specifications	Reel	Basic ordering unit(pcs)
ſ	ESR25	JZP	0	0	0	Embossed tape (4mm Pitch)	φ180mm (7inch)	4,000

Reel (\(\phi\)180mm): Compatible with JEITA standard "EIAJ ET-7200B" (\(\hat{O}\): Standard product

Notes

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Appendix1-Rev2.0