

# KA78LXXA/KA78L05AA

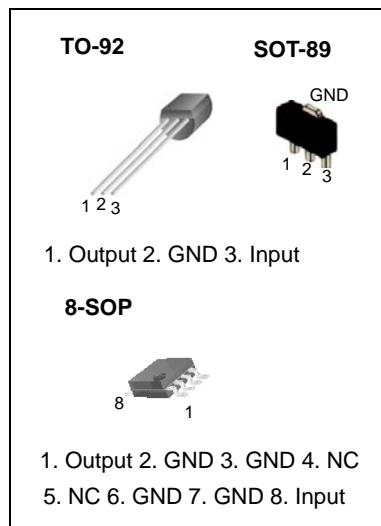
## 3-Terminal 0.1A Positive Voltage Regulator

### Features

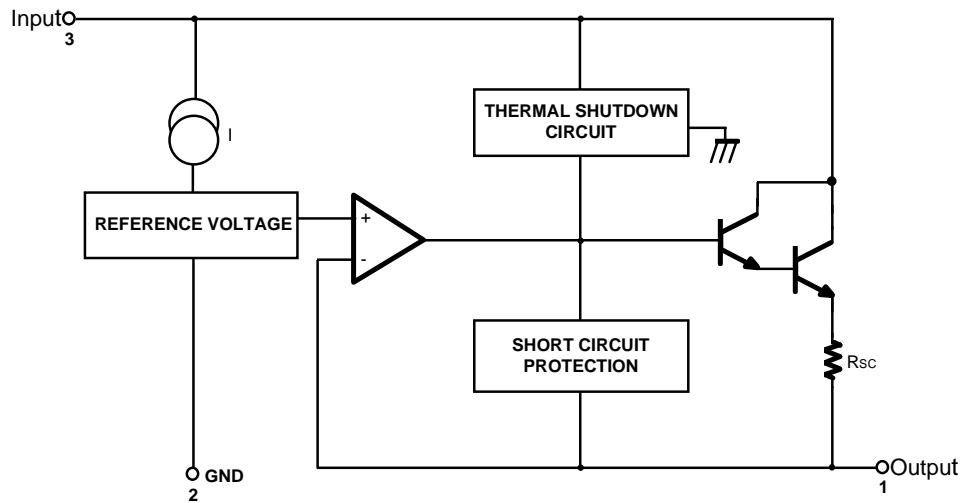
- Maximum Output Current of 100mA
- Output Voltage of 5V, 6V, 8V, 9V, 10V, 12V, 15V, 18V and 24V
- Thermal Overload Protection
- Short Circuit Current Limiting
- Output Voltage Offered in  $\pm 5\%$  Tolerance

### Description

The KA78LXXA/KA78L05AA series of fixed voltage monolithic integrated circuit voltage regulators are suitable for application that required supply current up to 100mA.



### Internal Block Diagram



## Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Input Voltage (for $V_O = 5V, 8V$ ) (for $V_O = 12V$ to $18V$ ) (for $V_O = 24V$ )	$V_I$	30 35 40	V
Operating Junction Temperature Range	$T_J$	0 ~ +150	°C
Storage Temperature Range	$T_{STG}$	-65 ~ +150	°C
Thermal Resistance Junction-Case TO-92	$R_{\theta JC}$	50	°C/W
Thermal Resistance Junction-Air TO-92 SOT-89 8-SOP	$R_{\theta JA}$	150 225 160	°C/W °C/W °C/W

## Electrical Characteristics(KA78L05A)

( $V_I = 10V$ ,  $I_O = 40mA$ ,  $0^{\circ}C \leq T_J \leq 125^{\circ}C$ ,  $C_I = 0.33\mu F$ ,  $C_O = 0.1\mu F$ , unless otherwise specified. (Note1)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
Output Voltage	$V_O$	$T_J = 25^{\circ}C$		4.8	5.0	5.2	V
Line Regulation (Note1)	$\Delta V_O$	$T_J = 25^{\circ}C$	$7V \leq V_I \leq 20V$	-	8	150	mV
			$8V \leq V_I \leq 20V$	-	6	100	mV
Load Regulation (Note1)	$\Delta V_O$	$T_J = 25^{\circ}C$	$1mA \leq I_O \leq 100mA$	-	11	60	mV
			$1mA \leq I_O \leq 40mA$	-	5.0	30	mV
Output Voltage	$V_O$	$7V \leq V_I \leq 20V$	$1mA \leq I_O \leq 40mA$	-	-	5.25	V
		$7V \leq V_I \leq V_{MAX}$ (Note2)	$1mA \leq I_O \leq 70mA$	4.75	-	5.25	V
Quiescent Current	$I_Q$	$T_J = 25^{\circ}C$		-	2.0	5.5	mA
Quiescent Current Change	With Line	$\Delta I_Q$	$8V \leq V_I \leq 20V$	-	-	1.5	mA
	With Load	$\Delta I_Q$	$1mA \leq I_O \leq 40 mA$	-	-	0.1	mA
Output Noise Voltage	$V_N$	$T_A = 25^{\circ}C$ , $10Hz \leq f \leq 100kHz$		-	40	-	$\mu V/V_o$
Temperature Coefficient of $V_O$	$\Delta V_O/\Delta T$	$I_O = 5mA$		-	-0.65	-	$mV/^{\circ}C$
Ripple Rejection	$RR$	$f = 120Hz$ , $8V \leq V_I \leq 18V$ , $T_J = 25^{\circ}C$		41	80	-	dB
Dropout Voltage	$V_D$	$T_J = 25^{\circ}C$		-	1.7	-	V

### Note:

1. The maximum steady state usable output current and input voltage are very dependent on the heat sinking and/or lead length of the package. The data above represent pulse test conditions with junction temperature as indicated at the initiation of tests.
2. Power dissipation  $P_D \leq 0.75W$ .

**Electrical Characteristics(KA78L06A)** (Continued)

(VI = 12V, IO = 40mA, 0°C ≤ TJ ≤ 125°C, CI = 0.33μF, CO = 0.1μF, unless otherwise specified. (Note 1)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
Output Voltage	VO	TJ = 25°C		5.75	6.0	6.25	V
Line Regulation (Note1)	ΔVO	TJ = 25°C	8.5V ≤ VI ≤ 20V	-	64	175	mV
			9V ≤ VI ≤ 20V	-	54	125	mV
Load Regulation (Note1)	ΔVO	TJ = 25°C	1mA ≤ IO ≤ 100mA	-	12.8	80	mV
			1mA ≤ IO ≤ 70mA	-	5.8	40	mV
Output Voltage	VO	8.5 ≤ VI ≤ 20V, 1mA ≤ IO ≤ 40mA		5.7	-	6.3	V
		8.5 ≤ VI ≤ VMAX(Note), 1mA ≤ IO ≤ 70mA		5.7	-	6.3	V
Quiescent Current	IQ	TJ = 25°C		-	-	5.5	mA
		TJ = 125°C		-	3.9	6.0	mA
Quiescent Current Change	With Line	ΔIQ	9 ≤ VI ≤ 20V		-	-	1.5 mA
	With Load	ΔIQ	1mA ≤ IO ≤ 40mA		-	-	0.1 mA
Output Noise Voltage		VN	TA = 25°C, 10Hz ≤ f ≤ 100kHz		-	40	- μV/Vo
Temperature Coefficient of VO		ΔVO/ΔT	IO = 5mA		-	0.75	- mV/°C
Ripple Rejection		RR	f = 120Hz, 10V ≤ VI ≤ 20V, TJ = 25°C		40	46	- dB
Dropout Voltage		VD	TJ = 25°C		-	1.7	- V

**Note:**

1. The maximum steady state usable output current and input voltage are very dependent on the heat sinking and/or lead length of the package. The data above represent pulse test conditions with junction temperature as indicated at the initiation of tests.
2. Power dissipation PD ≤ 0.75W.

**Electrical Characteristics(KA78L08A)** (Continued)

(VI = 14V, IO = 40mA, 0°C ≤ TJ ≤ 125°C, CI = 0.33µF, CO = 0.1µF, unless otherwise specified. (Note1)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
Output Voltage	VO	TJ = 25°C		7.7	8.0	8.3	V
Line Regulation (Note1)	ΔVO	TJ = 25°C	10.5V ≤ VI ≤ 23V	-	10	175	mV
			11V ≤ VI ≤ 23V	-	8	125	mV
Load Regulation (Note1)	ΔVO	TJ = 25°C	1mA ≤ IO ≤ 100mA	-	15	80	mV
			1mA ≤ IO ≤ 40mA	-	8.0	40	mV
Output Voltage	VO	10.5V ≤ VI ≤ 23V	1mA ≤ IO ≤ 40mA	7.6	-	8.4	V
		10.5V ≤ VI ≤ VMAX (Note2)	1mA ≤ IO ≤ 70mA	7.6	-	8.4	V
Quiescent Current	IQ	TJ = 25°C		-	2.0	5.5	mA
Quiescent Current Change	With Line	ΔIQ	11V ≤ VI ≤ 23V		-	-	1.5 mA
	With Load	ΔIQ	1mA ≤ IO ≤ 40mA		-	-	0.1 mA
Output Noise Voltage	VN	TA = 25°C, 10Hz ≤ f ≤ 100kHz		-	60	-	µV/Vo
Temperature Coefficient of VO	ΔVO/ΔT	IO = 5mA		-	-0.8	-	mV/°C
Ripple Rejection	RR	f = 120Hz, 11V ≤ VI ≤ 21V, TJ = 25°C		39	70	-	dB
Dropout Voltage	VD	TJ = 25°C		-	1.7	-	V

**Note:**

1. The maximum steady state usable output current and input voltage are very dependent on the heat sinking and/or lead length of the package. The data above represent pulse test conditions with junction temperature as indicated at the initiation of tests.
2. Power dissipation PD ≤ 0.75W.

**Electrical Characteristics(KA78L09A)** (Continued)

(VI = 15V, IO = 40mA, 0°C ≤ TJ ≤ 125°C, CI = 0.33µF, CO = 0.1µF, unless otherwise specified. (Note1)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
Output Voltage	VO	TJ = 25°C		8.64	9.0	9.36	V
Line Regulation (Note1)	ΔVO	TJ = 25°C	11.5V ≤ VI ≤ 24V	-	90	200	mV
			13V ≤ VI ≤ 24V	-	100	150	mV
Load Regulation (Note1)	ΔVO	TJ = 25°C	1mA ≤ IO ≤ 100mA	-	20	90	mV
			1mA ≤ IO ≤ 40mA	-	10	45	mV
Output Voltage	VO	11.5V ≤ VI ≤ 24V	1mA ≤ IO ≤ 40mA	8.55	-	9.45	V
		11.5V ≤ VI ≤ VMAX (Note 2)	1mA ≤ IO ≤ 70mA	8.55	-	9.45	V
Quiescent Current	IQ	TJ = 25°C		-	2.1	6.0	mA
Quiescent Current Change	With Line	ΔIQ	13V ≤ VI ≤ 24V		-	-	1.5 mA
	With Load	ΔIQ	1mA ≤ IO ≤ 40mA		-	-	0.1 mA
Output Noise Voltage	VN	TA = 25°C, 10Hz ≤ f ≤ 100kHz		-	70	-	µV/Vo
Temperature Coefficient of VO	ΔVO/ΔT	IO = 5mA		-	-0.9	-	mV/°C
Ripple Rejection	RR	f = 120Hz, 12V ≤ VI ≤ 22V, TJ = 25°C		38	44	-	dB
Dropout Voltage	VD	TJ = 25°C		-	1.7	-	V

**Note:**

1. The maximum steady state usable output current and input voltage are very dependent on the heat sinking and/or lead length of the package. The data above represent pulse test conditions with junction temperature as indicated at the initiation of tests.
2. Power dissipation PD ≤ 0.75W.

**Electrical Characteristics(KA78L10A)** (Continued)

(VI = 16V, IO = 40mA, 0 °C ≤ TJ ≤ 125 °C, CI = 0.33 µF, CO = 0.1µF, unless otherwise specified. (Note1)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
Output Voltage	VO	TJ = 25°C		9.6	10.0	10.4	V
Line Regulation (Note1)	ΔVO	TJ = 25°C	12.5 ≤ VI ≤ 25V	-	100	220	mV
			14V ≤ VI ≤ 25V	-	100	170	mV
Load Regulation (Note1)	ΔVO	TJ = 25°C	1mA ≤ IO ≤ 100mA	-	20	94	mV
			1mA ≤ IO ≤ 70mA	-	10	47	mV
Output Voltage	VO	12.5V ≤ VI ≤ 25V, 1mA ≤ IO ≤ 40mA		9.5	-	10.5	V
		12.5V ≤ VI ≤ VMAX (Note2) 1mA ≤ IO ≤ 70mA		9.5	-	10.5	
Quiescent Current	IQ	TJ = 25°C		-	-	6.0	mA
		TJ = 125°C		-	4.2	6.5	
Quiescent Current Change	With Line	ΔIQ	12.5 ≤ VI ≤ 25V	-	-	1.5	mA
	With Load	ΔIQ	1mA ≤ IO ≤ 40mA	-	-	0.1	mA
Output Noise Voltage		VN	TA = 25°C, 10Hz ≤ f ≤ 100kHz		-	74	-
Temperature Coefficient of VO		ΔVO/ΔT	IO = 5mA		-	0.95	-
Ripple Rejection		RR	f = 120Hz, 15V ≤ VI ≤ 25V, TJ = 25°C		38	43	-
Dropout Voltage		VD	TJ = 25°C		-	1.7	-

**Notes:**

1. The maximum steady state usable output current and input voltage are very dependent on the heat sinking and/or lead length of the package. The data above represent pulse test conditions with junction temperature as indicated at the initiation of tests.
2. Power dissipation PD ≤ 0.75W.

**Electrical Characteristics(KA78L12A)** (Continued)

(VI = 19V, IO = 40mA, 0°C ≤ TJ ≤ 125°C, CI = 0.33 µF, CO = 0.1µF, unless otherwise specified. (Note 1)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
Output Voltage	VO	TJ = 25°C		11.5	12	12.5	V
Line Regulation (Note1)	ΔVO	TJ = 25°C	14.5V ≤ VI ≤ 27V	-	20	250	mV
			16V ≤ VI ≤ 27V	-	15	200	mV
Load Regulation (Note1)	ΔVO	TJ = 25°C	1mA ≤ IO ≤ 100mA	-	20	100	mV
			1mA ≤ IO ≤ 40mA	-	10	50	mV
Output Voltage	VO	14.5V ≤ VI ≤ 27V	1mA ≤ IO ≤ 40mA	11.4	-	12.6	V
		14.5V ≤ VI ≤ VMAX (Note 2)	1mA ≤ IO ≤ 70mA	11.4	-	12.6	V
Quiescent Current	IQ	TJ = 25°C		-	2.1	6.0	mA
Quiescent Current Change	With Line	ΔIQ	16V ≤ VI ≤ 27V	-	-	1.5	mA
	With Load	ΔIQ	1mA ≤ IO ≤ 40mA	-	-	0.1	mA
Output Noise Voltage	VN	TA = 25°C, 10Hz ≤ f ≤ 100kHz		-	80	-	µV/Vo
Temperature Coefficient of VO	ΔVO/ΔT	IO = 5mA		-	-1.0	-	mV/°C
Ripple Rejection	RR	f = 120Hz, 15V ≤ VI ≤ 25V, TJ = 25°C		37	65	-	dB
Dropout Voltage	VD	TJ = 25°C		-	1.7	-	V

**Note:**

1. The maximum steady state usable output current and input voltage are very dependent on the heat sinking and/or lead length of the package. The data above represent pulse test conditions with junction temperature as indicated at the initiation of tests.
2. Power dissipation PD ≤ 0.75W.

**Electrical Characteristics(KA78L15A)** (Continued)

(VI = 23V, IO = 40mA, 0°C ≤ TJ ≤ 125°C, CI = 0.33 µF, CO = 0.1µF, unless otherwise specified. (Note1)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
Output Voltage	VO	TJ = 25°C		14.4	15	15.6	V
Line Regulation (Note1)	ΔVO	TJ = 25°C	17.5V ≤ VI ≤ 30V	-	25	300	mV
			20V ≤ VI ≤ 30V	-	20	250	mV
Load Regulation (Note1)	ΔVO	TJ = 25°C	1mA ≤ IO ≤ 100mA	-	25	150	mV
			1mA ≤ IO ≤ 40mA	-	12	75	mV
Output Voltage	VO	17.5V ≤ VI ≤ 30V	1mA ≤ IO ≤ 40mA	14.2 5	-	15.7 5	V
		17.5V ≤ VI ≤ VMAX (Note 2)	1mA ≤ IO ≤ 70mA	14.2 5	-	15.7 5	V
Quiescent Current	IQ	TJ = 25°C		-	2.1	6.0	mA
Quiescent Current Change	With Line	ΔIQ	20V ≤ VI ≤ 30V	-	-	1.5	mA
	With Load	ΔIQ	1mA ≤ IO ≤ 40mA	-	-	0.1	mA
Output Noise Voltage	VN	TA = 25°C, 10Hz ≤ f ≤ 100kHz		-	90	-	µV/Vo
Temperature Coefficient of VO	ΔVO/ΔT	IO = 5mA		-	-1.3	-	mV/°C
Ripple Rejection	RR	f = 120Hz, 18.5V ≤ VI ≤ 28.5V, TJ = 25°C		34	60	-	dB
Dropout Voltage	VD	TJ = 25°C		-	1.7	-	V

**Note:**

1. The maximum steady state usable output current and input voltage are very dependent on the heat sinking and/or lead length of the package. The data above represent pulse test conditions with junction temperature as indicated at the initiation of tests.
2. Power dissipation PD ≤ 0.75W.

**Electrical Characteristics(KA78L18A) (Continued)**

(VI = 27V, IO = 40mA, 0°C ≤ TJ ≤ 125°C, CI = 0.33 µF, CO = 0.1µF, unless otherwise specified. (Note1)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
Output Voltage	VO	TJ = 25°C		17.3	18	18.7	V
Line Regulation (Note1)	ΔVO	TJ = 25°C	21V ≤ VI ≤ 33V	-	145	300	mV
			22V ≤ VI ≤ 33V	-	135	250	mV
Load Regulation (Note1)	ΔVO	TJ = 25°C	1mA ≤ IO≤100mA	-	30	170	mV
			1mA ≤ IO ≤ 40mA	-	15	85	mV
Output Voltage	VO	21V ≤ VI ≤ 33V	1mA ≤ IO ≤ 40mA	17.1	-	18.9	V
		21V ≤ VI ≤ VMAX (Note 2)	1mA ≤ IO ≤ 70mA	17.1	-	18.9	V
Quiescent Current	IQ	TJ = 25°C		-	2.2	6.0	mA
Quiescent Current Change	With Line	ΔIQ	21V ≤ VI ≤ 33V	-	-	1.5	mA
	With Load	ΔIQ	1mA ≤ IO ≤ 40mA	-	-	0.1	mA
Output Noise Voltage	VN	TA = 25°C, 10Hz ≤ f ≤ 100kHz		-	150	-	µV/Vo
Temperature Coefficient of VO	ΔVO/ΔT	IO = 5mA		-	-1.8	-	mV/°C
Ripple Rejection	RR	f = 120Hz, 23V ≤ VI ≤ 33V, TJ = 25°C		34	48	-	dB
Dropout Voltage	VD	TJ = 25°C		-	1.7	-	V

**Note:**

1. The maximum steady state usable output current and input voltage are very dependent on the heat sinking and/or lead length of the package. The data above represent pulse test conditions with junction temperature as indicated at the initiation of tests.
2. Power dissipation PD ≤ 0.75W.

**Electrical Characteristics(KA78L24A) (Continued)**

(VI = 33V, IO = 40mA, 0°C ≤ TJ ≤ 125°C, CI = 0.33 µF, CO = 0.1µF, unless otherwise specified. (Note1)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
Output Voltage	VO	TJ = 25°C		23	24	25	V
Line Regulation (Note1)	ΔVO	TJ = 25°C	27V ≤ VI ≤ 38V	-	160	300	mV
			28V ≤ VI ≤ 38V	-	150	250	mV
Load Regulation (Note1)	ΔVO	TJ = 25°C	1mA ≤ IO ≤ 100mA	-	40	200	mV
			1mA ≤ IO ≤ 40mA	-	20	100	mV
Output Voltage	VO	27V ≤ VI ≤ 38V	1mA ≤ IO ≤ 40mA	22.8	-	25.2	V
		27V ≤ VI ≤ VMAX (Note2)	1mA ≤ IO ≤ 70mA	22.8	-	25.2	V
Quiescent Current	IQ	TJ = 25°C		-	2.2	6.0	mA
Quiescent Current Change	With Line	ΔIQ	28V ≤ VI ≤ 38V	-	-	1.5	mA
	With Load	ΔIQ	1mA ≤ IO ≤ 40mA	-	-	0.1	mA
Output Noise Voltage	VN	TA = 25°C, 10Hz ≤ f ≤ 100kHz		-	200	-	µV/Vo
Temperature Coefficient of VO	ΔVO/ΔT	IO = 5mA		-	-2.0	-	mV/°C
Ripple Rejection	RR	f = 120Hz, 28V ≤ VI ≤ 38V, TJ = 25°C		34	45	-	dB
Dropout Voltage	VD	TJ = 25°C		-	1.7	-	V

**Note:**

1. The maximum steady state usable output current and input voltage are very dependent on the heat sinking and/or lead length of the package. The data above represent pulse test conditions with junction temperature as indicated at the initiation of tests.
2. Power dissipation PD ≤ 0.75W.

## Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Input Voltage (for $V_O = 5V, 8V$ (for $V_O = 12V$ to $18V$ ) (for $V_O = 24V$ )	$V_I$	30	V
		35	V
		40	V
Operating Junction Temperature Range	$T_J$	0 ~ +150	°C
Storage Temperature Range	$T_{STG}$	-65 ~ +150	°C

## Electrical Characteristics(KA78L05AA) (Continued)

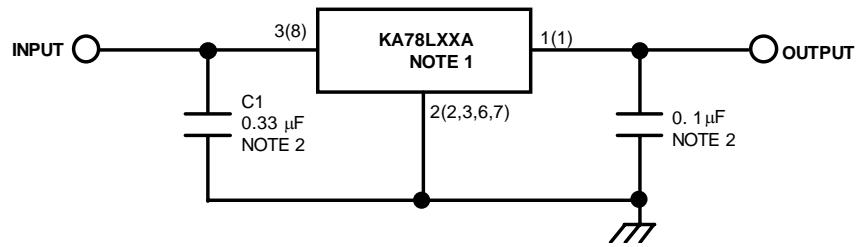
( $V_I = 10V$ ,  $I_O = 40mA$ ,  $0^\circ C \leq T_J \leq 125^\circ C$ ,  $C_I = 0.33 \mu F$ ,  $C_O = 0.1 \mu F$ , unless otherwise specified. (Note1)

Parameter	Symbol	Conditions		Min.	Typ.	Max.	Unit
Output Voltage	$V_O$	$T_J = 25^\circ C$		4.9	5.0	5.1	V
Line Regulation (Note1)	$\Delta V_O$	$T_J = 25^\circ C$	$7V \leq V_I \leq 20V$	-	8	150	mV
			$8V \leq V_I \leq 20V$	-	6	100	mV
Load Regulation (Note1)	$\Delta V_O$	$T_J = 25^\circ C$	$1mA \leq I_O \leq 100mA$	-	11	50	mV
			$1mA \leq I_O \leq 40mA$	-	5.0	25	mV
Output Voltage	$V_O$	$7V \leq V_I \leq 20V$	$1mA \leq I_O \leq 40mA$	-	-	5.15	V
		$7V \leq V_I \leq V_{MAX}$ (Note2)	$1mA \leq I_O \leq 70mA$	4.85	-	5.15	V
Quiescent Current	$I_Q$	$T_J = 25^\circ C$		-	2.0	5.5	mA
Quiescent Current Change	With Line	$\Delta I_Q$	$8V \leq V_I \leq 20V$	-	-	1.5	mA
	With Load	$\Delta I_Q$	$1mA \leq I_O \leq 40 mA$	-	-	0.1	mA
Output Noise Voltage	$V_N$	$T_A = 25^\circ C$ , $10Hz \leq f \leq 100kHz$		-	40	-	$\mu V/V_O$
Temperature Coefficient of $V_O$	$\Delta V_O/\Delta T$	$I_O = 5mA$		-	-0.65	-	$mV/^\circ C$
Ripple Rejection	$RR$	$f = 120Hz$ , $8V \leq V_I \leq 18V$ , $T_J = 25^\circ C$		41	80	-	dB
Dropout Voltage	$V_D$	$T_J = 25^\circ C$		-	1.7	-	V

### Note:

- The maximum steady state usable output current and input voltage are very dependent on the heat sinking and/or lead length of the package. The data above represent pulse test conditions with junction temperature as indicated at the initiation of tests.
- Power dissipation  $P_D \leq 0.75W$ .

## Typical Application



'( )' : 8SOP Type

**Note:**

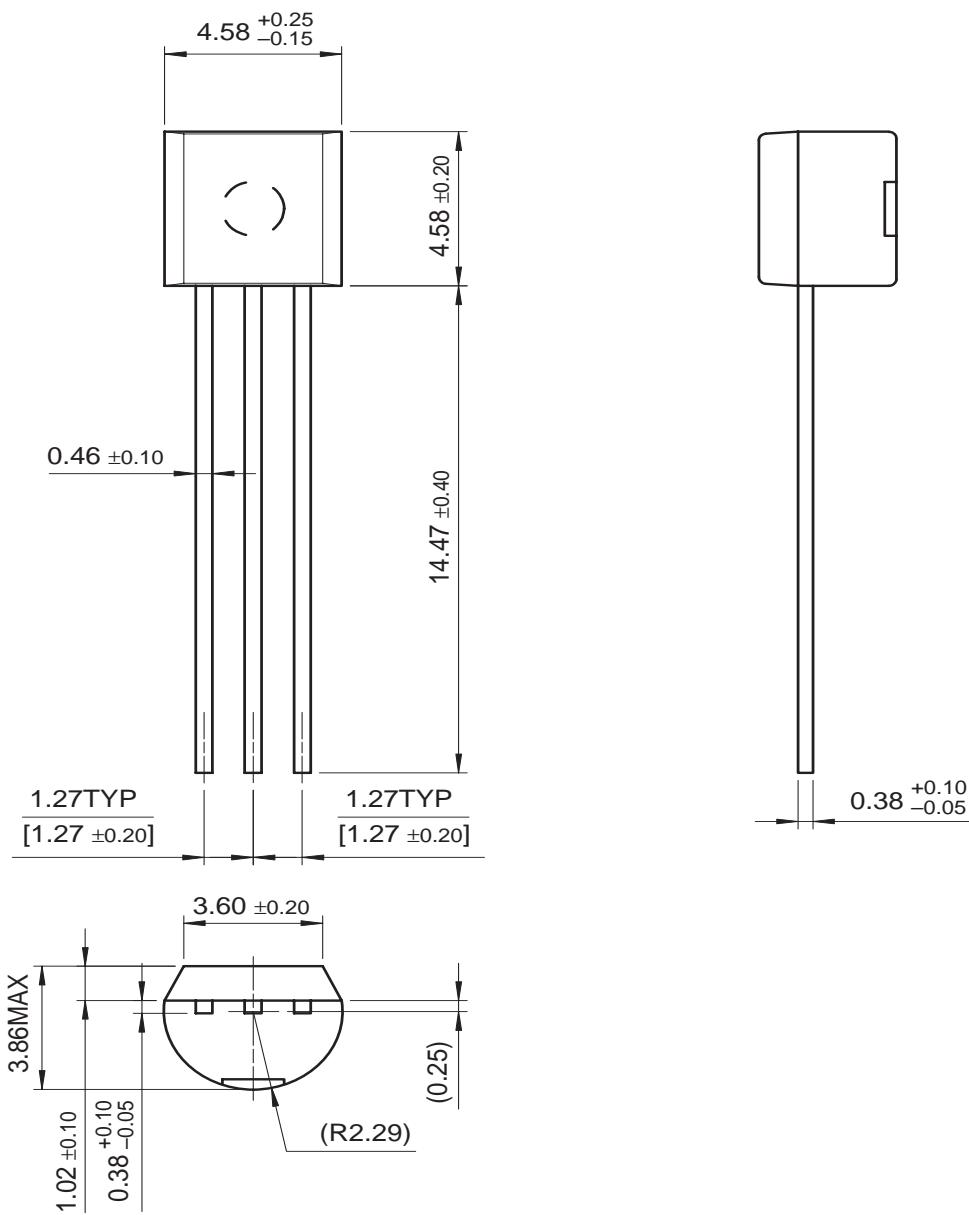
1. To specify an output voltage, substitute voltage value for "XX".
2. Bypass Capacitors are recommended for optimum stability and transient response and should be located as close as possible to the regulator

## Mechanical Dimensions

### Package

Dimensions in millimeters

### TO-92

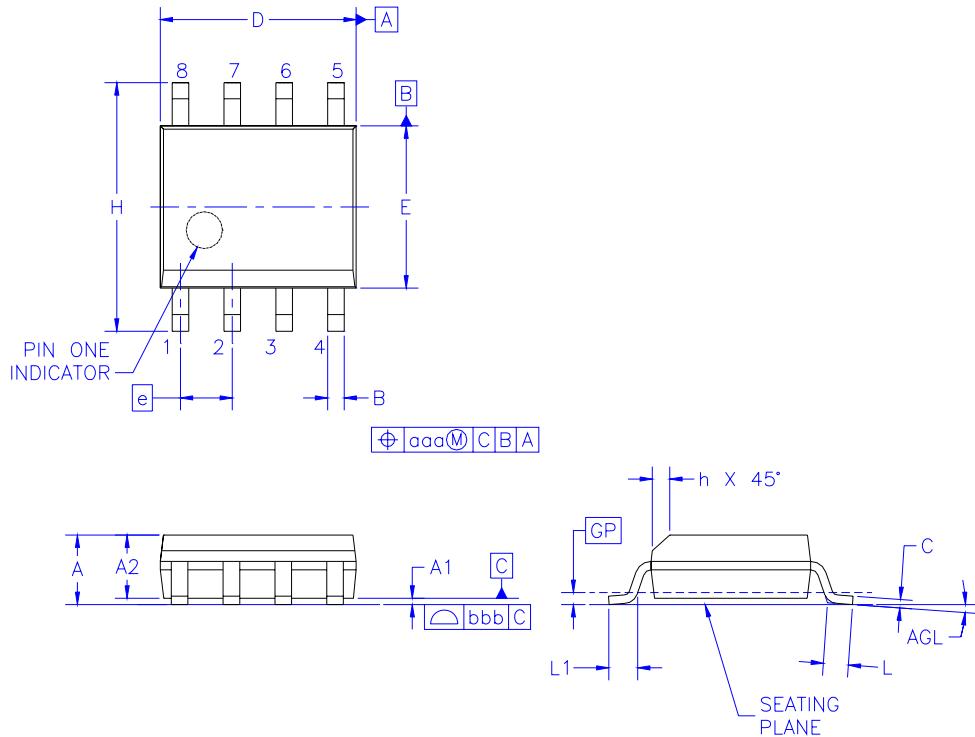


## Mechanical Dimensions (Continued)

### Package

Dimensions in millimeters

### 8-SOP



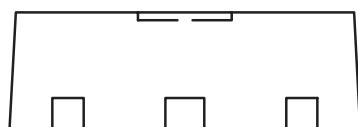
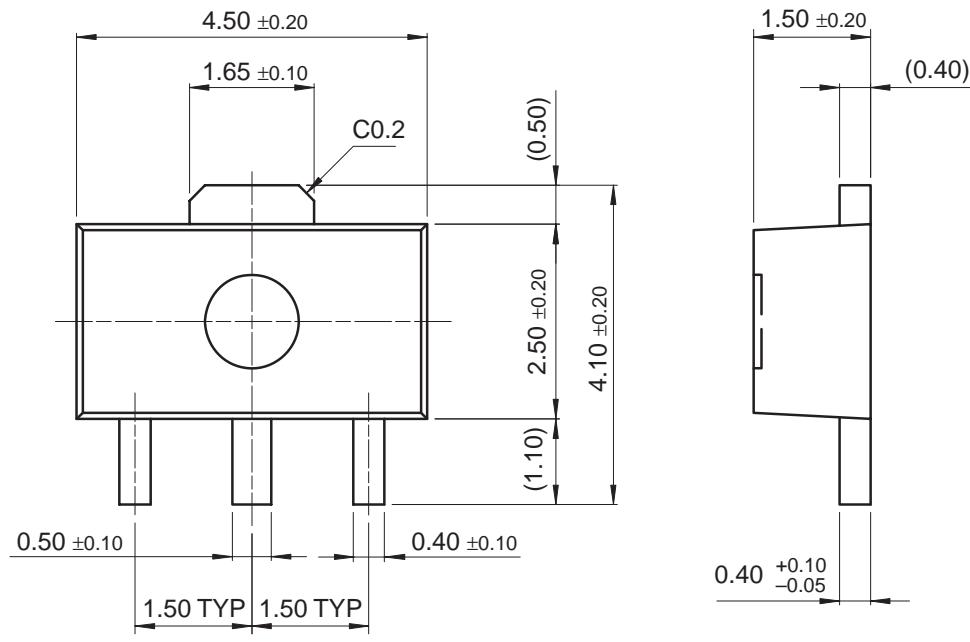
Symbol	Min	Nom	Max
A	-	-	1.75
A1	0.10	0.15	0.25
A2	1.25	1.45	1.50
B	0.35	0.37	0.51
C	0.19	0.20	0.25
D	4.80	4.90	5.00
E	3.80	3.90	4.00
e	1.27BSC		
H	5.79	5.99	6.20
h	0.25	-	0.50
L	0.50	0.70	0.90
GP	0.36 BSC		
q	0	-	8
aaa	-	-	0.25
bbb	-	-	0.10

## Mechanical Dimensions (Continued)

### Package

Dimensions in millimeters

### SOT-89



## Ordering Information

Product Number	Package	Shipping	Output Voltage Tolerance	Operating Temperature
KA78L05AZTA	TO-92	Ammo	5%	0 ~ +125 °C
KA78L05AZTF		Tape & Reel		
KA78L05AZBU		Bulk		
KA78L06AZTA		Ammo		
KA78L06AZBU		Bulk		
KA78L08AZTA		Ammo		
KA78L08AZTF		Tape & Reel		
KA78L08AZBU		Bulk		
KA78L09AZTA		Ammo		
KA78L09AZBU		Bulk		
KA78L10AZTA		Ammo		
KA78L10AZBU		Bulk		
KA78L12AZTA		Ammo		
KA78L12AZTF		Tape & Reel		
KA78L12AZBU		Bulk		
KA78L15AZTA		Ammo		
KA78L15AZBU		Bulk		
KA78L18AZTA		Ammo		
KA78L18AZBU		Bulk		
KA78L24AZTA		Ammo	2%	
KA78L24AZBU		Bulk		
KA78L05AMTF	SOT-89	Tape & Reel	5%	0 ~ +125 °C
KA78L05AMTM		Tape & Reel		
KA78L08AMTF		Tape & Reel		
KA78L09AMTF		Tape & Reel		
KA78L12AMTF		Tape & Reel		
KA78L05AD	8-SOP	Tube	5%	0 ~ +125 °C
KA78L05ADTF		Tape & Reel		
KA78L08AD		Tube		
KA78L08ADTF		Tape & Reel		
KA78L12AD		Tube		
KA78L12ADTF		Tape & Reel		
KA78L05AAZTA	TO-92	Ammo		
KA78L05AAZBU		Bulk		

- For information on tape & reel and ammo pack specifications, including part orientation and tape sizes, please refer to our tape and reel data, [www.fairchildsemi.com/products/discrete/pdf/to92\\_tr.pdf](http://www.fairchildsemi.com/products/discrete/pdf/to92_tr.pdf).

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1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.