

LM120/LM320

Series 3-Terminal Negative Regulators

General Description

The LM120 series are three-terminal negative regulators with a fixed output voltage of –5V, –12V, and –15V, and up to 1.5A load current capability. Where other voltages are required, the LM137 and LM137HV series provide an output voltage range of –1.2V to –47V.

The LM120 need only one external component—a compensation capacitor at the output, making them easy to apply. Worst case guarantees on output voltage deviation due to any combination of line, load or temperature variation assure satisfactory system operation.

Exceptional effort has been made to make the LM120 Series immune to overload conditions. The regulators have current limiting which is independent of temperature, combined with thermal overload protection. Internal current limiting protects against momentary faults while thermal shutdown prevents junction temperatures from exceeding safe limits during prolonged overloads.

Although primarily intended for fixed output voltage applications, the LM120 Series may be programmed for higher output voltages with a simple resistive divider. The low quiescent drain current of the devices allows this technique to be used with good regulation.

Features

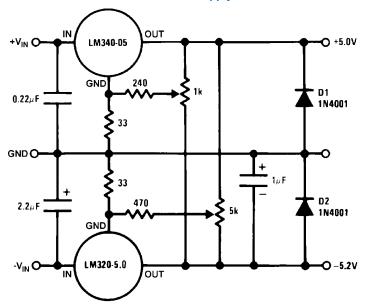
- Preset output voltage error less than ±3%
- Preset current limit
- Internal thermal shutdown
- Operates with input-output voltage differential down to 1V
- Excellent ripple rejection
- Low temperature drift
- Easily adjustable to higher output voltage

LM120 Series Packages and Power Capability

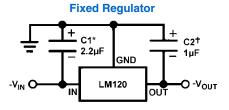
| | | Rated | Design |
|-------------|------------|-------------|---------|
| Device | Package | Power | Load |
| | | Dissipation | Current |
| LM120/LM320 | TO-3 (K) | 20W | 1.5A |
| | TO-39 (H) | 2W | 0.5A |
| LM320 | TO-220 (T) | 15W | 1.5A |

Typical Applications

Dual Trimmed Supply



77670



*Required if regulator is separated from filter capacitor by more than 3 inches. For value given, capacitor must be solid tantalum. 25 μF aluminum electrolytic may be substituted.

†Required for stability. For value given, capacitor must be solid tantalum. 25 μF aluminum electrolytic may be substituted. Values given may be increased without limit.

For output capacitance in excess of 100 μ F, a high current diode from input to output (1N4001, etc.) will protect the regulator from momentary input shorts

Absolute Maximum Ratings

-5 Volt Regulators (Note 5, Note 3)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Power Dissipation Internally Limited Input Voltage -25V Input-Output Voltage Differential 25V Junction Temperatures (Note 1)
Storage Temperature Range -65°C to +150°C

Lead Temperature

(Soldering, 10 sec.) 300°C Plastic 260°C

LM120K-5.0 and LM320K-5.0 Electrical Characteristics (Note 3)

| | | Metal Can Package | | | | | | |
|---|--|------------------------------|-----|-------|------------|-----|-------|-------|
| | Order Numbers | LM120K-5.0 | | | LM320K-5.0 | | | 1 |
| Design Output Current (I_D) Device Dissipation (P_D) | | (TO-3) (TO-3) 1.5A 20W | | | | | | Units |
| Parameter | Conditions (Note 1) | Min | Тур | Max | Min | Тур | Max | |
| Output Voltage | $T_J = 25$ °C, $V_{IN} = 10V$, $I_{LOAD} = 5$ mA | -5.1 | -5 | -4.9 | -5.2 | -5 | -4.8 | V |
| Line Regulation | $T_J = 25^{\circ}C$, $I_{LOAD} = 5$ mA, $V_{MIN} \le V_{IN} \le V_{MAX}$ | | 10 | 25 | | 10 | 40 | mV |
| Input Voltage | Wild III WOOK | -25 | | -7 | -25 | | -7 | V |
| Ripple Rejection | f = 120 Hz | 54 | 64 | | 54 | 64 | | dB |
| Load Regulation, | $T_J = 25^{\circ}C, V_{IN} = 10V,$ | | 50 | 75 | | 60 | 100 | mV |
| (Note 2) | $5 \text{ mA} \le I_{LOAD} \le I_{D}$ | | | | | | | |
| Output Voltage, | $-7.5V \le V_{IN} \le V_{MAX}$ | -5.20 | | -4.80 | -5.25 | | -4.75 | V |
| (Note 1) | $5 \text{ mA} \le I_{LOAD} \le I_D, P \le P_D$ | | | | | | | |
| Quiescent Current | $V_{MIN} \le V_{IN} \le V_{MAX}$ | | 1 | 2 | | 1 | 2 | mA |
| Quiescent Current | T _J = 25°C | | | | | | | |
| Change | $V_{MIN} \le V_{IN} \le V_{MAX}$ | | 0.1 | 0.4 | | 0.1 | 0.4 | mA |
| | $5 \text{ mA} \le I_{LOAD} \le I_{D}$ | | 0.1 | 0.4 | | 0.1 | 0.4 | mA |
| Output Noise Voltage | $T_A = 25^{\circ}C, C_L = 1 \mu F, I_L = 5 \text{ mA},$ | | 150 | | | 150 | | μV |
| | $V_{IN} = 10V, 10 \text{ Hz} \le f \le 100 \text{ kHz}$ | | | | | | | |
| Long Term Stability | | | 5 | 50 | | 5 | 50 | mV |
| Thermal Resistance | | | | | | | | |
| Junction to Case | | | | 3 | | | 3 | °C/W |
| Junction to Ambient | | | | 35 | | | 35 | °C/W |

LM120H-5.0 Electrical Characteristics (Note 3)

| Order Numbers Design Output Current (I _D) | | | Metal Can Package LM120H-5.0 | | | | |
|--|---|-------|---------------------------------|----------|-------|--|--|
| | | | (TO-39) | | | | |
| | | | 0.5A | | Units | | |
| | Device Dissipation (P _D) | | 2W | | | | |
| Parameter | Conditions (Note 1) | Min | Тур | Max | | | |
| Output Voltage | $T_J = 25^{\circ}C, V_{IN} = 10V,$ | -5.1 | - 5 | -4.9 | V | | |
| | $I_{LOAD} = 5 \text{ mA}$ | | | | | | |
| Line Regulation | $T_J = 25$ °C, $I_{LOAD} = 5$ mA, | | 10 | 25 | mV | | |
| | $V_{MIN} \le V_{IN} \le V_{MAX}$ | | | | | | |
| Input Voltage | | -25 | | -7 | V | | |
| Ripple Rejection | f = 120 Hz | 54 | 64 | | dB | | |
| Load Regulation, | $T_J = 25^{\circ}C, V_{IN} = 10V,$ | | 30 | 50 | mV | | |
| (Note 2) | $5 \text{ mA} \le I_{LOAD} \le I_{D}$ | | | | | | |
| Output Voltage, | $-7.5V \le V_{IN} \le V_{MAX}$ | -5.20 | | -4.80 | V | | |
| (Note 6) | $5 \text{ mA} \le I_{LOAD} \le I_D, P \le P_D$ | | | | | | |
| Quiescent Current | $V_{MIN} \le V_{IN} \le V_{MAX}$ | | 1 | 2 | mA | | |
| Quiescent Current | $T_J = 25^{\circ}C$ | | | | | | |
| Change | $V_{MIN} \le V_{IN} \le V_{MAX}$ | | 0.05 | 0.4 | mA | | |
| | $5 \text{ mA} \le I_{LOAD} \le I_{D}$ | | 0.04 | 0.4 | mA | | |
| Output Noise Voltage | $T_A = 25^{\circ}C, C_L = 1 \mu F, I_L = 5 \text{ mA},$ | | 150 | | μV | | |
| | $V_{IN} = 10V, 10 \text{ Hz} \le f \le 100 \text{ kHz}$ | | | | | | |
| Long Term Stability | | | 5 | | mV | | |
| Thermal Resistance | | | | | | | |
| Junction to Case | | | | (Note 4) | °C/W | | |
| Junction to Ambient | | | | (Note 4) | °C/W | | |

Note 1: This specification applies over $-55^{\circ}C \le T_{J} \le +150^{\circ}C$ for the LM120 and $0^{\circ}C \le T_{J} \le +125^{\circ}C$ for the LM320.

Note 2: Regulation is measured at constant junction temperature. Changes in output voltage due to heating effects must be taken into account separately. To ensure constant junction temperature, low duty cycle, pulse testing is used. The LM120/LM320 series does have low thermal feedback, improving line and load regulation. On all other tests, even though power dissipation is internally limited, electrical specifications apply only up to P_D.

Note 3: For -5V 3 amp regulators, see LM145 data sheet.

Note 4: Thermal resistance of typically 85°C/W (in 400 linear feet air flow), 224°C/W (in static air) junction to ambient, of typically 21°C/W junction to case.

Note 5: Refer to RETS120-5H drawing for LM120H-5.0 or RETS120-5K drawing for LM120-5K military specifications.

Absolute Maximum Ratings -12 Volt Regulators (*Note 9*)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Power Dissipation Internally Limited
Input Voltage -35V
Input-Output Voltage Differential 30V
Junction Temperatures (Note 6)
Storage Temperature Range -65°C to +150°C

Lead Temperature

(Soldering, 10 sec.) 300°C

LM120K-12 Electrical Characteristics

| | | Meta | Metal Can Package | | | | | |
|----------------------|---|-------|-------------------|-----------|------|--|--|--|
| | Order Numbers | | | LM120K-12 | | | | |
| | | | (TO-3) | | | | | |
| | Design Output Current (I _D) | | | | | | | |
| | Device Dissipation (P _D) | | 20W | 1 | | | | |
| Parameter | Conditions (Note 6) | Min | Тур | Max | | | | |
| Output Voltage | $T_{J} = 25^{\circ}C, V_{IN} = 17V,$ | -12.3 | -12 | -11.7 | V | | | |
| | $I_{LOAD} = 5 \text{ mA}$ | | | | | | | |
| Line Regulation | $T_J = 25^{\circ}C$, $I_{LOAD} = 5$ mA, | | 4 | 10 | mV | | | |
| | $V_{MIN} \le V_{IN} \le V_{MAX}$ | | | | | | | |
| Input Voltage | | -32 | | -14 | ٧ | | | |
| Ripple Rejection | f = 120 Hz | 56 | 80 | | dB | | | |
| Load Regulation, | $T_J = 25^{\circ}C, V_{IN} = 17V,$ | | 30 | 80 | mV | | | |
| (Note 7) | $5 \text{ mA} \le I_{LOAD} \le I_{D}$ | | | | | | | |
| Output Voltage, | $14.5V \le V_{IN} \le V_{MAX},$ | -12.5 | | -11.5 | V | | | |
| (Note 6) | $5 \text{ mA} \le I_{LOAD} \le I_D, P \le P_D$ | | | | | | | |
| Quiescent Current | $V_{MIN} \le V_{IN} \le V_{MAX}$ | | 2 | 4 | mA | | | |
| Quiescent Current | $T_J = 25^{\circ}C$ | | | | | | | |
| Change | $V_{MIN} \le V_{IN} \le V_{MAX}$ | | 0.1 | 0.4 | mA | | | |
| | $5 \text{ mA} \le I_{LOAD} \le I_{D}$ | | 0.1 | 0.4 | mA | | | |
| Output Noise Voltage | $T_A = 25^{\circ}C, C_L = 1 \mu F, I_L = 5 \text{ mA},$ | | 400 | | μV | | | |
| | $V_{IN} = 17V, 10 \text{ Hz} \le f \le 100 \text{ kHz}$ | | | | | | | |
| Long Term Stability | | | 12 | 120 | mV | | | |
| Thermal Resistance | | | | | | | | |
| Junction to Case | | | | 3 | °C/W | | | |
| Junction to Ambient | | | | 35 | °C/W | | | |

LM120H-12 Electrical Characteristics

| Order Numbers Design Output Current (I _D) | | M | | | |
|--|---|-------|------|----------|-------|
| | | | 0.2A | | Units |
| | Device Dissipation (P _D) | | 2W | | |
| Parameter | Conditions (Note 6) | Min | Тур | Max | |
| Output Voltage | $T_J = 25^{\circ}C, V_{IN} = 17V,$ | -12.3 | -12 | -11.7 | V |
| | $I_{LOAD} = 5 \text{ mA}$ | | | | |
| Line Regulation | $T_J = 25^{\circ}C$, $I_{LOAD} = 5$ mA, | | 4 | 10 | mV |
| | $V_{MIN} \le V_{IN} \le V_{MAX}$ | | | | |
| Input Voltage | | -32 | | -14 | V |
| Ripple Rejection | f = 120 Hz | 56 | 80 | | dB |
| Load Regulation, | $T_J = 25^{\circ}C, V_{IN} = 17V,$ | | 10 | 25 | mV |
| (Note 7) | $5 \text{ mA} \le I_{LOAD} \le I_{D}$ | | | | |
| Output Voltage, | $14.5V \le V_{IN} \le V_{MAX},$ | -12.5 | | -11.5 | V |
| (Note 6) | $5 \text{ mA} \le I_{\text{LOAD}} \le I_{\text{D}}, P \le P_{\text{D}}$ | | | | |
| Quiescent Current | $V_{MIN} \le V_{IN} \le V_{MAX}$ | | 2 | 4 | mA |
| Quiescent Current | T _J = 25°C | | | | |
| Change | $V_{MIN} \le V_{IN} \le V_{MAX}$ | | 0.05 | 0.4 | mA |
| | $5 \text{ mA} \le I_{\text{LOAD}} \le I_{\text{D}}$ | | 0.03 | 0.4 | mA |
| Output Noise Voltage | $T_A = 25^{\circ}C, C_L = 1 \mu F, I_L = 5 mA,$ | | 400 | | μV |
| | $V_{IN} = 17V$, 10 Hz $\leq f \leq 100 \text{ kHz}$ | | | | |
| Long Term Stability | | | 12 | 120 | mV |
| Thermal Resistance | | | | | |
| Junction to Case | | | | (Note 8) | °C/W |
| Junction to Ambient | | | | (Note 8) | °C/W |

LM320T-12 Electrical Characteristics

| | | Powe | Power Plastic Package | | | | |
|--|---|-------|-----------------------|-------|-------|--|--|
| Order Numbers Design Output Current (I _D) | | | LM320T-12 | | | | |
| | | | (TO-220) | | | | |
| | | | 1 A | | Units | | |
| | Device Dissipation (P _D) | | 15W | | | | |
| Parameter | Conditions (Note 6) | Min | Тур | Max | | | |
| Output Voltage | $T_J = 25^{\circ}C, V_{IN} = 17V,$ | -12.4 | -12 | -11.6 | V | | |
| | $I_{LOAD} = 5 \text{ mA}$ | | | | | | |
| Line Regulation | $T_J = 25$ °C, $I_{LOAD} = 5$ mA, | | 4 | 20 | mV | | |
| | $V_{MIN} \le V_{IN} \le V_{MAX}$ | | | | | | |
| Input Voltage | | -32 | | -14.5 | V | | |
| Ripple Rejection | f = 120 Hz | 56 | 80 | | dB | | |
| Load Regulation, | $T_J = 25^{\circ}C, V_{IN} = 17V,$ | | 30 | 80 | mV | | |
| (Note 7) | $5 \text{ mA} \le I_{\text{LOAD}} \le I_{\text{D}}$ | | | | | | |
| Output Voltage, | $14.5V \le V_{IN} \le V_{MAX},$ | -12.6 | | -11.4 | V | | |
| (Note 6) | $5 \text{ mA} \le I_{LOAD} \le I_D, P \le P_D$ | | | | | | |
| Quiescent Current | $V_{MIN} \le V_{IN} \le V_{MAX}$ | | 2 | 4 | mA | | |
| Quiescent Current | $T_J = 25^{\circ}C$ | | | | | | |
| Change | $V_{MIN} \le V_{IN} \le V_{MAX}$ | | 0.1 | 0.4 | mA | | |
| | $5 \text{ mA} \le I_{\text{LOAD}} \le I_{\text{D}}$ | | 0.1 | 0.4 | mA | | |
| Output Noise Voltage | $T_A = 25^{\circ}C, C_L = 1 \mu F, I_L = 5 \text{ mA},$ | | 400 | | μV | | |
| | V _{IN} = 17V, 10 Hz ≤ f ≤ 100 kHz | | | | | | |
| Long Term Stability | | | 24 | | mV | | |
| Thermal Resistance | | | | | | | |
| Junction to Case | | | 4 | | °C/W | | |
| Junction to Ambient | | | 50 | | °C/W | | |

Note 6: This specification applies over $-55^{\circ}\text{C} \le \text{T}_{\text{J}} \le +150^{\circ}\text{C}$ for the LM120 and $0^{\circ}\text{C} \le \text{T}_{\text{J}} \le +125^{\circ}\text{C}$ for the LM320. Note 7: Regulation is measured at constant junction temperature. Changes in output voltage due to heating effects must be taken into account separately. To ensure constant junction temperature, low duty cycle, pulse testing is used. The LM120/LM320 series does have low thermal feedback, improving line and load regulation. On all other tests, even though power dissipation is internally limited, electrical specifications apply only up to $P_{\rm D}$.

Note 8: Thermal resistance of typically 85°C/W (in 400 linear feet/min air flow), 224°C/W (in static air) junction to ambient, of typically 21°C/W junction to case.

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Note 9: Refer to RETS120H-12 drawing for LM120H-12 or RETS120-12K drawing for LM120K-12 military specifications.

Absolute Maximum Ratings

-15 Volt Regulators (Note 13)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Power Dissipation Internally Limited

Input Voltage

LM120/LM320 -40V
LM320T -35V
Input-Output Voltage Differential 30V
Junction Temperatures (Note 10)
Storage Temperature Range -65°C to +150°C

Lead Temperature

(Soldering, 10 sec.) 300°C

LM120K-15 and LM320K-15 Electrical Characteristics

| Metal C | | | Metal Ca | n Packa | ge | | | | |
|----------------------|---|--------|----------|---------|--------|-----------|-------|------|--|
| | Order Numbers | L | M120K- | 15 | L | LM320K-15 | | - | |
| | | (TO-3) | | | (TO-3) | | Units | | |
| - | gn Output Current (I _D) | | | | IA | | | Onno | |
| Dev | rice Dissipation (P _D) | | | | ow | | | | |
| Parameter | Conditions (Note 10) | Min | Тур | Max | Min | Тур | Max | | |
| Output Voltage | $T_{J} = 25^{\circ}C, V_{IN} = 20V,$ | -15.3 | -15 | -14.7 | -15.4 | -15 | -14.6 | V | |
| | I _{LOAD} = 5 mA | | | | | | | | |
| Line Regulation | $T_J = 25^{\circ}C$, $I_{LOAD} = 5$ mA, | | 5 | 10 | | 5 | 20 | mV | |
| | $V_{MIN} \le V_{IN} \le V_{MAX}$ | | | | | | | | |
| Input Voltage | | -35 | | -17 | -35 | | -17 | V | |
| Ripple Rejection | f = 120 Hz | 56 | 80 | | 56 | 80 | | dB | |
| Load Regulation, | $T_J = 25^{\circ}C, V_{IN} = 20V,$ | | 30 | 80 | | 30 | 80 | mV | |
| (Note 11) | $5 \text{ mA} \le I_{\text{LOAD}} \le I_{\text{D}}$ | | | | | | | | |
| Output Voltage, | $17.5V \le V_{IN} \le V_{MAX},$ | -15.5 | | -14.5 | -15.6 | | -14.4 | V | |
| (Note 10) | $5 \text{ mA} \le I_{LOAD} \le I_D, P \le P_D$ | | | | | | | | |
| Quiescent Current | $V_{MIN} \le V_{IN} \le V_{MAX}$ | | 2 | 4 | | 2 | 4 | mA | |
| Quiescent Current | T _J = 25°C | | | | | | | | |
| Change | $V_{MIN} \le V_{IN} \le V_{MAX}$ | | 0.1 | 0.4 | | 0.1 | 0.4 | mA | |
| | $5 \text{ mA} \le I_{\text{LOAD}} \le I_{\text{D}}$ | | 0.1 | 0.4 | | 0.1 | 0.4 | mA | |
| Output Noise Voltage | $T_A = 25^{\circ}C, C_L = 1 \mu F, I_L = 5 mA,$ | | 400 | | | 400 | | μV | |
| | $V_{IN} = 20V, 10 \text{ Hz} \le f \le 100 \text{ kHz}$ | | | | | | | | |
| Long Term Stability | | | 15 | 150 | | 15 | 150 | mV | |
| Thermal Resistance | | | | | | | | | |
| Junction to Case | | | | 3 | | | 3 | °C/W | |
| Junction to Ambient | | | | 35 | | | 35 | °C/W | |

LM120H-15 Electrical Characteristics

| Order Numbers Design Output Current (I _D) | | Me | | | |
|--|---|-------|------|-----------|-------|
| | | | 0.24 | 1 | Units |
| | Device Dissipation (P _D) | | 2W | | |
| Parameter | Conditions (Note 10) | Min | Тур | Max | |
| Output Voltage | $T_J = 25^{\circ}C, V_{IN} = 20V,$ | -15.3 | -15 | -14.7 | V |
| | $I_{LOAD} = 5 \text{ mA}$ | | | | |
| Line Regulation | $T_J = 25^{\circ}C$, $I_{LOAD} = 5$ mA, | | 5 | 10 | mV |
| | $V_{MIN} \le V_{IN} \le V_{MAX}$ | | | | |
| Input Voltage | | -35 | | -17 | V |
| Ripple Rejection | f = 120 Hz | 56 | 80 | | dB |
| Load Regulation, | $T_J = 25^{\circ}C, V_{IN} = 20V,$ | | 10 | 25 | mV |
| (Note 11) | $5 \text{ mA} \le I_{\text{LOAD}} \le I_{\text{D}}$ | | | | |
| Output Voltage, | $17.5V \le V_{IN} \le V_{MAX}$ | -15.5 | | -14.5 | V |
| (Note 10) | $5 \text{ mA} \le I_{LOAD} \le I_D, P \le P_D$ | | | | |
| Quiescent Current | $V_{MIN} \le V_{IN} \le V_{MAX}$ | | 2 | 4 | mA |
| Quiescent Current | T _J = 25°C | | | | |
| Change | $V_{MIN} \le V_{IN} \le V_{MAX}$ | | 0.05 | 0.4 | mA |
| | $5 \text{ mA} \leq I_{\text{LOAD}} \leq I_{\text{D}}$ | | 0.03 | 0.4 | mA |
| Output Noise Voltage | $T_A = 25^{\circ}C, C_L = 1 \mu F, I_L = 5 \text{ mA},$ | | 400 | | μV |
| | $V_{IN} = 20V$, 10 Hz $\leq f \leq$ 100 kHz | | | | |
| Long Term Stability | | | 15 | 150 | mV |
| Thermal Resistance | | | | | |
| Junction to Case | | | | (Note 12) | °C/W |
| Junction to Ambient | | | | (Note 12) | °C/W |

LM320T-15 Electrical Characteristics

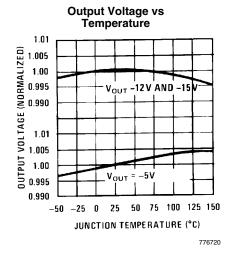
| | | Powe | Power Plastic Package | | | | |
|--|---|-------|-----------------------|-------|------|--|--|
| Order Numbers Design Output Current (I _D) | | | LM320T-15 | | | | |
| | | | (TO-220) | | | | |
| | | | 1A | | | | |
| | Device Dissipation (P _D) | | 15W | , | | | |
| Parameter | Conditions (Note 10) | Min | Тур | Max | | | |
| Output Voltage | $T_J = 25^{\circ}C, V_{IN} = 20V,$ | -15.5 | -15 | -14.5 | V | | |
| | $I_{LOAD} = 5 \text{ mA}$ | | | | | | |
| Line Regulation | $T_J = 25^{\circ}C$, $I_{LOAD} = 5$ mA, | | 5 | 20 | mV | | |
| | $V_{MIN} \le V_{IN} \le V_{MAX}$ | | | | | | |
| Input Voltage | | -35 | | -17.5 | V | | |
| Ripple Rejection | f = 120 Hz | 56 | 80 | | dB | | |
| Load Regulation, | $T_J = 25^{\circ}C, V_{IN} = 20V,$ | | 30 | 80 | mV | | |
| (Note 11) | $5 \text{ mA} \le I_{LOAD} \le I_{D}$ | | | | | | |
| Output Voltage, | $17.5V \le V_{IN} \le V_{MAX},$ | -15.7 | | -14.3 | V | | |
| (Note 10) | $5 \text{ mA} \le I_{LOAD} \le I_D, P \le P_D$ | | | | | | |
| Quiescent Current | $V_{MIN} \le V_{IN} \le V_{MAX}$ | | 2 | 4 | mA | | |
| Quiescent Current | $T_J = 25^{\circ}C$ | | | | | | |
| Change | $V_{MIN} \le V_{IN} \le V_{MAX}$ | | 0.1 | 0.4 | mA | | |
| | $5 \text{ mA} \le I_{LOAD} \le I_{D}$ | | 0.1 | 0.4 | mA | | |
| Output Noise Voltage | $T_A = 25^{\circ}C, C_L = 1 \mu F, I_L = 5 mA,$ | | 400 | | μV | | |
| | $V_{IN} = 20V, 10 \text{ Hz} \le f \le 100 \text{ kHz}$ | | | | | | |
| Long Term Stability | | | 30 | | mV | | |
| Thermal Resistance | | | | | | | |
| Junction to Case | | | 4 | | °C/W | | |
| Junction to Ambient | | | 50 | | °C/W | | |

Note 10: This specification applies over $-55^{\circ}\text{C} \le \text{T}_{\text{J}} \le +150^{\circ}\text{C}$ for the LM120 and $0^{\circ}\text{C} \le \text{T}_{\text{J}} \le +125^{\circ}\text{C}$ for the LM320. Note 11: Regulation is measured at constant junction temperature. Changes in output voltage due to heating effects must be taken into account separately. To ensure constant junction temperature, low duty cycle, pulse testing is used. The LM120/LM320 series does have low thermal feedback, improving line and load regulation. On all other tests, even though power dissipation is internally limited, electrical specifications apply only up to $P_{\rm D}$.

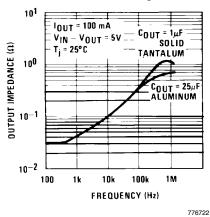
Note 12: Thermal resistance of typically 85°C/W (in 400 linear feet/min air flow), 224°C/W (in static air) junction to ambient, of typically 21°C/W junction to case.

Note 13: Refer to RETS120-15H drawing for LM120H-15 or RETS120-15K drawing for LM120K-15 military specifications.

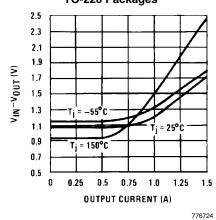
Typical Performance Characteristics



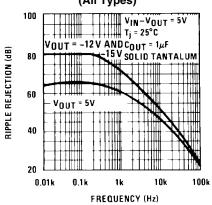
Output Impedance TO-3 and TO-220 Packages



Minimum Input-Output Differential TO-3 and TO-220 Packages

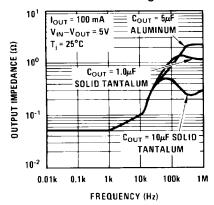


Ripple Rejection (All Types)



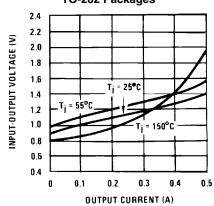
776721

Output Impedance TO-5 and TO-202 Packages



776723

Minimum Input-Output Differential TO-5 and TO-202 Packages

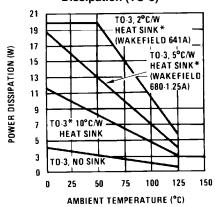


776725

Quiescent Current vs Input Voltage 1.3 LM120-5 1.25 QUIESCENT CURRENT (mA) 1.2 1.15 = 25°C 1.1 1.05 1.0 T, = 150°C 0.95 0.9 10 15 20 25 30 35 40 INPUT VOLTAGE (V)

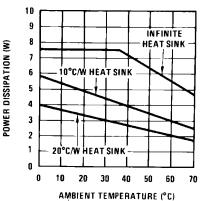
Maximum Average Power Dissipation (TO-3)

776726



*These curves for LM120. Derate 25°C further for LM320.

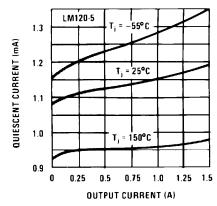
Maximum Average Power Dissipation (TO-202)



776730

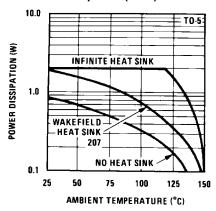
776728

Quiescent Current vs Load Current



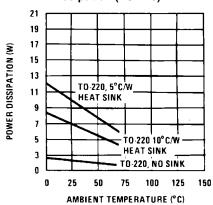
776727

Maximum Average Power Dissipation (TO-5)

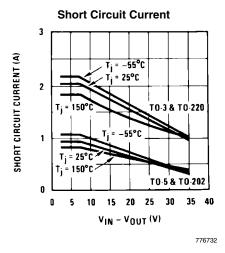


776729

Maximum Average Power Dissipation (TO-220)



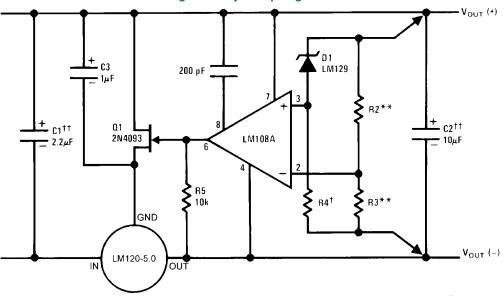
776731



776706

Typical Applications

High Stability 1 Amp Regulator



Lead and line regulation — 0.01% temperature stability — 0.2%

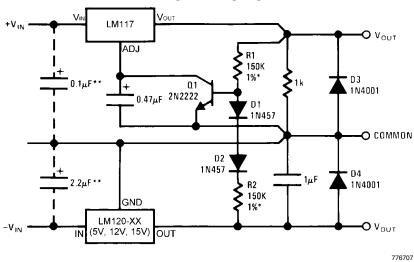
†Determines Zener current.

††Solid tantalum.

An LM120-12 or LM120-15 may be used to permit higher input voltages, but the regulated output voltage must be at least –15V when using the LM120-12 and –18V for the LM120-15.

**Select resistors to set output voltage. 2 ppm/°C tracking suggested.

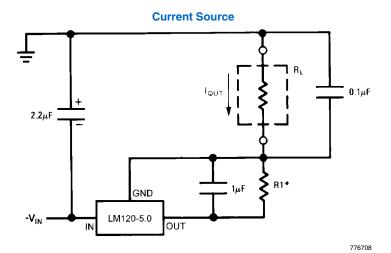
Wide Range Tracking Regulator



*Resistor tolerance of R1 and R2 determine matching of (+) and (-) inputs.

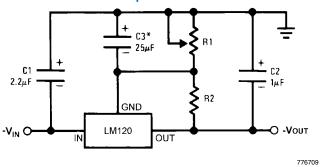
**Necessary only if raw supply capacitors are more than 3 from regulators

An LM3086N array may substitute for Q1, D1 and D2 for better stability and tracking. In the array diode transistors Q5 and Q4 (in parallel) make up D2; similarly, Q1 and Q2 become D1 and Q3 replaces the 2N2222.



 $^{\bullet}I_{OUT} = 1 \text{ mA} + \frac{5.0V}{R1}$

Variable Output Current Source

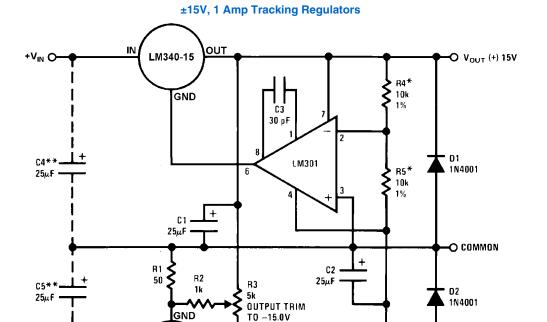


SELECT R2 AS FOLLOWS: LM120-5 300Ω

LM120-12 750Ω LM120-15 1k

$$V_{OUT} = V_{SET} \frac{R1 + R2}{R2}$$

 $^{\star}\text{C3}$ optional. Improves transient response and ripple rejection.



776712

◆ V_{OUT} (−) 15V

Performance (Typical)

LM320-15

OUT

IN

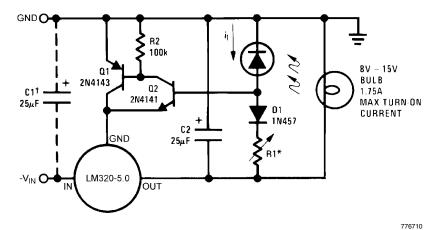
Load Regulation at $\Delta I_L = 1A$ 10 mV 1 mV Output Ripple, $C_{IN} = 3000~\mu F$, 100 $\mu V rms$ 100 $\mu V rms$ $I_L = 1A$

Temperature Stability +50 mV +50 mV Output Noise 10 Hz \leq f \leq 10 kHz 150 µVrms 150 µVrms

^{*}Resistor tolerance of R4 and R5 determine matching of (+) and (-) outputs.

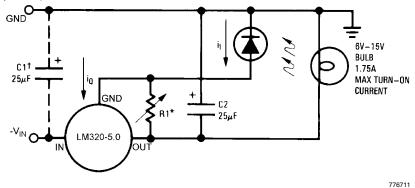
^{**}Necessary only if raw supply filter capacitors are more than 2 inches from regulators.

Light Controllers Using Silicon Photo Cells



*Lamp brightness increases until $i_1 = 5V/R1$ (i_1 can be set as low as 1 μ A).

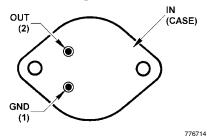
†Necessary only if raw supply filter capacitor is more than 2 inches from LM320MP.



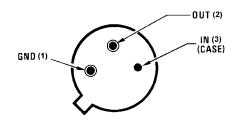
*Lamp brightness increases until $i_1 = i_Q (1 \text{ mA}) + 5V/R1$.

†Necessary only if raw supply filter capacitor is more than 2 inches from LM320.

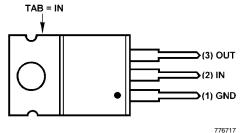
Connection Diagrams



Bottom View
Steel Metal Can Package TO-3 (K)
Order Number LM120K-5.0/883, LM120K-12/883,
LM120K-15/883, LM320K-5.0, LM320K-15
See NS Package Number K02A



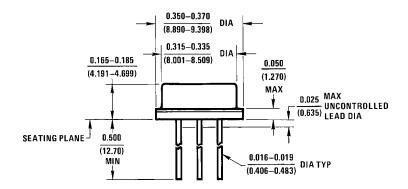
Bottom View
Metal Can Package TO-39 (H)
Order Number LM120H-5.0, LM120H-12, LM120H-15,
LM120H-5.0/883, LM120H-12/883, LM120H-15/883
See NS Package Number H03A

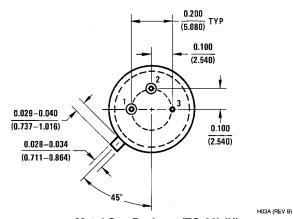


Front View
Power Package TO-220 (T)
Order Number LM320T-12 or LM320T-15
See NS Package Number T03B

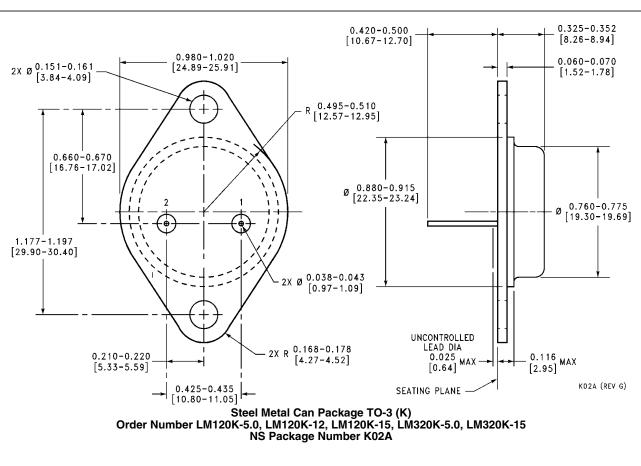
Schematic Diagrams -5V ₹87 1.4k ₹ R18 08 R19 5k ₩ D2 6.2V Q10 a9 ₹R5 ₹15k 15k **₹**R17 **011** Q12 **Q**17 0.2 **-**0 V_{оит} Q16 Q18 ₹R10 Q19 **√** D3 ₹ R11 10k 6.2V 01 013 **\$**R20 R12 R14 10 pF 20 pF **Q3** ₹ R4 20k 014 015 020 R21 150 R16 0.05 R8 20k R9 20k R13 5k ₹83 6k R1 750 776718 -12V and -15V ₹ R7 1.4k _09 ₹ R18 4k 08 Q10 **≸**R5 **§** R17 011 Q17 Q7 Q19 **Q**2 O V_{OUT} 020 Q16 ₹ R10 3k 021 **~** D3 6.2V Q1 012 ₹R20 20k 20 pF **Q**5 Q6 04 ₹R4 20k 014 Q13 020 R1 750 R21 R16 \$ R8 20k R9 20k R13 ₹R2 ₹83 6k 1k 776719

Physical Dimensions inches (millimeters) unless otherwise noted



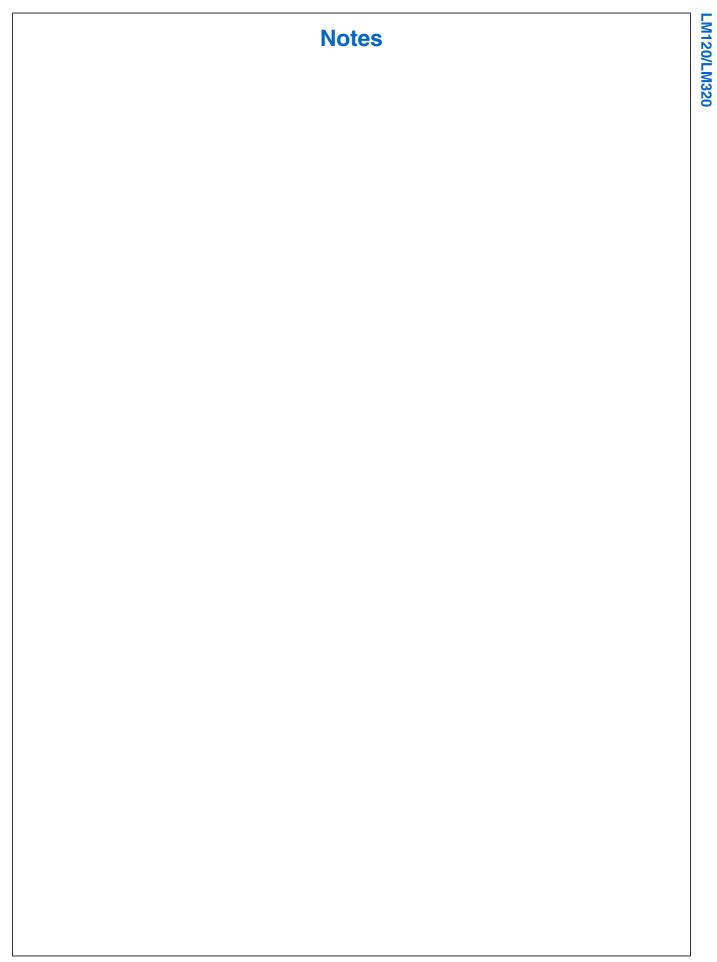


Metal Can Package (TO-39) (H)
Order Number LM120H-5.0, LM120H-12, LM120H-15
NS Package Number H03A



0.240-0.260 0.330-0.350 [8.38-8.89] [6.10-6.60] 0.100-0.120 0.149-0.153 [2.54 - 3.05][3.78 - 3.89]0.090-0.110 0.400 +0.015 [2.29-2.79] 0.190-0.210 [10.16 ^{+0.38}_{-0.13}] [4.83-5.33] 0.048-0.055 0.130-0.160 TYP [1.22-1.40] [3.30-4.06] PIN #1 ID -0.027-0.037 1.005-1.035 [0.69-0.94] [25.53-26.29] TYP $0.015^{+0.007}_{-0.001} [0.38^{+0.18}_{-0.03}]$ (0.525-0.555 [13.34-14.10]) 0.175-0.185 [4.45-4.70] 00-60 $0.105^{+0.010}_{-0.015}$ [2.67 $^{+0.25}_{-0.38}$] 0.048-0.052 [1.22-1.32] SEATING PLANE TAPERED SIDES 1º TO3B (REV L) Power Package TO-220 (T) Order Number LM320T-12 or LM320T-15 NS Package Number T03B

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Notes

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